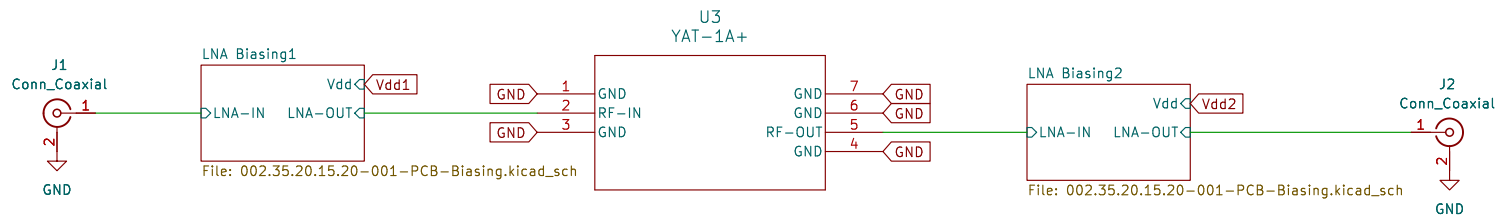


DC Voltage Regulators



RF Gain Circuit

Remy Nguyen

Sheet: /  
File: 002.35.20.15.20-001-PCB-LNA.kicad\_sch

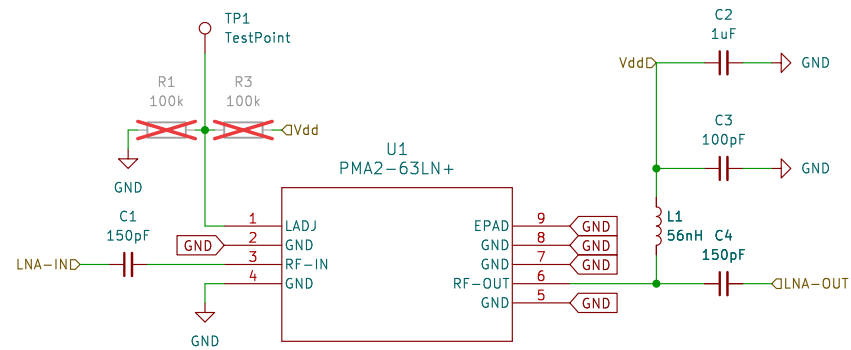
**Title:** RF-DFS Gain Circuit

Size: A4 Date: 7/6/2023

KiCad E.D.A. kicad 7.0.5

**Rev:** B

Id: 1/3



Remy Nguyen

Sheet: /LNA Biasing1/  
File: 002.35.20.15.20-001-PCB-Biasing.kicad\_sch

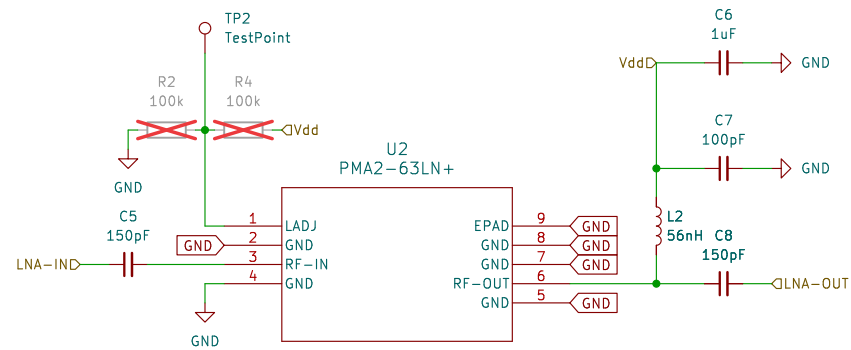
**Title:** LNA Biasing Circuit

Size: A4 Date: 7/6/2023

KiCad E.D.A. kicad 7.0.5

Rev: A

Id: 2/3



Remy Nguyen

Sheet: /LNA Biasing2/  
File: 002.35.20.15.20-001-PCB-Biasing.kicad\_sch

**Title:** LNA Biasing Circuit

Size: A4 Date: 7/6/2023

KiCad E.D.A. kicad 7.0.5

Rev: A

Id: 3/3