

High Stability Clock Oscillators Surface Mount Type **KC2520C-C2 Series**



CMOS/ 2.5V to 3.3V/ 2.5×2.0mm



Features

- Miniature ceramic package 2.5 (L) ×2.0 (W) ×0.7 (H) mm (Typ.)
- High Stability Output Frequency ±10×10⁻⁶ (-10 to +70°C) ±15×10⁻⁶ (-40 to +85°C)
- CMOS output
- Supply voltage Vcc = 2.5V/ 3.3V Compatible Low Power Supply Consumption
- Wide Operating Voltage Range 2.25 to 3.63V

Applications

• Wi-Fi, Bluetooth® etc.

Table 1

Freq. Tol.		Operating Temperature	Note		
Code	× 10 ⁻⁶	Range (°C)	Note		
Y	±10	-10 to +70	Please contact us for		
K	±20	-40 to +85	available frequencies		
L	±15	-40 10 +65	Standard specifications		

How to Order

 $\frac{\text{KC2520C}}{\textcircled{1}} \ \frac{40.0000}{\textcircled{2}} \ \frac{\texttt{C}}{\textcircled{3}} \ \frac{\texttt{2}}{\textcircled{4}} \ \frac{\texttt{E}}{\textcircled{5}} \ \frac{\texttt{00}}{\textcircled{7}}$

- 1) Series
- 2 Output Frequency
- ③ Output Type (CMOS)
- 4 Supply Voltage (2.5V, 3.3V Compatible)
- (5) Frequency Tolerance (See Table 1)
- 6 Symmetry/ INH Function (45/55%, Stand-by)
- ① Individual Specification (STD Specification is "00")

Packaging (Tape & Reel 2000 pcs./ reel)

Specifications

Item	Symbol	Conditions		Min.	Max.	Units	
	fo	Conditions		1.5	54	MHz	
Output Frequency Range	10			On Tames 40 to 10500	1.5 15	+15	IVITZ
Frequency Tolerance	f_tol	Initial tolerance, Operating temperature range, Rated power supply voltage change, Aging Op. Temp.: -40 to +85°C Op. Temp.: -40 to +85°C Op. Temp.: -40 to +85°C				×10 ⁻⁶	
				-20	+20		
		(1 year @25°C), Shock and vibration		Op. Temp.: -10 to +70°C	-10	+10	
Storage Temperature Range	T_stg				-55	+125	°C
Operating Temperature Range	T_use				-10	+70	°C
					-40	+85	
Max. Supply Voltage	_			-0.3	+4.0	V	
Supply Voltage	Vcc			+2.25	+3.63	V	
	Icc	CL = 15pF	1.5≤fo<24MHz		_	3.0	
		@2.5V	24≤fo≤40MHz		_	3.5	
O		@2.5V	0 <fo≤5< td=""><td>54MHz</td><td>_</td><td>4.5</td><td> ^</td></fo≤5<>	54MHz	_	4.5	^
Current Consumption		1.	1.5≤fo<24MHz		_	3.5	mA
		CL = 15pF @3.3V	24≤fo≤40MHz		_	5.0	
		@3.3V 4	0 <fo≤5< td=""><td>54MHz</td><td>_</td><td>6.0</td><td colspan="2">1</td></fo≤5<>	54MHz	_	6.0	1
Stand-by Current	I_std			_	5	μΑ	
Symmetry	SYM	@50% Vcc		45	55	%	
Rise/ Fall Time (10% Vcc to 90% Vcc Maximum Loaded)	tr/ tf			_	4	ns	
Low Level Output Voltage	Vol	IoL = 4mA		_	10% Vcc	V	
High Level Output Voltage	Vон	$I_{OH} = -4mA$		90% Vcc	_	V	
CMOS Load	L_CMOS	CMOS Output		_	15	pF	
Input Voltage Range	VIN				0	Vcc	V
Low Level Input Voltage	VIL				_	30% Vcc	V
High Level Input Voltage	ViH				70% Vcc	_	V
Disable Time	t_dis				_	100	ns
Enable Time	t_ena			_	5	ms	
Start-up Time	t_str	@Minimum operating voltage to be 0 sec.		_	10	ms	
1 Sigma Jitter	J Sigma	Measured with Wavecrest SIA-3000		_	8	ps	
Peak to Peak Jitter	Јрк-рк			_	80	ps	

Note: All electrical characteristics are defined at the maximum load and operating temperature range.

Please contact us for inquiry about operating temperature range, available frequencies and other conditions

Plating: Ni+Au Tolerance: ±0.1 Pad Connections #1 | NiH Function | Pad1 | Pad3 (Output) | Pad3 (Output) | Pad4 |

Recommended Land Pattern (Unit: mm)

