

Chaoyi Huang

SHANGHAI JIAO TONG UNIVERSITY

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EDUCATION

Shanghai Jiao Tong University

Shanghai, China

B.S. in Computer Science, ACM Honor Class

Aug. 2023 - June. 2027(expected)

Selected courses: Principle and Practice of Computer Algorithms(97/100), Large Language Models(94/100), Introduction to Computer Systems(98/100).

EXPERIENCE

Machine Vision and Intelligence Group, RHOS Lab, SJTU

Shanghai, China

Research Assistant, advised by Prof. Yonglu Li and Prof. Cewu Lu

June. 2024- Present

Teaching Assistant

Principle and Practice of Computer Algorithms(CS1952), 2025 summer

Teaching Assistant

Great Ideas in Computer Science(CS1950), 2024 fall

PROJECTS

RAY TRACER — Rust

[Link to Github](#)

A path tracing based ray tracing renderer written in Rust.

RISC-V CPU — Verilog

[Link to Github](#)

A Tomasulo RISC-V CPU with iCache and branch predictor. The architecture design can be successfully implemented on FPGA board.

COMPILER for MxLang — Java

[Link to Github](#)

A ANTLR-4 based compiler that translates Mx language (which is a C++ and Java like language) into RV32I Assembly. Designed based on LLVM IR and many optimizations are added for better performance.

RUST KERNEL — Rust

[Link to Github](#)

A toy operating system kernel implemented in Rust with privileged mode according to RISC-V specification, process scheduler, etc.

PUBLICATIONS

AWARDS AND HONORS

Zhiyuan Honors Scholarship

Zhiyuan College of SJTU, 2023, 2024, 2025

Awarded to top 5% students in SJTU annually.

SKILLS

Programming C++, Java, Rust, Python, Verilog

Tools SolidWorks, ROS

Languages Chinese(Native speaker), English(CET6:586/750)