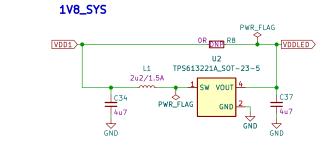
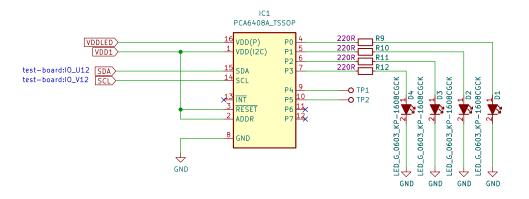
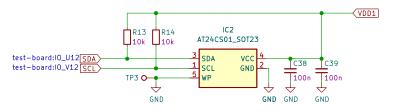
1 2	3	4	5	6 7	8
LPDDR4 testbed					
A					
В					
c					
	Sheet606DB5E5	SODIMM	LPDDR4		
	File: EEPROM.kicad_sch	File: sodim.kicad_sch	File: lpddr4.kicad_sch		
Ε					
source source hardware				Shoot: /	
open source				Sheet: / File: lpddr4-testbed.kicad_sch	
hardware				Title: LPDDR4 testbed	
				Size: A3 Date: 2023-08-09 KiCad E.D.A. eeschema 6.0.11+dfsg-1-bpo11+1	Rev: 1.2.1 Id: 1/4



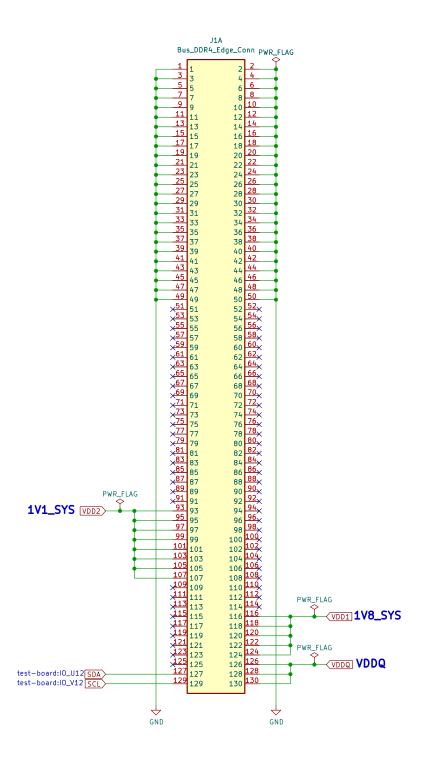


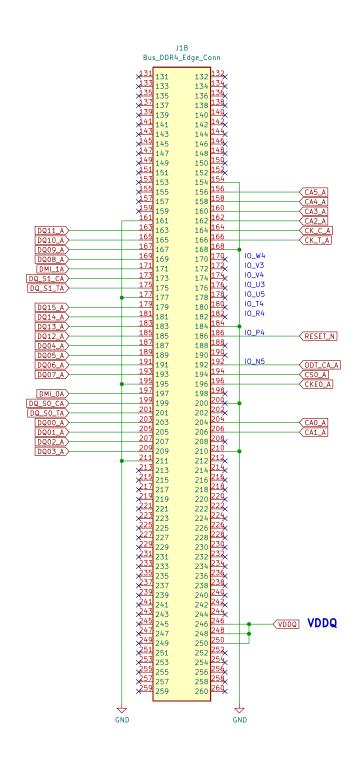




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Size: A3	Date: 2023-08-09	Rev: 1.2.1
KiCad E.D.A.	eeschema 6.0.11+dfsq-1~bpo11+1	ld: 2/4

SODIMM Connector



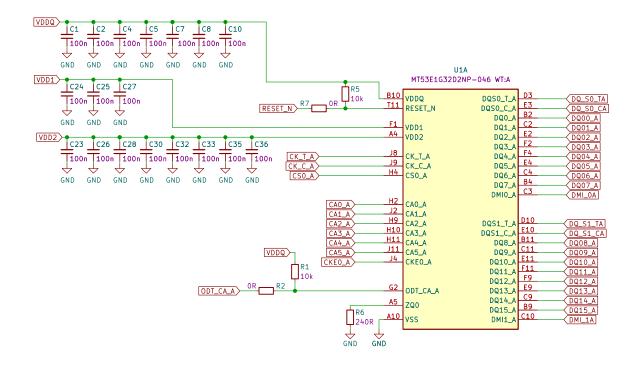


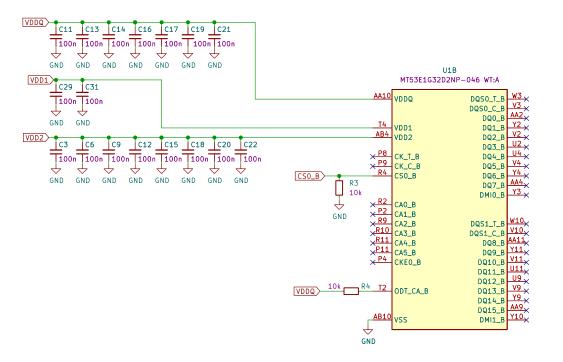




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KiCad E.D.A.	eeschema 6.0.11+dfsg-1~bpo11+1	ld: 3/4
	7	8

LPDDR4









 Sheet: /LPDDR4/

 File: lpddr4.kicad_sch

 Title: LPDDR4 testbed

 Size: A3
 Date: 2023-08-09
 Rev: 1.2.1

 KiCad E.D.A. eeschema 6.0.11+dfsg-1-bpo11+1
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