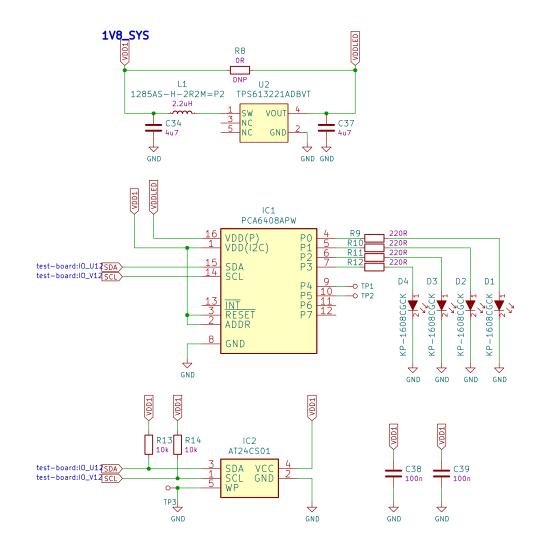
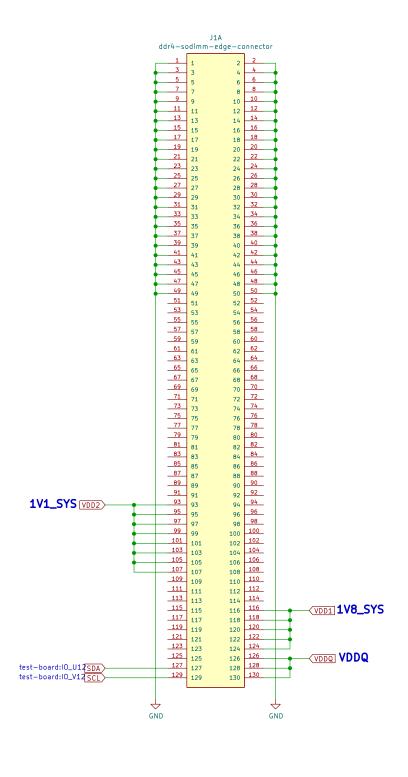
1	2	3	4 5	6	7	8
LPDDR4 testbed						
A						
_						
В						
c						
		Sheet606DB5E5	SODIMM	LPDDR4		
				File: lpddr4.kicad_sch		
		File: EEPROM.kicad_sch	File: sodim.kicad_sch	rite: tpddr4.kicad_scn		
D						
E						
1 5						
open source hardware	ı N2					
5}{} [%]ant	Logo N2 Logo N1 Anthricro_logo				Sheet: /	
open source	Logo N1 antmicro_logo				Sheet: / File: lpddr4-testbed.kicad_sch	
hardwaro	· · · · · · · · · · · · · · · · · · ·					
riai uwai e	·				Title: LPDDR4 testbed Size: A3 Date: 2022-12-30 KiCad E.D.A. eeschema 6.0.5+dfsg-1-bpo11+1	Rev: 1.2.0

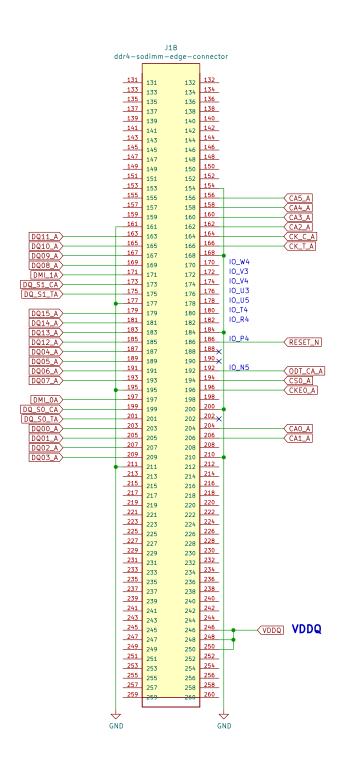






 SODIMM Connector



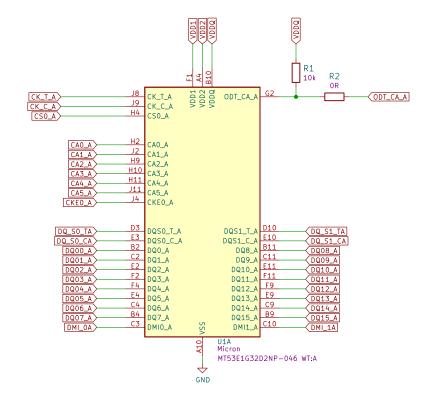


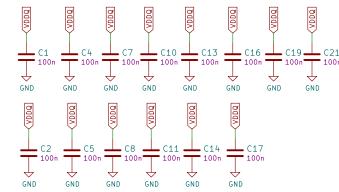


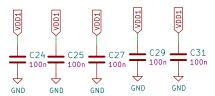


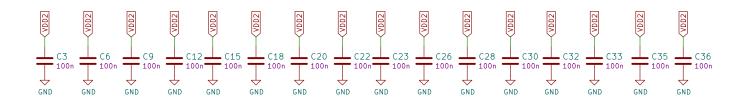
Sheet: /SODI	MM /	
File: sodim.k		
Title:		
Size: A3	Date: 2022-12-30	Rev: 1.2.0
KiCad E.D.A.	eeschema 6.0.5+dfsg-1~bpo11+1	ld: 3/4

LPDDR4













CSO_B R3 10k GND	X P8	R4 R5 10k R7 OR RESET_N
	XR11 CA3_B XR11 CA4_B YP11 CA4_B CA5_B CKEO_B XW3 XV3 DQSO_T_B DQS1_T_B DQS1_T_B DQS1_C_B V10 XV10 XV2 DQ0_B DQS0_CB DQS1_CB V11 XV1 Y11 DQ2_B DQ11_B V11 XV2 V2 DQ2_B DQ11_B U11 XV4 V4 V4 V4 V4 V4 V4 V4 X4 V4 X4	w T :A
	↓ GND	