

28Pins
Variant: 5V/3V3

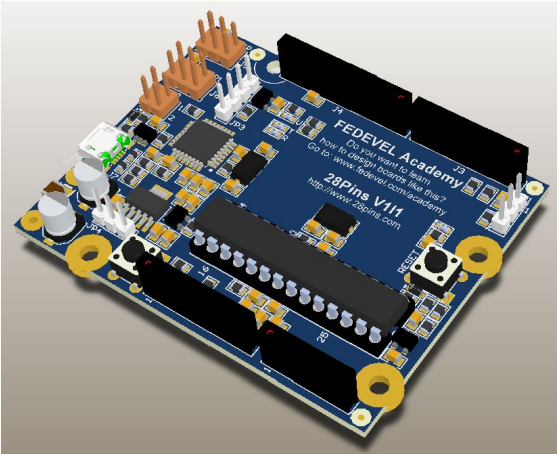
30-07-2021
V1I1

RELEASED 30-JULY-2021

Table with 8 columns: Page, Index, Page, Index, Page, Index, Page, Index. It lists the document structure including Cover Page, Block Diagram, 28Pins Schematic, Revision History, and various schematic and layout pages.

DESIGN CONSIDERATIONS

Four colored boxes (grey, orange, yellow, blue) containing design and layout notes. The orange box is empty, while the others contain example text for informational, critical design, and layout guidelines.

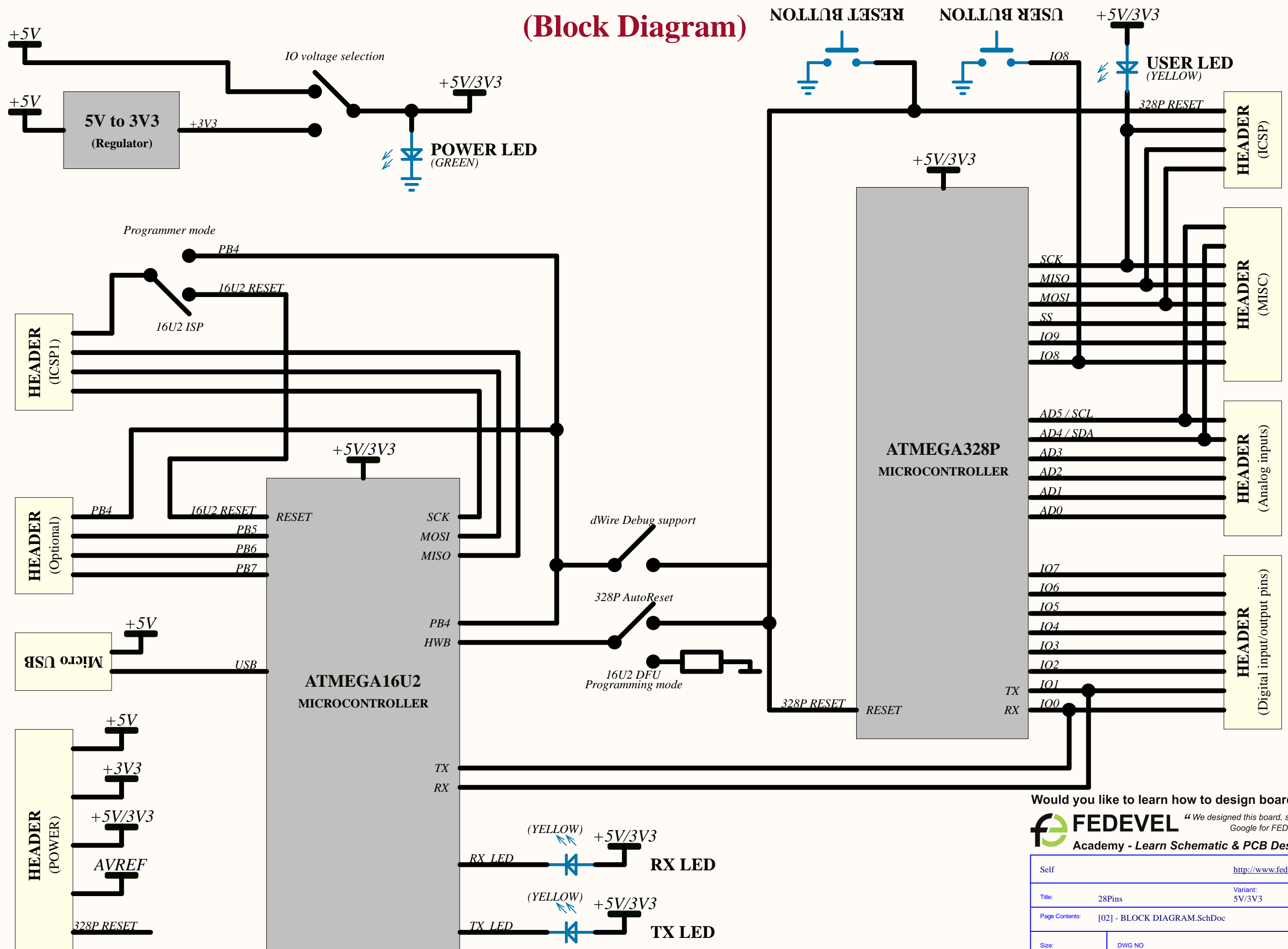


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FEDEVEL Academy logo and tagline: "We designed this board, so you can learn, Google for FEDEVEL Academy". Below it, the text "Academy - Learn Schematic & PCB Design ONLINE".

Metadata table with fields: Self, Title (28Pins), Variant (5V/3V3), Page Contents ([01] - COVER PAGE.SchDoc), Checked by, Size, DWG NO, Date (30-07-2021), Designed by (www.fedevel.com), Sheet (1 of 5), and Revision (V1I1).

28Pins (Block Diagram)

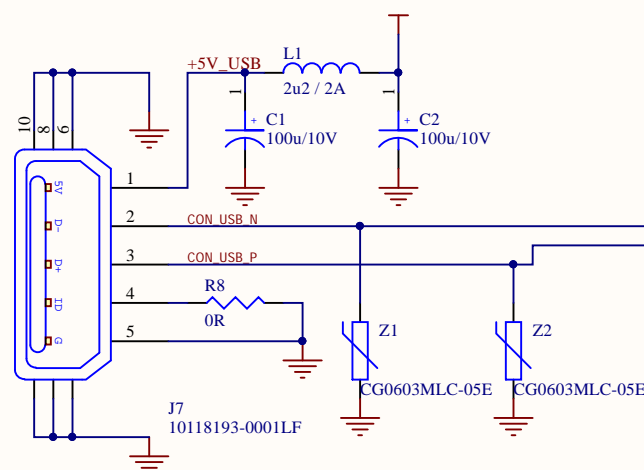


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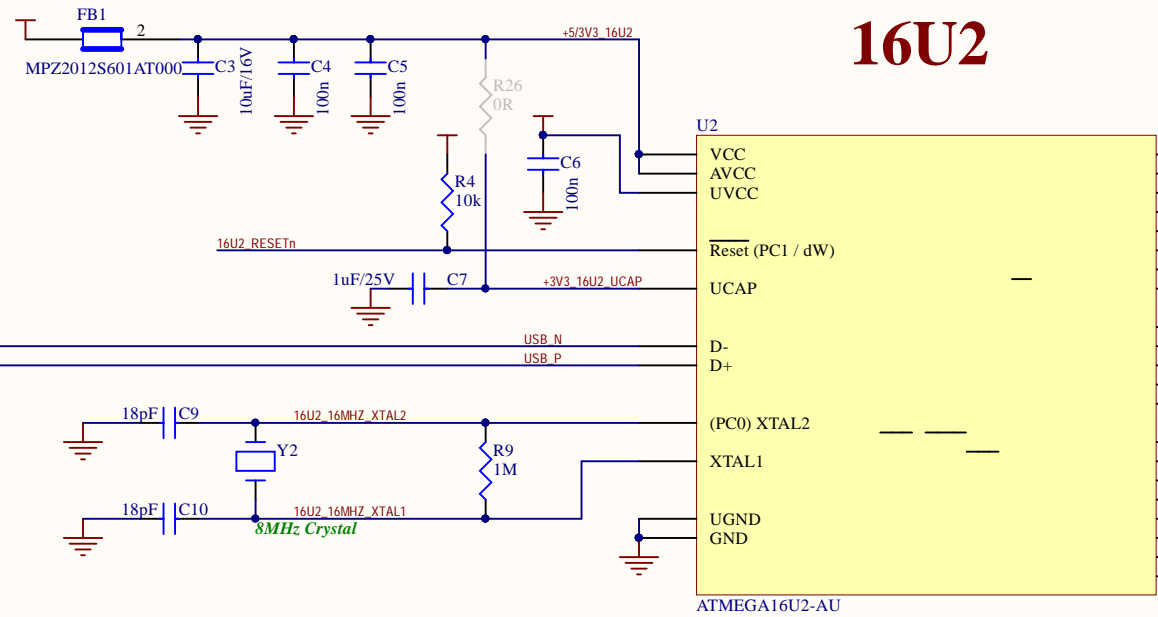


Self		http://www.fedevel.com/academy	
Title:	28Pins	Variant:	5V/3V3
Page Contents: [02] - BLOCK DIAGRAM.SchDoc			
Size:	DWG NO		Revision: V111
Date:	30-07-2021	Designed by www.fedevel.com	Sheet 2 Checked by

Micro USB



16U2



JP2

ICSP1

JP3

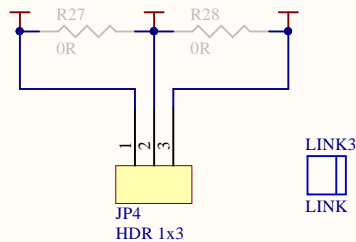
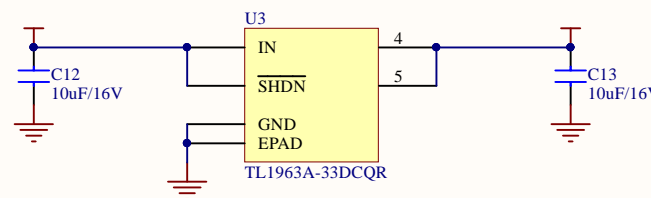
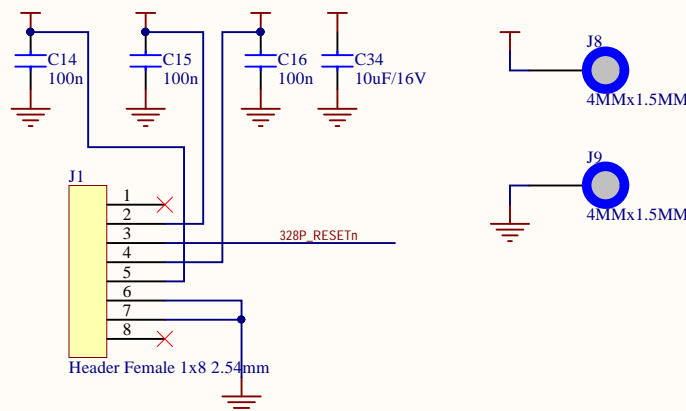
JP1

POWER

POWER PADS

3V3 LDO

Power Selection



ICSP

IOH

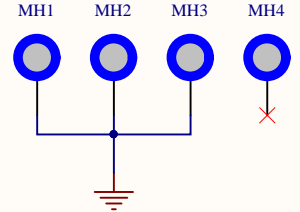
AD

IOL

DIP SOCKET

SCKT_FOR_U1
28-DIP SOCKET
28-DIP Socket

MOUNTING HOLES

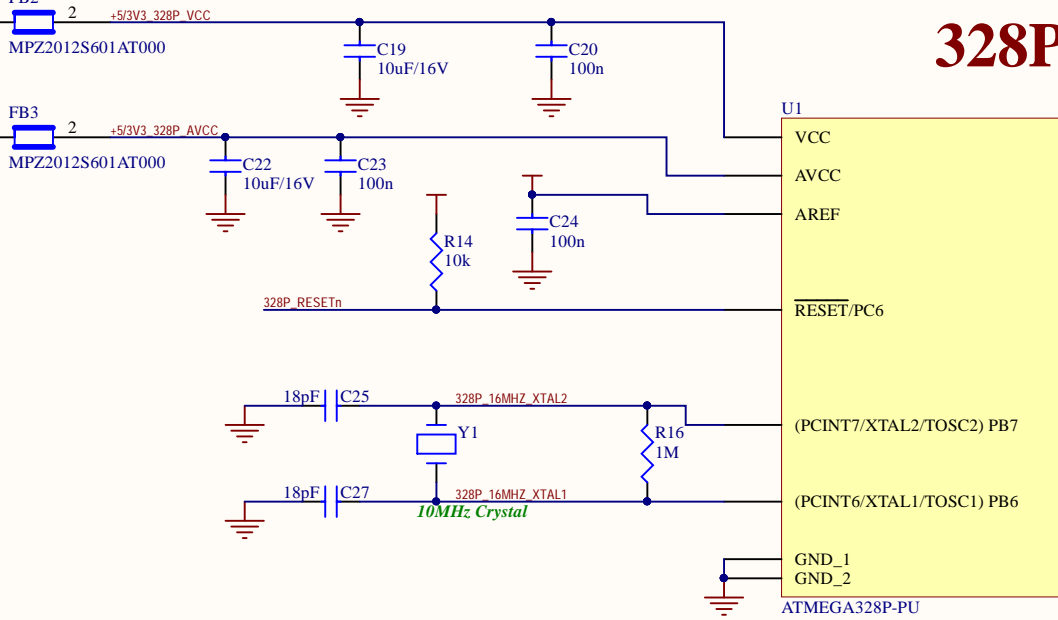
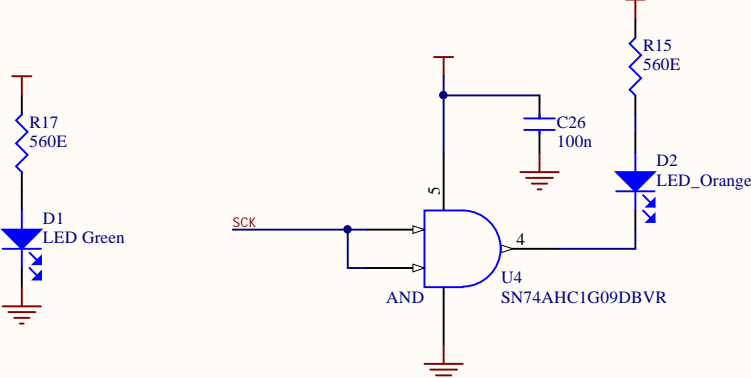


FIDUCIALS

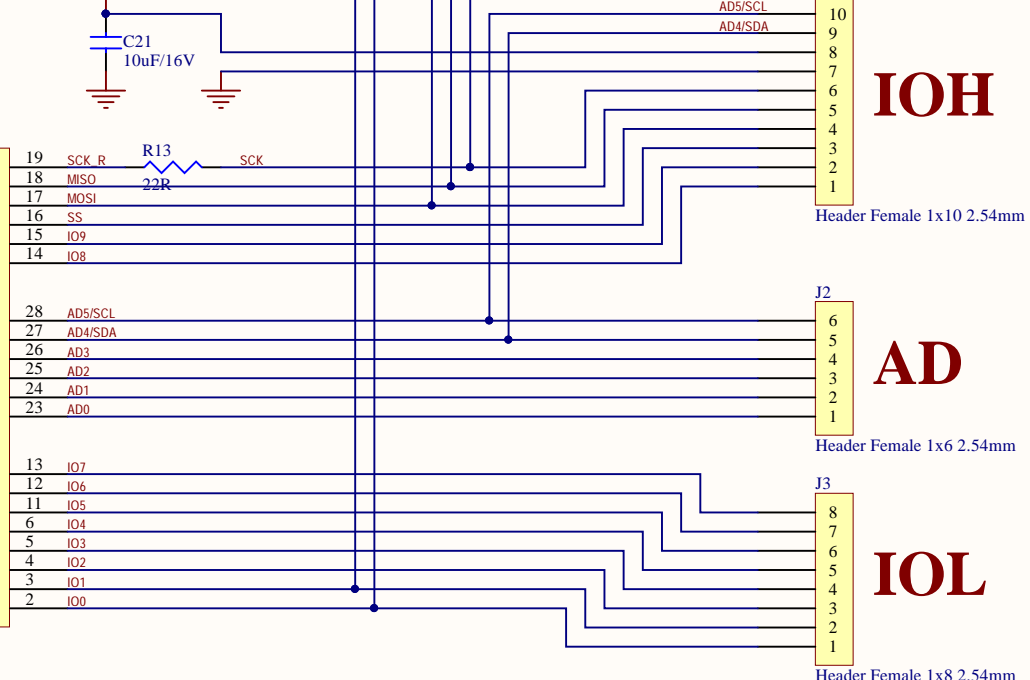
FID1 FID2 FID3 FID4

POWER LED

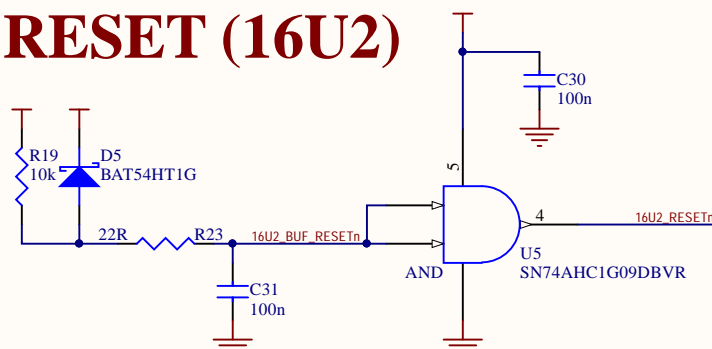
USER LED



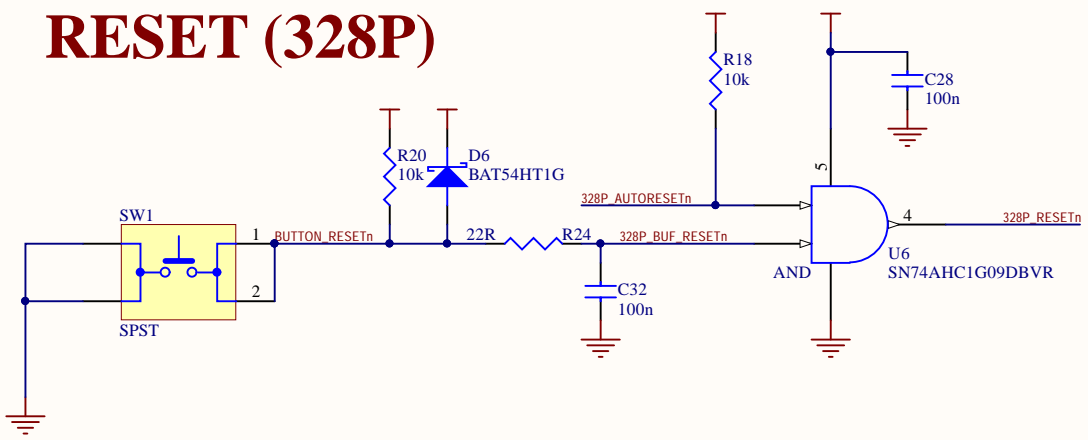
328P



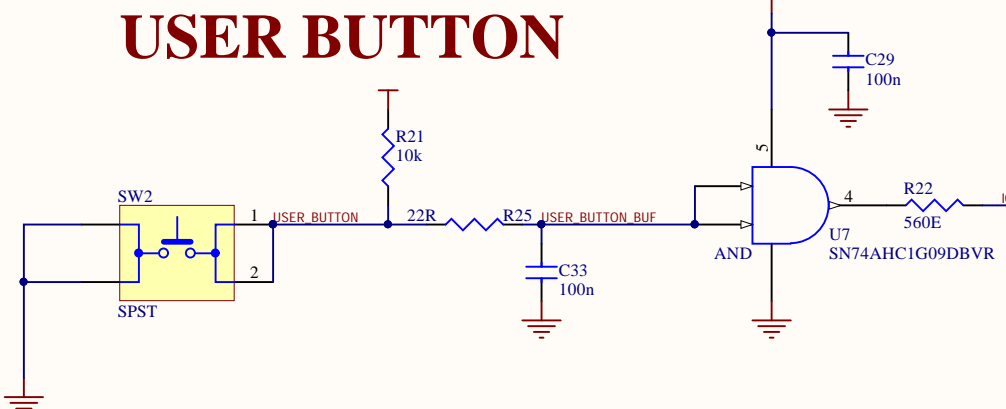
RESET (16U2)



RESET (328P)



USER BUTTON



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Size:	DWG NO	Revision:	V111
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Sheet	3	of	5

REVISION HISTORY

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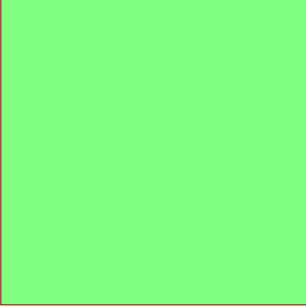
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Page Contents:	[04] - REVISION HISTORY.SchDoc		Checked by
Size:	DWG NO		Revision: VIII
Date:	30-07-2021	Sheet	4 of 5

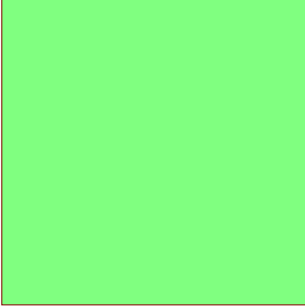
Designator
[01] - COVER PAGE.SchDoc



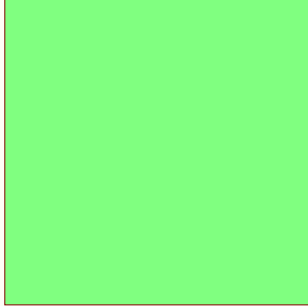
Designator
[02] - BLOCK DIAGRAM.SchDoc



Designator
[03] - 28PINS SCHEMATIC.SchDoc



Designator
[04] - REVISION HISTORY.SchDoc



NOTES

Mark Not Fitted Components as
NF

- DRAFT** - Very early stage of schematic, ignore details.
- PRELIMINARY** - Close to final schematic.
- CHECKED** - There should not be any mistakes. Tell the engineer if you find one.
- RELEASED** - A board with this schematic has been sent to production.

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Size:	DWG NO		Revision: VIII1
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