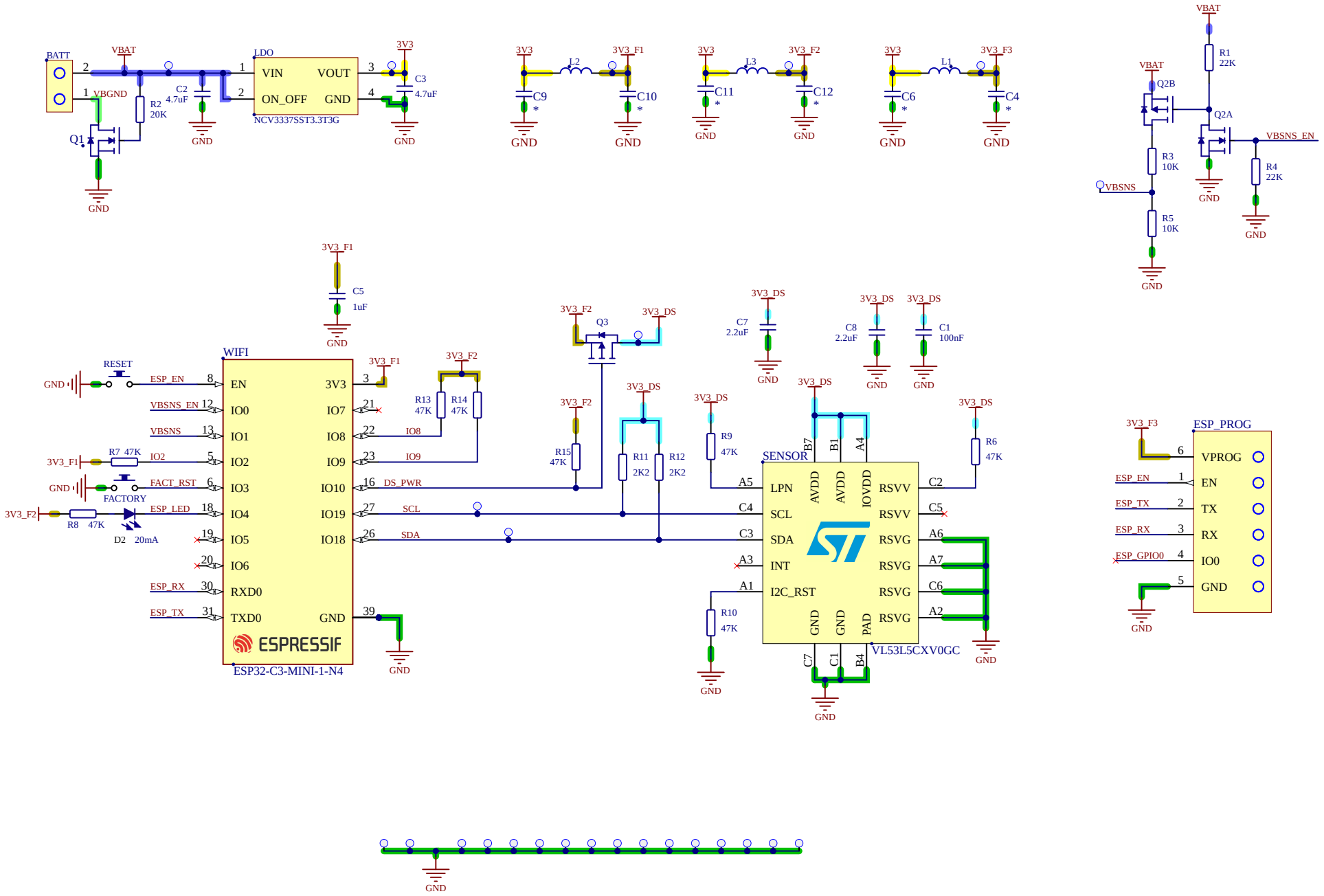
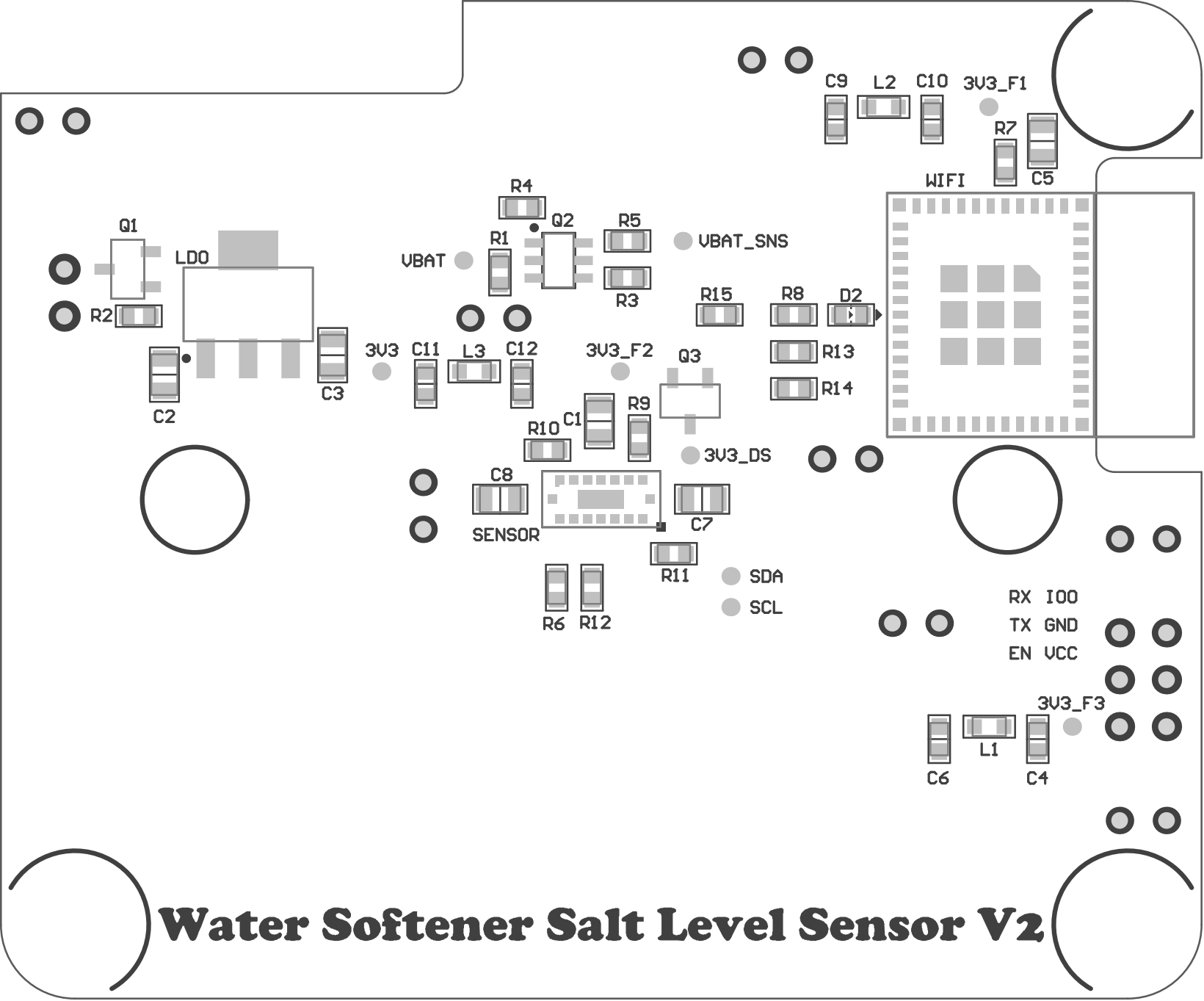
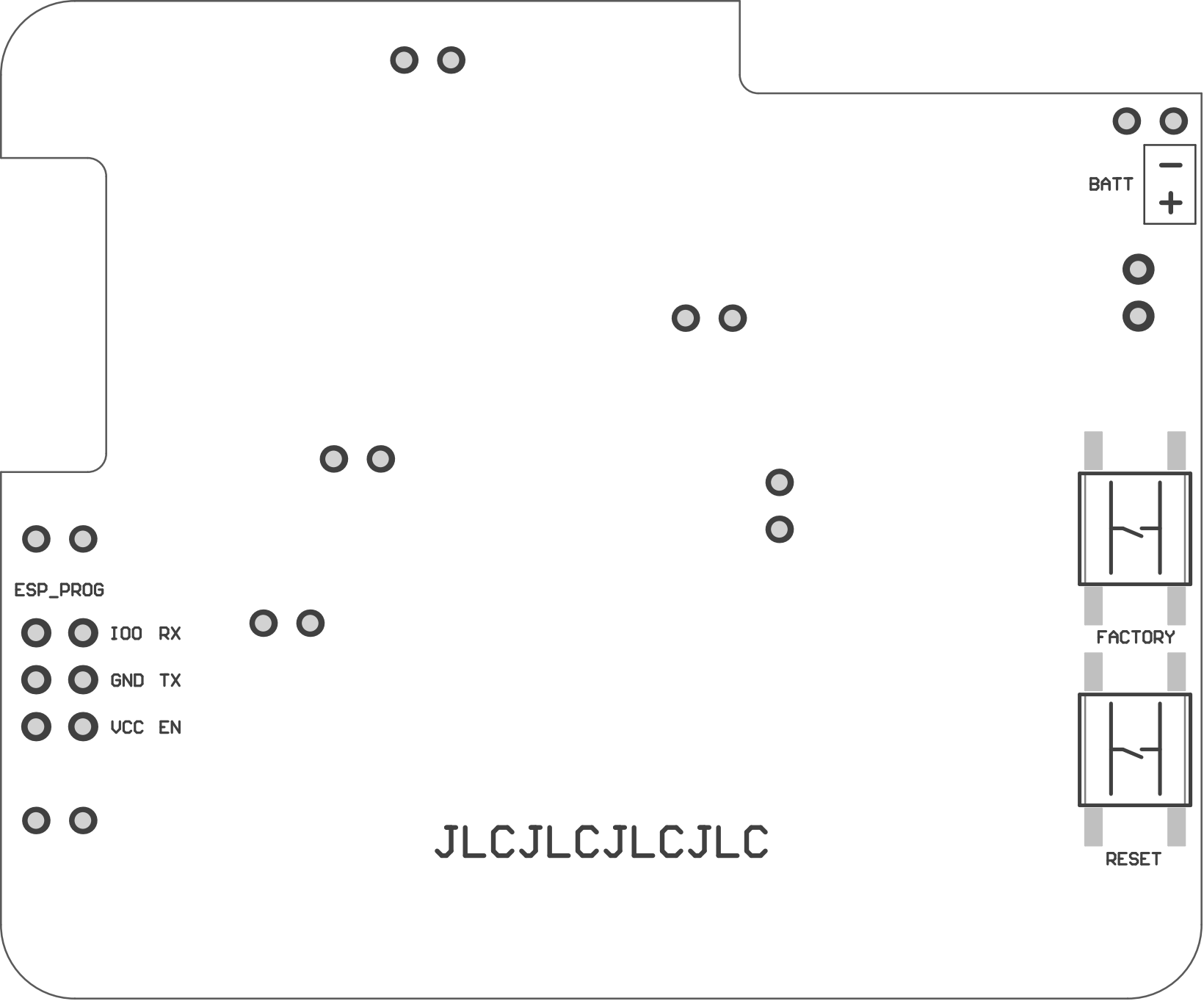


Distance Sensor 2	Revision 1 Saved on 12/10/2023
chs	Sheet 1 of 1


C9
L2
C10
3V3_F1
R7
C5
WIFI
R4
Q2
R5
R1
VBAT
VBAT_SNS
R3
Q1
LDO
R2
R15
R8
D2
R13
R14
3V3
C11
L3
C12
3V3_F2
Q3
C3
C2
R9
C1
R10
3V3_DS
C8
C7
SENSOR
R11
SDA
SCL
R6
R12
RX IO0
TX GND
EN VCC
3V3_F3
L1
C6
C4
Water Softener Salt Level Sensor V2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GND TX
VCC EN
FACTORY
RESET
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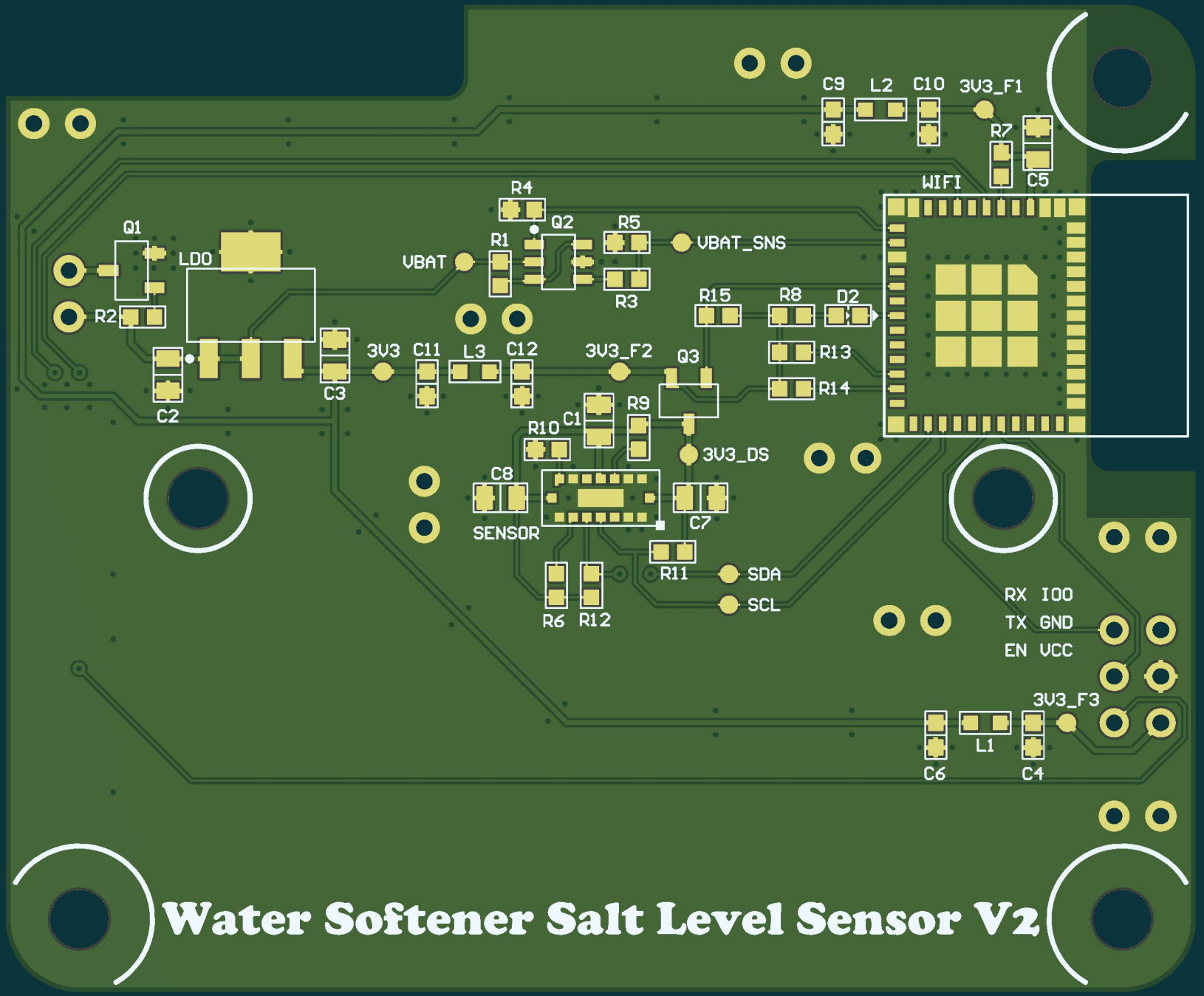
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C10
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R7
C5
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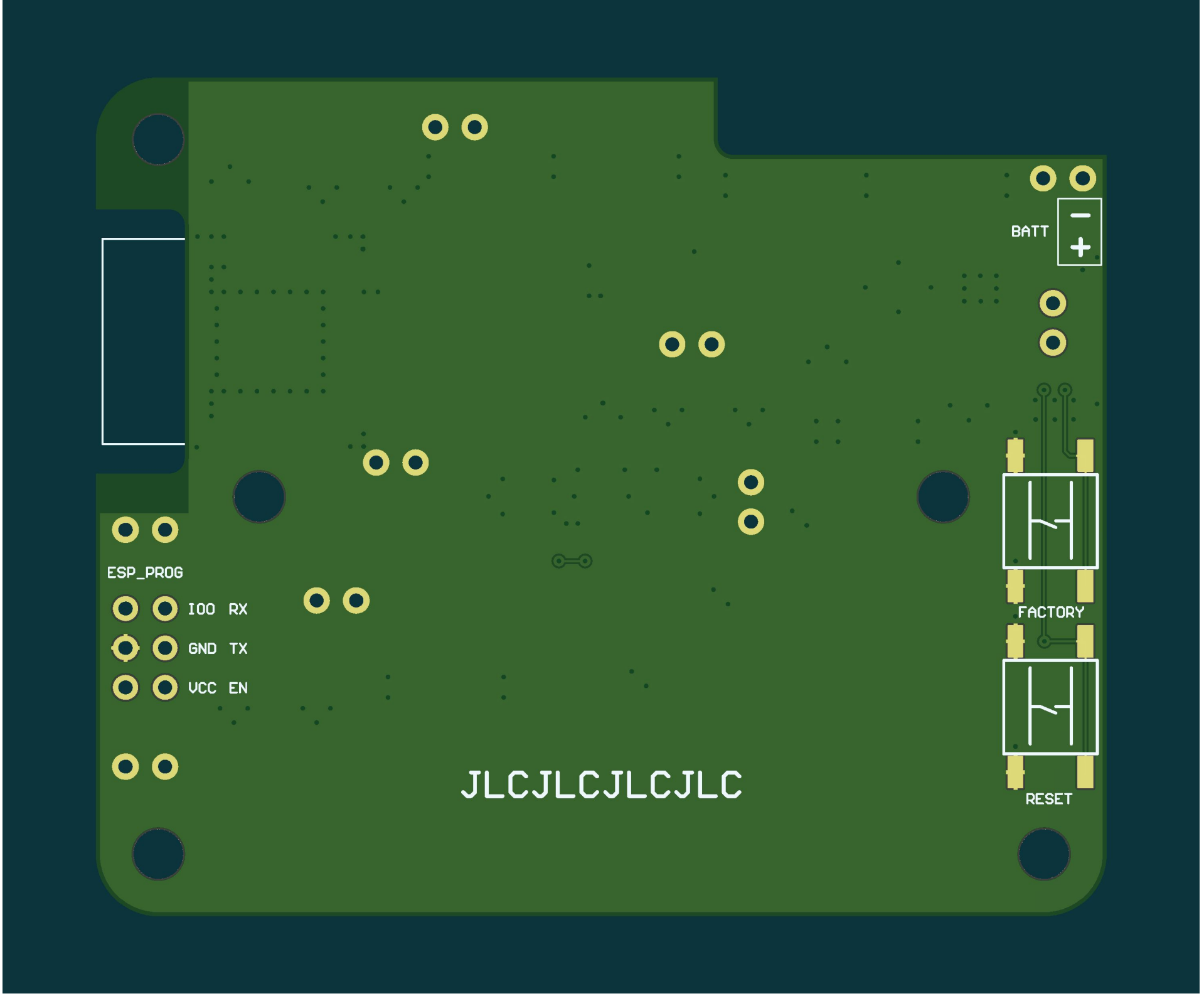
R4
Q1
Q2
R5
VBAT_SNS
LDO
VBAT
R1
R3
R15
R8
D2
R2
3V3
C11
L3
C12
3V3_F2
Q3
R13
R14
C3
C2
C1
R9
R10
3V3_DS
C8
SENSOR
C7
R11
SDA
SCL
R6
R12
RX IO0
TX GND
EN VCC
3V3_F3
L1
C6
C4
Water Softener Salt Level Sensor V2


BATT
ESP_PROG
IO0 RX
GND TX
VCC EN
FACTORY
RESET
JLCJLCJLCJLC

Design Rules Verification Report

Filename : U:\dev\distance_sensor\pcb\distance_sensor_v2\distance_sensor_v2\distance_sensor_v2.PcbDoc

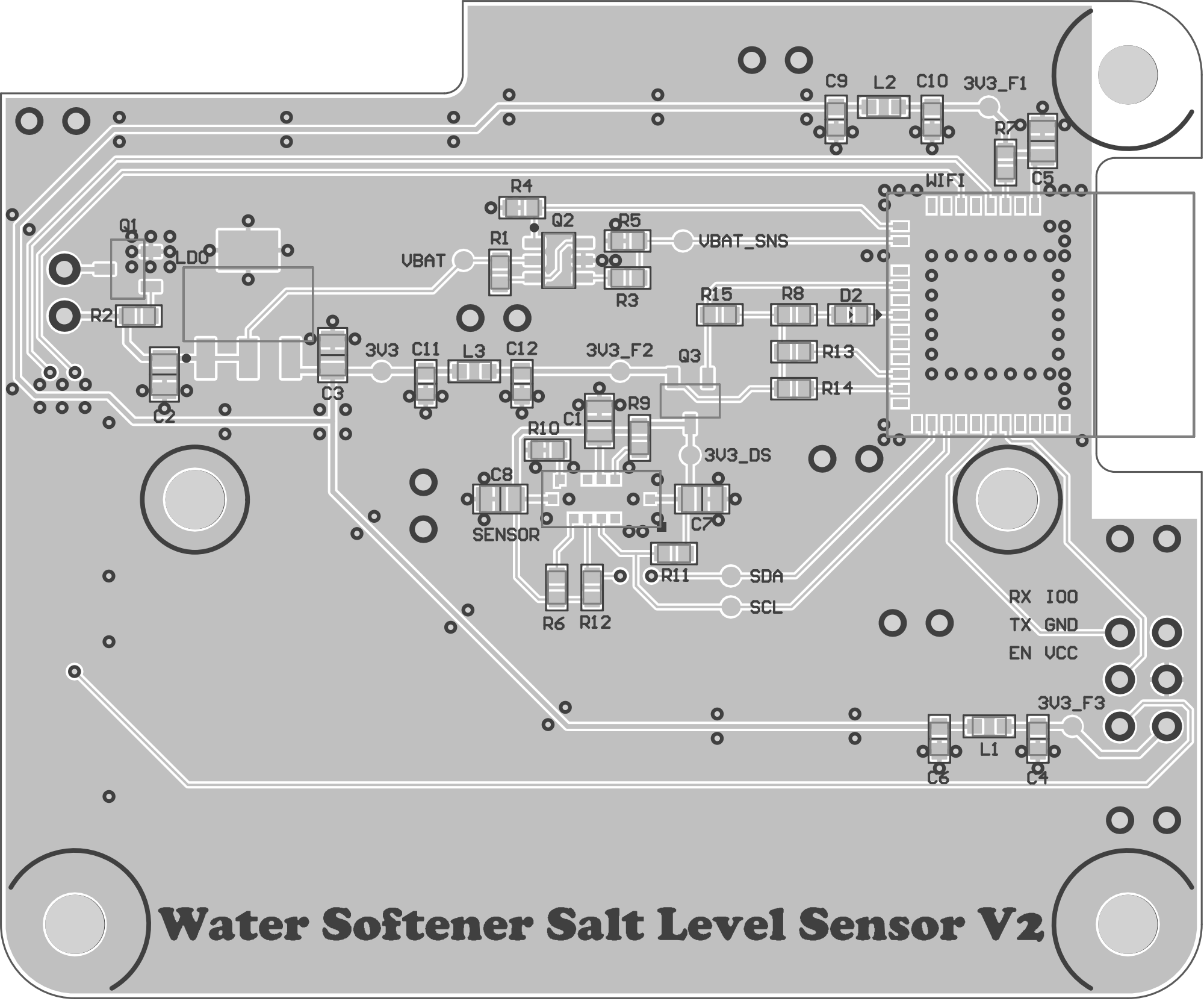
Warnings 0
Rule Violations 0

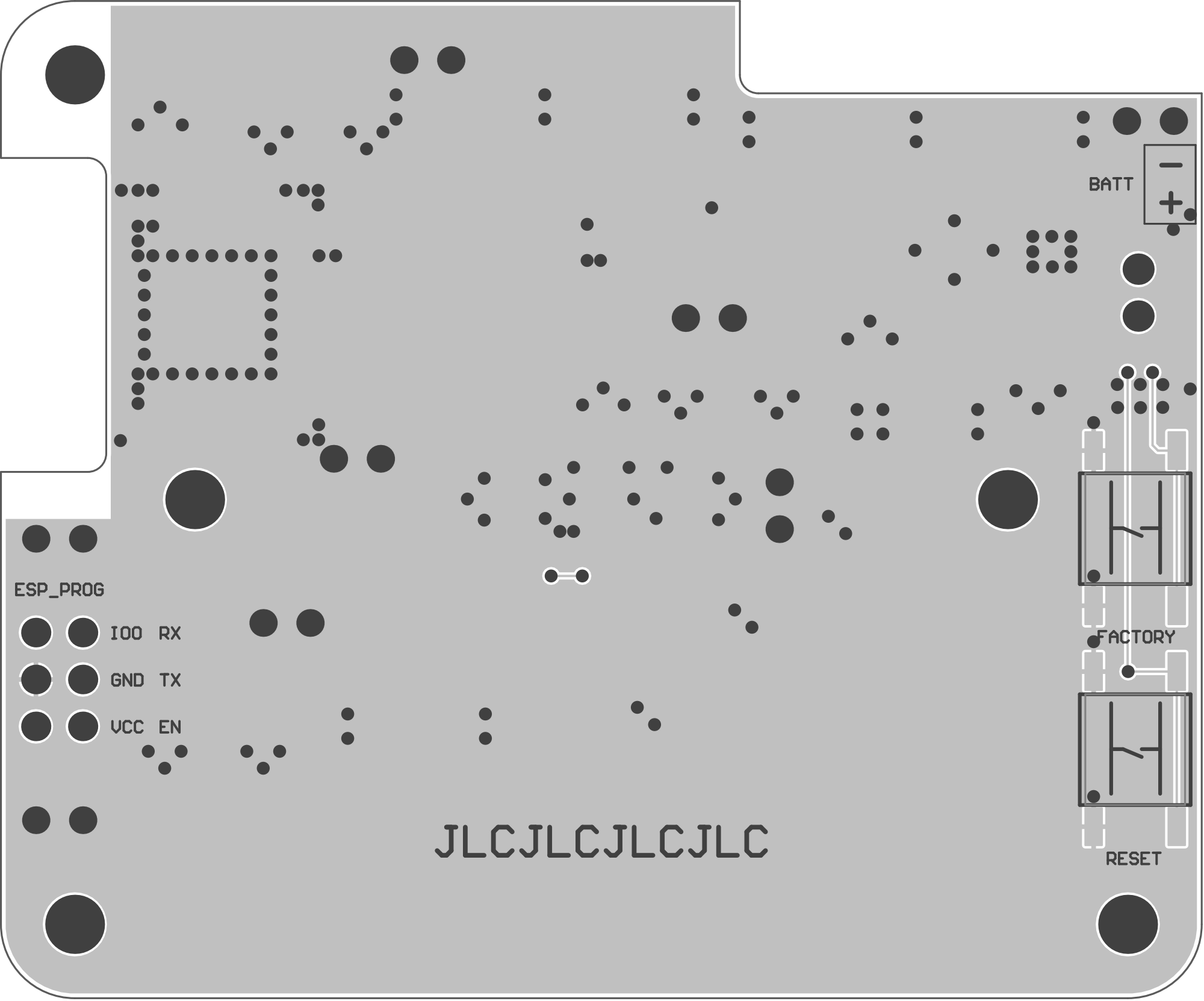
Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.127mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.127mm) (Max=1.27mm) (Preferred=0.127mm) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Hole Size Constraint (Min=0.3mm) (Max=5mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.08mm) (All),(All)	0
Silk To Solder Mask (Clearance=0.254mm) (False),(All)	0
Silk to Silk (Clearance=0mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Board Clearance Constraint (Gap=0mm) (OnSignal)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Prefered=12.7mm) (All)	0
Total	0

Electrical Rules Check Report

Class	Document	Message
		Successful Compile for


C9
L2
C10
3V3_F1
R7
C5
WIFI
R4
Q1
LDO
R2
Q2
R5
R1
VBAT
VBAT_SNS
R3
R15
R8
D2
R13
R14
3V3
C11
L3
C12
3V3_F2
Q3
C3
C2
C1
R9
R10
3V3_DS
C8
C7
SENSOR
R11
SDA
SCL
R6
R12
RX IO0
TX GND
EN VCC
3V3_F3
L1
C6
C4
Water Softener Salt Level Sensor V2


BATT
ESP_PROG
IO0 RX
GND TX
VCC EN
FACTORY
RESET
JLCJLCJLCJLC