s_ICE1PCS01 (subcircuit)

Attributes

```
inputs: VSENSE ISENSE
outputs: gate
e_left_nodes:
e_right_nodes:
e_top_nodes:
e_bottom_nodes:
b_left_nodes:
b_right_nodes:
b_top_nodes:
b_bottom_nodes:
parameters:
  C_icomp: 1e-9
  C_parallel: 0.1e-6
  C_series: 1e-6
  K1: 7
  KFQ: 9.183
  R_series: 33e3
  a1_filter: computed
  b1_filter: computed
  b2_filter: computed
  c1: computed
  delt_min: 0.01u
  delt_nrml: 10u
  dt_clock: computed
  fc: 62.5e3
  gOTA1: 42e-6
  gOTA1start: 15.12e-6
  gOTA2: 1.1e-3
```

Description

 $s_ICE1PCS01$ is an equivalent circuit model for ICE1PCS01, the continuous conduction mode power factor correction controller from Infineon. The circuit has been adapted from PLECS demo examples.