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This spec describes the thread scheduler of a per-CPU *HiRTOS* instance.

EXTENDS *FiniteSets*, *Sequences*, *Naturals*, *TLC*

CONSTANTS

Invalid_Thread_Priority

Num_Thread_Priorities $\triangleq 3$

Num_Interrupt_Priorities $\triangleq 2$

Valid_Thread_Priority_Type $\triangleq 0 \dots \text{Num_Thread_Priorities} - 1$

Thread_Priority_Type $\triangleq \text{Valid_Thread_Priority_Type} \cup \{\text{Invalid_Thread_Priority}\}$

Threads $\triangleq \{\text{"Idle_Thread"}, \text{"thread1"}, \text{"thread2"}, \text{"thread3"}\}$

Mutexes $\triangleq \{\text{"mutex1"}\}$

Condvars $\triangleq \{\text{"thread1_condvar"}, \text{"thread2_condvar"}, \text{"thread3_condvar"}, \text{"condvar1"}\}$

Timers $\triangleq \{\text{"thread1_timer"}, \text{"thread2_timer"}, \text{"thread3_timer"}\}$

Interrupts $\triangleq \{\text{"Timer_Interrupt"}\}$

Thread_Id_Type $\triangleq \text{Threads} \cup \{\text{"Invalid_Thread_Id"}\}$

Mutex_Id_Type $\triangleq \text{Mutexes} \cup \{\text{"Invalid_Mutex_Id"}\}$

Condvar_Id_Type $\triangleq \text{Condvars} \cup \{\text{"Invalid_Condvar_Id"}\}$

Timer_Id_Type $\triangleq \text{Timers} \cup \{\text{"Invalid_Timer_Id"}\}$

Interrupt_Id_Type $\triangleq \text{Interrupts} \cup \{\text{"Invalid_Interrupt_Id"}\}$

Thread_State_Type $\triangleq \{\text{"Suspended"}, \text{"Runnable"}, \text{"Running"}, \text{"Blocked_On_Condvar"}, \text{"Blocked_On_Mutex"}\}$

Timer_State_Type $\triangleq \{\text{"Timer_Stopped"}, \text{"Timer_Running"}\}$

No_Duplicates_List_Type(*S*) \triangleq

$\{x \in \text{Seq}(S) : \text{Len}(x) \neq 0 \Rightarrow \forall i, j \in 1 \dots \text{Len}(x) : i \neq j \Rightarrow x[i] \neq x[j]\}$

Range(*f*) $\triangleq \{f[x] : x \in \text{DOMAIN } f\}$

Thread_Queue_Type $\triangleq \text{No_Duplicates_List_Type}(\text{Thread_Id_Type})$

Thread_Priority_Queue_Type $\triangleq [\text{Valid_Thread_Priority_Type} \rightarrow \text{Thread_Queue_Type}]$

HiRTOS_Type $\triangleq [$

Current_Thread_Id : *Thread_Id_Type*,

Runnable_Threads_Queue : *Thread_Priority_Queue_Type*,

Interrupts_Enabled : BOOLEAN

$]$

$$\begin{aligned}
& \text{HiRTOS_Initializer} \triangleq [\\
& \quad \text{Current_Thread_Id} \mapsto \text{"Invalid_Thread_Id"}, \\
& \quad \text{Runnable_Threads_Queue} \mapsto \\
& \quad \quad [p \in \text{Valid_Thread_Priority_Type} \mapsto \\
& \quad \quad \quad \text{CASE } p = 0 \rightarrow \langle \text{"Idle_Thread"} \rangle \\
& \quad \quad \quad \square p = 1 \rightarrow \langle \text{"thread1"} \rangle \\
& \quad \quad \quad \square p = 2 \rightarrow \langle \text{"thread2"}, \text{"thread3"} \rangle \\
& \quad \quad], \\
& \quad \text{Interrupts_Enabled} \mapsto \text{TRUE} \\
&] \\
& \text{Thread_Object_Type} \triangleq [\\
& \quad \text{State} : \text{Thread_State_Type}, \\
& \quad \text{Current_Priority} : \text{Valid_Thread_Priority_Type}, \\
& \quad \text{Base_Priority} : \text{Valid_Thread_Priority_Type}, \\
& \quad \text{Builtin_Timer_Id} : \text{Timer_Id_Type}, \\
& \quad \text{Builtin_Condvar_Id} : \text{Condvar_Id_Type}, \\
& \quad \text{Waiting_On_Condvar_Id} : \text{Condvar_Id_Type}, \\
& \quad \text{Waiting_On_Mutex_Id} : \text{Mutex_Id_Type}, \\
& \quad \text{Owned_Mutexes} : \text{No_Duplicates_List_Type}(\text{Mutex_Id_Type}), \\
& \quad \text{ghost_Time_Slice_Consumed} : \text{BOOLEAN}, \\
& \quad \text{ghost_Condvar_Wait_Mutex_Id} : \text{Mutex_Id_Type} \\
&] \\
& \text{Thread_Object_Initializer}(\text{priority}, \text{timer_id}, \text{condvar_id}) \triangleq [\\
& \quad \text{State} \mapsto \text{"Runnable"}, \\
& \quad \text{Current_Priority} \mapsto \text{priority}, \\
& \quad \text{Base_Priority} \mapsto \text{priority}, \\
& \quad \text{Builtin_Timer_Id} \mapsto \text{timer_id}, \\
& \quad \text{Builtin_Condvar_Id} \mapsto \text{condvar_id}, \\
& \quad \text{Waiting_On_Condvar_Id} \mapsto \text{"Invalid_Condvar_Id"}, \\
& \quad \text{Waiting_On_Mutex_Id} \mapsto \text{"Invalid_Mutex_Id"}, \\
& \quad \text{Owned_Mutexes} \mapsto \langle \rangle, \\
& \quad \text{ghost_Time_Slice_Consumed} \mapsto \text{FALSE}, \\
& \quad \text{ghost_Condvar_Wait_Mutex_Id} \mapsto \text{"Invalid_Mutex_Id"} \\
&] \\
& \text{Mutex_Object_Type} \triangleq [\\
& \quad \text{Owner_Thread_Id} : \text{Thread_Id_Type}, \\
& \quad \text{Last_Inherited_Priority} : \text{Thread_Priority_Type}, \\
& \quad \text{Waiting_Threads_Queue} : \text{Thread_Priority_Queue_Type} \\
&] \\
& \text{Mutex_Object_Initializer} \triangleq [\\
& \quad \text{Owner_Thread_Id} \mapsto \text{"Invalid_Thread_Id"}, \\
& \quad \text{Last_Inherited_Priority} \mapsto \text{Invalid_Thread_Priority}, \\
&]
\end{aligned}$$

```

Waiting_Threads_Queue  $\mapsto$ 
  [  $p \in \text{Valid\_Thread\_Priority\_Type} \mapsto \langle \rangle$  ]
]

Condvar_Object_Type  $\triangleq$  [
  Wakeup_Mutex_Id : Mutex_Id_Type,
  Waiting_Threads_Queue : Thread_Priority_Queue_Type
]

Condvar_Object_Initializer  $\triangleq$  [
  Wakeup_Mutex_Id  $\mapsto$  "Invalid_Mutex_Id",
  Waiting_Threads_Queue  $\mapsto$ 
    [  $p \in \text{Valid\_Thread\_Priority\_Type} \mapsto \langle \rangle$  ]
]

Timer_Object_Type  $\triangleq$  [
  State : Timer_State_Type
]

Timer_Object_Initializer  $\triangleq$  [
  State  $\mapsto$  "Timer_Stopped"
]

Is_Thread_Priority_Queue_Empty(prio_queue)  $\triangleq$ 
   $\forall p \in \text{Valid\_Thread\_Priority\_Type} : \text{prio\_queue}[p] = \langle \rangle$ 

Is_Thread_In_Priority_Queue(prio_queue, thread_id)  $\triangleq$ 
   $\exists p \in \text{Valid\_Thread\_Priority\_Type} : \text{thread\_id} \in \text{Range}(\text{prio\_queue}[p])$ 

Is_Thread_In_Priority_Queue_In_Only_One_Queue(prio_queue, thread_id)  $\triangleq$ 
   $\text{Is\_Thread\_In\_Priority\_Queue}(\text{prio\_queue}, \text{thread\_id}) \Rightarrow$ 
   $\text{Cardinality}(\{p \in \text{Valid\_Thread\_Priority\_Type} : \text{thread\_id} \in \text{Range}(\text{prio\_queue}[p])\}) = 1$ 

Get_Highest_Priority(prio_queue)  $\triangleq$ 
  LET
    Non_Empty_Queues  $\triangleq$   $\{p \in \text{Valid\_Thread\_Priority\_Type} : \text{prio\_queue}[p] \neq \langle \rangle\}$ 
  IN
    CHOOSE  $p1 \in \text{Non\_Empty\_Queues} :$ 
       $\forall p2 \in \text{Non\_Empty\_Queues} \setminus \{p1\} : p2 < p1$ 

  Get_Highest_Priority_Queue(prio_queue)  $\triangleq$ 
    prio_queue[Get_Highest_Priority(prio_queue)]

*****

--algorithm hirtos_threads_model
variables
  HiRTOS = HiRTOS_Initializer,
  Thread_Objects = [

```

```

Idle_Thread  $\mapsto$ 
  Thread_Object_Initializer(0, "Invalid_Timer_Id", "Invalid_Condvar_Id"),
thread1  $\mapsto$ 
  Thread_Object_Initializer(1, "thread1_timer", "thread1_condvar"),
thread2  $\mapsto$ 
  Thread_Object_Initializer(2, "thread2_timer", "thread2_condvar"),
thread3  $\mapsto$ 
  Thread_Object_Initializer(2, "thread3_timer", "thread3_condvar")
],
Mutex_Objects = [m  $\in$  Mutexes  $\mapsto$  Mutex_Object_Initializer],
Condvar_Objects = [cv  $\in$  Condvars  $\mapsto$  Condvar_Object_Initializer],
Timer_Objects = [tm  $\in$  Timers  $\mapsto$  Timer_Object_Initializer],
Global_Resource_Available = FALSE;

define
  Enqueue_Thread(priority_queue, thread_id)  $\triangleq$ 
    LET
      priority  $\triangleq$  Thread_Objects[thread_id].Current_Priority
    IN
      [priority_queue EXCEPT ![priority] = Append(@, thread_id)]

  Enqueue_Thread_As_Head(priority_queue, thread_id)  $\triangleq$ 
    LET
      priority  $\triangleq$  Thread_Objects[thread_id].Current_Priority
    IN
      [priority_queue EXCEPT ![priority] = <thread_id>  $\circ$  @]

  Priority_Queue_Head(priority_queue)  $\triangleq$ 
    Head(Get_Highest_Priority_Queue(priority_queue))

  Priority_Queue_Tail(priority_queue)  $\triangleq$  [
    priority_queue EXCEPT ![Get_Highest_Priority(priority_queue)] = Tail(@)
  ]

end define ;

```

Macros

```

macro Move_Thread_To_Another_Queue(priority_queue, thread_id, old_prio, new_prio)
begin
  assert new_prio  $\neq$  old_prio ;
  Remove thread from its current queue:
  priority_queue[old_prio] := SelectSeq(priority_queue[old_prio], LAMBDA x : x  $\neq$  thread_id) ||
  Enqueue thread to new queue:
  priority_queue[new_prio] := Append(priority_queue[new_prio], thread_id) ||

```

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    Thread_Objects[thread_id].Current_Priority := new_prio ;
end macro ;

```

```

macro Enter_Critical_Section(context_id)
begin
    await HiRTOS.Interrupts_Enabled ∧
        (context_id ∈ Threads ⇒
            Thread_Objects[context_id].State = "Running") ;
    HiRTOS.Interrupts_Enabled := FALSE ;
end macro ;

```

```

macro Exit_Critical_Section()
begin
    HiRTOS.Interrupts_Enabled := TRUE ;
end macro ;

```

Procedures

```

procedure Run_Thread_Scheduler()
begin
    check_time_slice_step:
    assert ¬HiRTOS.Interrupts_Enabled ;
    if HiRTOS.Current_Thread_Id ≠ "Invalid_Thread_Id" then
        Thread_Objects[HiRTOS.Current_Thread_Id].State := "Runnable" ;
        if Thread_Objects[HiRTOS.Current_Thread_Id].ghost_Time_Slice_Consumed then
            HiRTOS.Runnable_Threads_Queue :=
                Enqueue_Thread(HiRTOS.Runnable_Threads_Queue, HiRTOS.Current_Thread_Id) ||
            HiRTOS.Current_Thread_Id := "Invalid_Thread_Id" ;
        else
            HiRTOS.Runnable_Threads_Queue :=
                Enqueue_Thread_As_Head(HiRTOS.Runnable_Threads_Queue,
                    HiRTOS.Current_Thread_Id) ||
            HiRTOS.Current_Thread_Id := "Invalid_Thread_Id" ;
        end if ;
    end if ;

    choose_next_thread_step:
    HiRTOS.Current_Thread_Id := Priority_Queue_Head(HiRTOS.Runnable_Threads_Queue) ||
    HiRTOS.Runnable_Threads_Queue := Priority_Queue_Tail(HiRTOS.Runnable_Threads_Queue) ;
    assert HiRTOS.Current_Thread_Id ≠ "Invalid_Thread_Id" ;
    Thread_Objects[HiRTOS.Current_Thread_Id].ghost_Time_Slice_Consumed := FALSE ||
    Thread_Objects[HiRTOS.Current_Thread_Id].State := "Running" ;

    run_scheduler_return_step:
    return ;
end

```

```

end procedure ;

procedure Do_Acquire_Mutex(thread_id, mutex_id, waking_up_thread_after_condvar_wait)
  variable owner_thread_id = "Invalid_Thread_Id" ;
begin
  acquire_mutex_step:
  assert ¬HiRTOS.Interrupts_Enabled ;
  if Mutex_Objects[mutex_id].Owner_Thread_Id = "Invalid_Thread_Id" then
    acquire_mutex_acquire_step:
    Mutex_Objects[mutex_id].Owner_Thread_Id := thread_id ||
    Thread_Objects[thread_id].Owned_Mutexes :=
      ⟨mutex_id⟩ ∘ Thread_Objects[thread_id].Owned_Mutexes ;
  if waking_up_thread_after_condvar_wait then
    acquire_mutex_make_condvar_wait_awoken_thread_runnable_step:
    assert thread_id ≠ HiRTOS.Current_Thread_Id ;
    assert Thread_Objects[thread_id].State = "Blocked_On_Condvar" ;
    assert thread_id ∉
      Range(HiRTOS.Runnable_Threads_Queue[Thread_Objects[thread_id].Current_Priority]) ;
    HiRTOS.Runnable_Threads_Queue :=
      Enqueue_Thread(HiRTOS.Runnable_Threads_Queue, thread_id) ||
    Thread_Objects[thread_id].State := "Runnable" ;
  else
    assert thread_id = HiRTOS.Current_Thread_Id ;
    assert Thread_Objects[thread_id].State = "Running" ;
  end if ;
else
  acquire_mutex_wait_on_mutex_step:
  owner_thread_id := Mutex_Objects[mutex_id].Owner_Thread_Id ;
  assert owner_thread_id ≠ thread_id ;
  Mutex_Objects[mutex_id].Waiting_Threads_Queue :=
    Enqueue_Thread(Mutex_Objects[mutex_id].Waiting_Threads_Queue, thread_id) ;
  if waking_up_thread_after_condvar_wait then
    assert thread_id ≠ HiRTOS.Current_Thread_Id ;
    assert Thread_Objects[thread_id].State = "Blocked_On_Condvar" ;
  else
    assert thread_id = HiRTOS.Current_Thread_Id ;
    assert Thread_Objects[thread_id].State = "Running" ;
    HiRTOS.Current_Thread_Id := "Invalid_Thread_Id" ;
  end if ;

  Thread_Objects[thread_id].State := "Blocked_On_Mutex" ||
  Thread_Objects[thread_id].Waiting_On_Mutex_Id := mutex_id ;

  acquire_mutex_check_if_priority_inheritance_needed_step:
  if Thread_Objects[owner_thread_id].Current_Priority <
    Thread_Objects[thread_id].Current_Priority then

```

```

acquire_mutex_priority_inheritance_step:
Mutex_Objects[mutex_id].Last_Inherited_Priority :=
  Thread_Objects[thread_id].Current_Priority ;
if Thread_Objects[owner_thread_id].State = "Runnable" then
  acquire_mutex_priority_inheritance_if_mutex_owner_runnable_step:
  Move_Thread_To_Another_Queue(
    HiRTOS.Runnable_Threads_Queue,
    owner_thread_id,
    Thread_Objects[owner_thread_id].Current_Priority,
    Thread_Objects[thread_id].Current_Priority) ;
elsif Thread_Objects[owner_thread_id].State = "Blocked_On_Mutex" then
  acquire_mutex_priority_inheritance_if_mutex_owner_blocked_on_mutex_step:
  Move_Thread_To_Another_Queue(
    Mutex_Objects[Thread_Objects[owner_thread_id].Waiting_On_Mutex_Id].
    Waiting_Threads_Queue,
    owner_thread_id,
    Thread_Objects[owner_thread_id].Current_Priority,
    Thread_Objects[thread_id].Current_Priority) ;
else
  assert Thread_Objects[owner_thread_id].State = "Blocked_On_Condvar" ;
  acquire_mutex_priority_inheritance_if_mutex_owner_blocked_on_condvar_step:
  Move_Thread_To_Another_Queue(
    Mutex_Objects[Thread_Objects[owner_thread_id].Waiting_On_Condvar_Id].
    Waiting_Threads_Queue,
    owner_thread_id,
    Thread_Objects[owner_thread_id].Current_Priority,
    Thread_Objects[thread_id].Current_Priority) ;
end if ;

  acquire_mutex_priority_inheritance_update_prio_step:
  Thread_Objects[owner_thread_id].Current_Priority :=
    Thread_Objects[thread_id].Current_Priority ;
end if ;

  acquire_mutex_check_if_synchronous_context_switch_needed_step:
  if  $\neg$ waking-up-thread-after-condvar-wait then
    acquire_mutex_synchronous_context_switch_step:
    call Run_Thread_Scheduler() ;
  end if ;
end if ;

  do_acquire_mutex_return_step:
  return ;
end procedure ;

procedure Acquire_Mutex(thread_id, mutex_id)

```

```

    variable owner_thread_id = "Invalid_Thread_Id" ;
begin
    enter_critical_section_step:
    Enter_Critical_Section(thread_id) ;
    assert HiRTOS.Current_Thread_Id = thread_id ;
    call Do_Acquire_Mutex(thread_id, mutex_id, FALSE) ;
    exit_critical_section_step:
    Exit_Critical_Section() ;
    acquire_mutex_return_step:
    return ;
end procedure ;

procedure Do_Release_Mutex(thread_id, mutex_id, doing_condvar_wait)
    variable awoken_thread_id = "Invalid_Thread_Id" ;
begin
    release_mutex_step:
    assert ¬HiRTOS.Interrupts_Enabled ;
    assert Mutex_Objects[mutex_id].Owner_Thread_Id = thread_id ;
    assert Thread_Objects[thread_id].Owned_Mutexes ≠ ⟨⟩ ;
    assert Head(Thread_Objects[thread_id].Owned_Mutexes) = mutex_id ;
    Thread_Objects[thread_id].Owned_Mutexes := Tail(Thread_Objects[thread_id].Owned_Mutexes) ;

    release_mutex_restore_priority_step:
    if Thread_Objects[thread_id].Owned_Mutexes ≠ ⟨⟩ ∧ ¬doing_condvar_wait then
        with prev_mutex_obj = Mutex_Objects[Head(Thread_Objects[thread_id].Owned_Mutexes)] do
            if prev_mutex_obj.Last_Inherited_Priority ≠ Invalid_Thread_Priority then
                Thread_Objects[thread_id].Current_Priority := prev_mutex_obj.Last_Inherited_Priority ;
            end if ;
        end with ;
    else
        Thread_Objects[thread_id].Current_Priority := Thread_Objects[thread_id].Base_Priority ;
    end if ;

    release_mutex_check_if_mutex_waiters_step:
    if Is_Thread_Priority_Queue_Empty(Mutex_Objects[mutex_id].Waiting_Threads_Queue) then
        Mutex_Objects[mutex_id].Owner_Thread_Id := "Invalid_Thread_Id" ;
    else
        release_mutex_wakeup_mutex_waiter_step:
        awoken_thread_id :=
            Priority_Queue_Head(Mutex_Objects[mutex_id].Waiting_Threads_Queue) ;
        assert Thread_Objects[awoken_thread_id].Waiting_On_Mutex_Id = mutex_id ;
        Mutex_Objects[mutex_id].Owner_Thread_Id := awoken_thread_id ||
        Mutex_Objects[mutex_id].Waiting_Threads_Queue :=
            Priority_Queue_Tail(Mutex_Objects[mutex_id].Waiting_Threads_Queue) ||
        HiRTOS.Runnable_Threads_Queue :=
            Enqueue_Thread(HiRTOS.Runnable_Threads_Queue, awoken_thread_id) ||

```



```

    Thread_Objects[awoken_thread_id].State := "Runnable" ||
    Thread_Objects[awoken_thread_id].Waiting_On_Mutex_Id := "Invalid_Mutex_Id" ||
    Thread_Objects[awoken_thread_id].Owned_Mutexes :=
        (mutex_id) o Thread_Objects[awoken_thread_id].Owned_Mutexes ;

    if ¬doing_condvar_wait then
        release_mutex_synchronous_context_switch_step:
        call Run_Thread_Scheduler() ;
    end if ;
end if ;

do_release_mutex_return_step:
return ;
end procedure ;

procedure Release_Mutex(thread_id, mutex_id)
begin
    enter_critical_section_step:
    Enter_Critical_Section(thread_id) ;
    assert HiRTOS.Current_Thread_Id = thread_id ;
    call Do_Release_Mutex(thread_id, mutex_id, FALSE) ;
    exit_critical_section_step:
    Exit_Critical_Section() ;
    release_mutex_return_step:
    return ;
end procedure ;

procedure Do_Wait_On_Condvar(thread_id, condvar_id, mutex_id)
begin
    wait_on_condvar_wait_step:
    assert ¬HiRTOS.Interrupts_Enabled ;
    Thread_Objects[thread_id].ghost_Condvar_Wait_Mutex_Id := mutex_id ||
    Condvar_Objects[condvar_id].Waiting_Threads_Queue :=
        Enqueue_Thread(Condvar_Objects[condvar_id].Waiting_Threads_Queue, thread_id) ||
    Thread_Objects[thread_id].State := "Blocked_On_Condvar" ||
    Thread_Objects[thread_id].Waiting_On_Condvar_Id := condvar_id ;
    HiRTOS.Current_Thread_Id := "Invalid_Thread_Id" ;

    wait_on_condvar_release_mutex_step:
    if mutex_id ≠ "Invalid_Mutex_Id" then
        call Do_Release_Mutex(thread_id, mutex_id, TRUE) ;
    end if ;

    wait_on_condvar_synchronous_context_switch_step:
    call Run_Thread_Scheduler() ;

    do_wait_on_condvar_return_step:

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```

    return ;
end procedure ;

procedure Wait_On_Condvar(thread_id, condvar_id, mutex_id)
begin
    enter_critical_section_step:
    Enter_Critical_Section(thread_id) ;
    call Do_Wait_On_Condvar(thread_id, condvar_id, mutex_id) ;
    exit_critical_section_step:
    Exit_Critical_Section() ;
    wait_on_condvar_return_step:
    return ;
end procedure ;

procedure Do_Signal_Condvar(condvar_id, do_context_switch)
    variables awoken_thread_id = "Invalid_Thread_Id",
               to_reacquire_mutex_id = "Invalid_Mutex_Id" ;
begin
    signal_condvar_step:
    assert ¬HiRTOS.Interrupts_Enabled ;
    if ¬Is_Thread_Priority_Queue_Empty(Condvar_Objects[condvar_id].Waiting_Threads_Queue)
    then
        signal_condvar_wakeup_waiter_step:
        awoken_thread_id :=
            Priority_Queue_Head(Condvar_Objects[condvar_id].Waiting_Threads_Queue) ;
        Condvar_Objects[condvar_id].Waiting_Threads_Queue :=
            Priority_Queue_Tail(Condvar_Objects[condvar_id].Waiting_Threads_Queue) ;
        assert awoken_thread_id ≠ HiRTOS.Current_Thread_Id ;
        assert Thread_Objects[awoken_thread_id].Waiting_On_Condvar_Id = condvar_id ;
        assert Thread_Objects[awoken_thread_id].Waiting_On_Mutex_Id = "Invalid_Mutex_Id" ;

        to_reacquire_mutex_id := Thread_Objects[awoken_thread_id].ghost_Condvar_Wait_Mutex_Id ;
        Thread_Objects[awoken_thread_id].ghost_Condvar_Wait_Mutex_Id := "Invalid_Mutex_Id" ||
        Thread_Objects[awoken_thread_id].Waiting_On_Condvar_Id := "Invalid_Condvar_Id" ;

        signal_condvar_check_if_mutex_reacquire_needed_step:
        if to_reacquire_mutex_id ≠ "Invalid_Mutex_Id" then
            signal_condvar_reacquire_mutex_step:
            call Do_Acquire_Mutex(awoken_thread_id, to_reacquire_mutex_id, TRUE) ;
        else
            signal_condvar_awoken_thread_runnable_step:
            HiRTOS.Runnable_Threads_Queue :=
                Enqueue_Thread(HiRTOS.Runnable_Threads_Queue, awoken_thread_id) ||
            Thread_Objects[awoken_thread_id].State := "Runnable" ;
        end if ;
    end if ;
end procedure ;

```

```

    signal_condvar_check_if_sync_context_switch_needed_step:
    if do_context_switch then
        signal_condvar_synchronous_context_switch_step:
        call Run_Thread_Scheduler() ;
    end if ;
end if ;

do_condvar_signal_return_step:
return ;
end procedure ;

procedure Signal_Condvar(context_id, condvar_id)
begin
    enter_critical_section_step:
    Enter_Critical_Section(context_id) ;
    call Do_Signal_Condvar(condvar_id, TRUE) ;
    exit_critical_section_step:
    Exit_Critical_Section() ;
    condvar_signaled_step:
    return ;
end procedure ;

procedure Broadcast_Condvar(context_id, condvar_id)
    variable thread_was_awaken = FALSE ;
begin
    enter_critical_section_step:
    Enter_Critical_Section(context_id) ;

    broadcast_condvar_step:
    while  $\neg \text{Is\_Thread\_Priority\_Queue\_Empty}(\text{Condvar\_Objects}[\text{condvar\_id}].\text{Waiting\_Threads\_Queue})$ 
    do
        broadcast_condvar_wakeup_waiter_step:
        call Do_Signal_Condvar(condvar_id, FALSE) ;
        broadcast_condvar_after_waking_up_one_waiter_step:
        thread_was_awaken := TRUE ;
    end while ;

    broadcast_condvar_check_if_sync_context_switch_needed_step:
    if  $\text{context\_id} \in \text{Threads} \wedge \text{thread\_was\_awaken}$  then
        broadcast_condvar_synchronous_context_switch_step:
        call Run_Thread_Scheduler() ;
    end if ;

    exit_critical_section_step:
    Exit_Critical_Section() ;
    condvar_broadcasted_step:
    return ;

```

```

end procedure ;

procedure Delay_Until(thread_id)
begin
  enter_critical_section_step:
  Enter_Critical_Section(thread_id) ;

  delay_until_step:
  Timer_Objects[Thread_Objects[thread_id].Builtin_Timer_Id].State := "Timer_Running" ;
  call Do_Wait_On_Condvar(thread_id, Thread_Objects[thread_id].Builtin_Condvar_Id,
    "Invalid_Mutex_Id") ;

  exit_critical_section_step:
  Exit_Critical_Section() ;

  after_delay_until_step:
  return ;
end procedure ;

```

Processes

```

fair process Thread_State_Machine  $\in$  Threads  $\setminus \{ \text{"Idle_Thread"} \}$ 
begin
  thread_state_machine_next_state_loop:
  while TRUE do
    await Thread_Objects[self].State = "Running"  $\wedge$  HiRTOS.Interrupts_Enabled ;
    context_switch0:
    either
      acquire_mutex_step:
      call Acquire_Mutex(self, "mutex1") ;
      context_switch1:
      await Thread_Objects[self].State = "Running"  $\wedge$  HiRTOS.Interrupts_Enabled ;
      assert (Mutex_Objects["mutex1"].Owner_Thread_Id = self) ;
    either
      waiting_for_resource_step:
      while  $\neg$ Global_Resource_Available do
        call Wait_On_Condvar(self, "condvar1", "mutex1") ;
        context_switch2:
        await Thread_Objects[self].State = "Running"  $\wedge$  HiRTOS.Interrupts_Enabled ;
        assert Mutex_Objects["mutex1"].Owner_Thread_Id = self ;
      end while ;
      Global_Resource_Available := FALSE ;
    or

```

```

        skip ;
    end either ;

    release_mutex_step:
    call Release_Mutex(self, "mutex1") ;
or
    Global_Resource_Available := TRUE ;
    either
        call Signal_Condvar(self, "condvar1") ;
    or
        call Broadcast_Condvar(self, "condvar1") ;
    end either ;
or
    call Delay_Until(self)
end either ;

    thread_iteration_completed_step:
    skip ;
end while ;
end process ;

fair process Idle_Thread = "Idle_Thread"
begin
    idle_thread_next_state_loop:
    while TRUE do
        await Thread_Objects["Idle_Thread"].State = "Running"  $\wedge$  HiRTOS.Interrupts_Enabled ;
    end while ;
end process ;

fair process Timer_Interrupt = "Timer_Interrupt"
    variable delayed_threads = {} ;
begin
    timer_interrupt_next_state_loop:
    while TRUE do
        enter_critical_section_step:
        Enter_Critical_Section("Timer_Interrupt") ;

        track_time_slice:
        if HiRTOS.Current_Thread_Id  $\neq$  "Invalid_Thread_Id" then
            assert  $\neg$  Thread_Objects[HiRTOS.Current_Thread_Id].ghost_Time_Slice_Consumed ;
            Thread_Objects[HiRTOS.Current_Thread_Id].ghost_Time_Slice_Consumed := TRUE ;
        end if ;

        delayed_threads :=
            {t  $\in$  Threads \ {"Idle_Thread"} :
            Timer_Objects[Thread_Objects[t].Builtin_Timer_Id].State = "Timer_Running"} ;
    end while ;
end process ;

```

```

    wakeup_delay_until_waiters:
    while delayed_threads ≠ {} do
        with t ∈ delayed_threads do
            delayed_threads := delayed_threads \ {t};
            Timer_Objects[Thread_Objects[t].Builtin_Timer_Id].State := "Timer_Stopped" ;
            call Do_Signal_Condvar( Thread_Objects[t].Builtin_Condvar_Id, FALSE);
        end with ;
    end while ;

    timer_interrupt_asynchronous_context_switch_step:
    call Run_Thread_Scheduler();

    exit_critical_section_step:
    Exit_Critical_Section();
end while ;
end process ;

fair process Other_Interrupt = "Other_Interrupt"
begin
    other_interrupt_next_state_loop:
    while TRUE do
        enter_critical_section_step:
        Enter_Critical_Section("Other_Interrupt");

        other_interrupt_asynchronous_context_switch_step:
        call Run_Thread_Scheduler();

        exit_critical_section_step:
        Exit_Critical_Section();
    end while ;
end process ;

end algorithm ;
*****

BEGIN TRANSLATION (chksum(pcal) = "ec6916c" ∧ chksum(tla) = "5dd4fa09")
Label acquire_mutex_step of procedure Do_Acquire_Mutex at line 248 col 7 changed to acquire_mutex_step_
Label enter_critical_section_step of procedure Acquire_Mutex at line 200 col 7 changed to enter_critical_section_step_
Label exit_critical_section_step of procedure Acquire_Mutex at line 208 col 7 changed to exit_critical_section_step_
Label release_mutex_step of procedure Do_Release_Mutex at line 350 col 7 changed to release_mutex_step_
Label enter_critical_section_step of procedure Release_Mutex at line 200 col 7 changed to enter_critical_section_step_R
Label exit_critical_section_step of procedure Release_Mutex at line 208 col 7 changed to exit_critical_section_step_R
Label enter_critical_section_step of procedure Wait_On_Condvar at line 200 col 7 changed to enter_critical_section_step_W
Label exit_critical_section_step of procedure Wait_On_Condvar at line 208 col 7 changed to exit_critical_section_step_W
Label enter_critical_section_step of procedure Signal_Condvar at line 200 col 7 changed to enter_critical_section_step_S
Label exit_critical_section_step of procedure Signal_Condvar at line 208 col 7 changed to exit_critical_section_step_S
Label enter_critical_section_step of procedure Broadcast_Condvar at line 200 col 7 changed to enter_critical_section_step_

```

Label *exit_critical_section_step* of procedure *Broadcast_Condvar* at line 208 col 7 changed to *exit_critical_section_step_B*
Label *enter_critical_section_step* of procedure *Delay_Until* at line 200 col 7 changed to *enter_critical_section_step_D*
Label *exit_critical_section_step* of procedure *Delay_Until* at line 208 col 7 changed to *exit_critical_section_step_D*
Label *enter_critical_section_step* of process *Timer_Interrupt* at line 200 col 7 changed to *enter_critical_section_step_T*
Label *exit_critical_section_step* of process *Timer_Interrupt* at line 208 col 7 changed to *exit_critical_section_step_T*
Procedure variable *owner_thread_id* of procedure *Do_Acquire_Mutex* at line 245 col 16 changed to *owner_thread_id_*
Procedure variable *awoken_thread_id* of procedure *Do_Release_Mutex* at line 347 col 16 changed to *awoken_thread_id_*
Parameter *thread_id* of procedure *Do_Acquire_Mutex* at line 244 col 31 changed to *thread_id_*
Parameter *mutex_id* of procedure *Do_Acquire_Mutex* at line 244 col 42 changed to *mutex_id_*
Parameter *thread_id* of procedure *Acquire_Mutex* at line 333 col 28 changed to *thread_id_A*
Parameter *mutex_id* of procedure *Acquire_Mutex* at line 333 col 39 changed to *mutex_id_A*
Parameter *thread_id* of procedure *Do_Release_Mutex* at line 346 col 31 changed to *thread_id_D*
Parameter *mutex_id* of procedure *Do_Release_Mutex* at line 346 col 42 changed to *mutex_id_D*
Parameter *thread_id* of procedure *Release_Mutex* at line 395 col 25 changed to *thread_id_R*
Parameter *mutex_id* of procedure *Release_Mutex* at line 395 col 36 changed to *mutex_id_R*
Parameter *thread_id* of procedure *Do_Wait_On_Condvar* at line 407 col 33 changed to *thread_id_Do*
Parameter *condvar_id* of procedure *Do_Wait_On_Condvar* at line 407 col 44 changed to *condvar_id_*
Parameter *mutex_id* of procedure *Do_Wait_On_Condvar* at line 407 col 56 changed to *mutex_id_Do*
Parameter *thread_id* of procedure *Wait_On_Condvar* at line 430 col 30 changed to *thread_id_W*
Parameter *condvar_id* of procedure *Wait_On_Condvar* at line 430 col 41 changed to *condvar_id_W*
Parameter *condvar_id* of procedure *Do_Signal_Condvar* at line 441 col 32 changed to *condvar_id_D*
Parameter *context_id* of procedure *Signal_Condvar* at line 484 col 29 changed to *context_id_*
Parameter *condvar_id* of procedure *Signal_Condvar* at line 484 col 41 changed to *condvar_id_S*

CONSTANT *defaultInitValue*

VARIABLES *HiRTOS*, *Thread_Objects*, *Mutex_Objects*, *Condvar_Objects*,
Timer_Objects, *Global_Resource_Available*, *pc*, *stack*

define statement

```

Enqueue_Thread(priority_queue, thread_id)  $\triangleq$ 
  LET
    priority  $\triangleq$  Thread_Objects[thread_id].Current_Priority
  IN
    [priority_queue EXCEPT ![priority] = Append(@, thread_id)]

Enqueue_Thread_As_Head(priority_queue, thread_id)  $\triangleq$ 
  LET
    priority  $\triangleq$  Thread_Objects[thread_id].Current_Priority
  IN
    [priority_queue EXCEPT ![priority] =  $\langle$ thread_id $\rangle \circ$  @]

Priority_Queue_Head(priority_queue)  $\triangleq$ 
  Head(Get_Highest_Priority_Queue(priority_queue))

Priority_Queue_Tail(priority_queue)  $\triangleq$  [
  priority_queue EXCEPT ![Get_Highest_Priority(priority_queue)] = Tail(@)
]

```

VARIABLES *thread_id_*, *mutex_id_*, *waking_up_thread_after_condvar_wait*,
owner_thread_id_, *thread_id_A*, *mutex_id_A*, *owner_thread_id*,
thread_id_D, *mutex_id_D*, *doing_condvar_wait*, *awoken_thread_id_*,
thread_id_R, *mutex_id_R*, *thread_id_Do*, *condvar_id_*, *mutex_id_Do*,
thread_id_W, *condvar_id_W*, *mutex_id*, *condvar_id_D*,
do_context_switch, *awoken_thread_id*, *to_reacquire_mutex_id*,
context_id_, *condvar_id_S*, *context_id*, *condvar_id*,
thread_was_awaken, *thread_id*, *delayed_threads*

vars \triangleq $\langle \text{HiRTOS}, \text{Thread_Objects}, \text{Mutex_Objects}, \text{Condvar_Objects},$
Timer_Objects, *Global_Resource_Available*, *pc*, *stack*, *thread_id_*,
mutex_id_, *waking_up_thread_after_condvar_wait*, *owner_thread_id_*,
thread_id_A, *mutex_id_A*, *owner_thread_id*, *thread_id_D*, *mutex_id_D*,
doing_condvar_wait, *awoken_thread_id_*, *thread_id_R*, *mutex_id_R*,
thread_id_Do, *condvar_id_*, *mutex_id_Do*, *thread_id_W*, *condvar_id_W*,
mutex_id, *condvar_id_D*, *do_context_switch*, *awoken_thread_id*,
to_reacquire_mutex_id, *context_id_*, *condvar_id_S*, *context_id*,
condvar_id, *thread_was_awaken*, *thread_id*, *delayed_threads* \rangle

ProcSet \triangleq (*Threads* \setminus { "Idle_Thread" }) \cup { "Idle_Thread" } \cup { "Timer_Interrupt" } \cup { "Other_Interrupt" }

Init \triangleq Global variables
 \wedge *HiRTOS* = *HiRTOS_Initializer*
 \wedge *Thread_Objects* = [
Idle_Thread \mapsto *Thread_Object_Initializer*(0, "Invalid_Timer_Id", "Invalid_Condvar_Id"),
thread1 \mapsto *Thread_Object_Initializer*(1, "thread1_timer", "thread1_condvar"),
thread2 \mapsto *Thread_Object_Initializer*(2, "thread2_timer", "thread2_condvar"),
thread3 \mapsto *Thread_Object_Initializer*(2, "thread3_timer", "thread3_condvar")
]
 \wedge *Mutex_Objects* = [*m* \in *Mutexes* \mapsto *Mutex_Object_Initializer*]
 \wedge *Condvar_Objects* = [*cv* \in *Condvars* \mapsto *Condvar_Object_Initializer*]
 \wedge *Timer_Objects* = [*tm* \in *Timers* \mapsto *Timer_Object_Initializer*]
 \wedge *Global_Resource_Available* = FALSE
Procedure *Do_Acquire_Mutex*
 \wedge *thread_id_* = [*self* \in *ProcSet* \mapsto *defaultInitValue*]
 \wedge *mutex_id_* = [*self* \in *ProcSet* \mapsto *defaultInitValue*]
 \wedge *waking_up_thread_after_condvar_wait* = [*self* \in *ProcSet* \mapsto *defaultInitValue*]
 \wedge *owner_thread_id_* = [*self* \in *ProcSet* \mapsto "Invalid_Thread_Id"]
Procedure *Acquire_Mutex*
 \wedge *thread_id_A* = [*self* \in *ProcSet* \mapsto *defaultInitValue*]
 \wedge *mutex_id_A* = [*self* \in *ProcSet* \mapsto *defaultInitValue*]
 \wedge *owner_thread_id* = [*self* \in *ProcSet* \mapsto "Invalid_Thread_Id"]


```

Procedure Do_Release_Mutex
 $\wedge$  thread_id_D = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  mutex_id_D = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  doing_condvar_wait = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  awoken_thread_id_ = [self  $\in$  ProcSet  $\mapsto$  "Invalid_Thread_Id"]
Procedure Release_Mutex
 $\wedge$  thread_id_R = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  mutex_id_R = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
Procedure Do_Wait_On_Condvar
 $\wedge$  thread_id_Do = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  condvar_id_ = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  mutex_id_Do = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
Procedure Wait_On_Condvar
 $\wedge$  thread_id_W = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  condvar_id_W = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  mutex_id = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
Procedure Do_Signal_Condvar
 $\wedge$  condvar_id_D = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  do_context_switch = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  awoken_thread_id = [self  $\in$  ProcSet  $\mapsto$  "Invalid_Thread_Id"]
 $\wedge$  to_reacquire_mutex_id = [self  $\in$  ProcSet  $\mapsto$  "Invalid_Mutex_Id"]
Procedure Signal_Condvar
 $\wedge$  context_id_ = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  condvar_id_S = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
Procedure Broadcast_Condvar
 $\wedge$  context_id = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  condvar_id = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
 $\wedge$  thread_was_awaken = [self  $\in$  ProcSet  $\mapsto$  FALSE]
Procedure Delay_Until
 $\wedge$  thread_id = [self  $\in$  ProcSet  $\mapsto$  defaultInitValue]
Process Timer_Interrupt
 $\wedge$  delayed_threads = {}
 $\wedge$  stack = [self  $\in$  ProcSet  $\mapsto$   $\langle \rangle$ ]
 $\wedge$  pc = [self  $\in$  ProcSet  $\mapsto$  CASE self  $\in$  Threads  $\setminus$  {"Idle_Thread"}  $\rightarrow$  "thread_state_machine_next_step"
 $\square$  self = "Idle_Thread"  $\rightarrow$  "idle_thread_next_state_loop"
 $\square$  self = "Timer_Interrupt"  $\rightarrow$  "timer_interrupt_next_state_loop"
 $\square$  self = "Other_Interrupt"  $\rightarrow$  "other_interrupt_next_state_loop"]

check_time_slice_step(self)  $\triangleq$   $\wedge$  pc[self] = "check_time_slice_step"
 $\wedge$  Assert( $\neg$ HiRTOS.Interrupts_Enabled,
"Failure of assertion at line 218, column 7.")
 $\wedge$  IF HiRTOS.Current_Thread_Id  $\neq$  "Invalid_Thread_Id"
THEN  $\wedge$  Thread_Objects' = [Thread_Objects EXCEPT ![HiRTOS.Current_Thread_Id].ghost_Timer]
 $\wedge$  IF Thread_Objects'[HiRTOS.Current_Thread_Id].ghost_Timer
THEN  $\wedge$  HiRTOS' = [HiRTOS EXCEPT !.Runnable_Thread_Objects]

```

$$\begin{aligned}
& \text{ELSE } \wedge \text{HiRTOS}' = [\text{HiRTOS} \text{ EXCEPT } !.Current_Thread_Id = Priority_Queue_Head, \\
& \quad !.Runnable_Threads_Queue = Priority_Queue_Head] \\
& \text{ELSE } \wedge \text{TRUE} \\
& \quad \wedge \text{UNCHANGED } \langle \text{HiRTOS}, \\
& \quad \quad \text{Thread_Objects} \rangle \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"choose_next_thread_step"}] \\
& \wedge \text{UNCHANGED } \langle \text{Mutex_Objects}, \text{Condvar_Objects}, \\
& \quad \text{Timer_Objects}, \\
& \quad \text{Global_Resource_Available}, \\
& \quad \text{stack}, \text{thread_id_}, \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait}, \\
& \quad \text{owner_thread_id_}, \text{thread_id_A}, \\
& \quad \text{mutex_id_A}, \text{owner_thread_id_}, \\
& \quad \text{thread_id_D}, \text{mutex_id_D}, \\
& \quad \text{doing_condvar_wait}, \\
& \quad \text{awoken_thread_id_}, \text{thread_id_R}, \\
& \quad \text{mutex_id_R}, \text{thread_id_Do}, \\
& \quad \text{condvar_id_}, \text{mutex_id_Do}, \\
& \quad \text{thread_id_W}, \text{condvar_id_W}, \\
& \quad \text{mutex_id}, \text{condvar_id_D}, \\
& \quad \text{do_context_switch}, \\
& \quad \text{awoken_thread_id}, \\
& \quad \text{to_reacquire_mutex_id}, \\
& \quad \text{context_id_}, \text{condvar_id_S}, \\
& \quad \text{context_id}, \text{condvar_id}, \\
& \quad \text{thread_was_awaken}, \text{thread_id}, \\
& \quad \text{delayed_threads} \rangle \\
\text{choose_next_thread_step}(self) & \triangleq \wedge pc[self] = \text{"choose_next_thread_step"} \\
& \wedge \text{HiRTOS}' = [\text{HiRTOS} \text{ EXCEPT } !.Current_Thread_Id = Priority_Queue_Head, \\
& \quad !.Runnable_Threads_Queue = Priority_Queue_Head] \\
& \wedge \text{Assert}(\text{HiRTOS}'.Current_Thread_Id \neq \text{"Invalid_Thread_Id"}, \\
& \quad \text{"Failure of assertion at line 236, column 7."}) \\
& \wedge \text{Thread_Objects}' = [\text{Thread_Objects} \text{ EXCEPT } ![\text{HiRTOS}'.Current_Thread_Id] = \text{"Invalid_Thread_Id"}, \\
& \quad ![\text{HiRTOS}'.Current_Thread_Id] = \text{"Invalid_Thread_Id"}] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"run_scheduler_return_step"}] \\
& \wedge \text{UNCHANGED } \langle \text{Mutex_Objects}, \\
& \quad \text{Condvar_Objects}, \\
& \quad \text{Timer_Objects}, \\
& \quad \text{Global_Resource_Available}, \\
& \quad \text{stack}, \text{thread_id_}, \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait}, \\
& \quad \text{owner_thread_id_}, \text{thread_id_A},
\end{aligned}$$

mutex_id_A, *owner_thread_id*,
thread_id_D, *mutex_id_D*,
doing_condvar_wait,
awoken_thread_id,
thread_id_R, *mutex_id_R*,
thread_id_Do, *condvar_id*,
mutex_id_Do, *thread_id_W*,
condvar_id_W, *mutex_id*,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id, *condvar_id_S*,
context_id, *condvar_id*,
thread_was_awaken, *thread_id*,
delayed_threads

$run_scheduler_return_step(self) \triangleq$ $\wedge pc[self] = \text{"run_scheduler_return_step"}$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = Head(stack[self]).pc]$
 $\wedge stack' = [stack \text{ EXCEPT } ![self] = Tail(stack[self])]$
 $\wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects,$
 $\quad Mutex_Objects,$
 $\quad Condvar_Objects,$
 $\quad Timer_Objects,$
 $\quad Global_Resource_Available,$
 $\quad thread_id_ , mutex_id_ ,$
 $\quad waking_up_thread_after_condvar_wait,$
 $\quad owner_thread_id_ ,$
 $\quad thread_id_A, mutex_id_A,$
 $\quad owner_thread_id,$
 $\quad thread_id_D, mutex_id_D,$
 $\quad doing_condvar_wait,$
 $\quad awoken_thread_id_ ,$
 $\quad thread_id_R, mutex_id_R,$
 $\quad thread_id_Do, condvar_id_ ,$
 $\quad mutex_id_Do, thread_id_W,$
 $\quad condvar_id_W, mutex_id,$
 $\quad condvar_id_D,$
 $\quad do_context_switch,$
 $\quad awoken_thread_id,$
 $\quad to_reacquire_mutex_id,$
 $\quad context_id_ , condvar_id_S,$
 $\quad context_id, condvar_id,$
 $\quad thread_was_awaken,$
 $\quad thread_id, delayed_threads \rangle$

$$\begin{aligned}
\text{Run_Thread_Scheduler}(self) &\triangleq \text{check_time_slice_step}(self) \\
&\quad \vee \text{choose_next_thread_step}(self) \\
&\quad \vee \text{run_scheduler_return_step}(self) \\
\\
\text{acquire_mutex_step_}(self) &\triangleq \wedge pc[self] = \text{"acquire_mutex_step_"} \\
&\quad \wedge \text{Assert}(\neg \text{HiRTOS.Interrupts_Enabled}, \\
&\quad \quad \text{"Failure of assertion at line 248, column 7."}) \\
&\quad \wedge \text{IF } \text{Mutex_Objects}[\text{mutex_id_}[self]].\text{Owner_Thread_Id} = \text{"Invalid_Thread_Id"} \\
&\quad \quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT } !self] = \text{"acquire_mutex_acquire_step"} \\
&\quad \quad \text{ELSE } \wedge pc' = [pc \text{ EXCEPT } !self] = \text{"acquire_mutex_wait_on_mutex_step"} \\
&\quad \wedge \text{UNCHANGED } \langle \text{HiRTOS}, \text{Thread_Objects}, \\
&\quad \quad \text{Mutex_Objects}, \text{Condvar_Objects}, \\
&\quad \quad \text{Timer_Objects}, \\
&\quad \quad \text{Global_Resource_Available}, \text{stack}, \\
&\quad \quad \text{thread_id_}, \text{mutex_id_}, \\
&\quad \quad \text{waking_up_thread_after_condvar_wait}, \\
&\quad \quad \text{owner_thread_id_}, \text{thread_id_A}, \\
&\quad \quad \text{mutex_id_A}, \text{owner_thread_id}, \\
&\quad \quad \text{thread_id_D}, \text{mutex_id_D}, \\
&\quad \quad \text{doing_condvar_wait}, \\
&\quad \quad \text{awoken_thread_id_}, \text{thread_id_R}, \\
&\quad \quad \text{mutex_id_R}, \text{thread_id_Do}, \\
&\quad \quad \text{condvar_id_}, \text{mutex_id_Do}, \\
&\quad \quad \text{thread_id_W}, \text{condvar_id_W}, \\
&\quad \quad \text{mutex_id}, \text{condvar_id_D}, \\
&\quad \quad \text{do_context_switch}, \\
&\quad \quad \text{awoken_thread_id}, \\
&\quad \quad \text{to_reacquire_mutex_id}, \\
&\quad \quad \text{context_id_}, \text{condvar_id_S}, \\
&\quad \quad \text{context_id}, \text{condvar_id}, \\
&\quad \quad \text{thread_was_awaken}, \text{thread_id}, \\
&\quad \quad \text{delayed_threads} \rangle \\
\\
\text{acquire_mutex_acquire_step}(self) &\triangleq \wedge pc[self] = \text{"acquire_mutex_acquire_step"} \\
&\quad \wedge \wedge \text{Mutex_Objects}' = [\text{Mutex_Objects} \text{ EXCEPT } ![\text{mutex_id_}[self]].\text{Owner_Thread_Id}] \\
&\quad \quad \wedge \text{Thread_Objects}' = [\text{Thread_Objects} \text{ EXCEPT } ![\text{thread_id_}[self]].\text{Owner_Thread_Id}] \\
&\quad \wedge \text{IF } \text{waking_up_thread_after_condvar_wait}[self] \\
&\quad \quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT } !self] = \text{"acquire_mutex_make_condvar_wait"} \\
&\quad \quad \text{ELSE } \wedge \text{Assert}(\text{thread_id_}[self] = \text{HiRTOS.Current_Thread_Id}, \\
&\quad \quad \quad \text{"Failure of assertion at line 264, column 13."}) \\
&\quad \quad \wedge \text{Assert}(\text{Thread_Objects}'[\text{thread_id_}[self]].\text{State} = \text{"Running"}, \\
&\quad \quad \quad \text{"Failure of assertion at line 265, column 13."}) \\
&\quad \quad \wedge pc' = [pc \text{ EXCEPT } !self] = \text{"do_acquire_mutex_return_step"} \\
&\quad \wedge \text{UNCHANGED } \langle \text{HiRTOS}, \text{Condvar_Objects}, \\
&\quad \quad \text{Timer_Objects},
\end{aligned}$$

Global_Resource_Available,
stack, thread_id_,
mutex_id_,
waking_up_thread_after_condvar_wait,
owner_thread_id_,
thread_id_A, mutex_id_A,
owner_thread_id,
thread_id_D, mutex_id_D,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R, mutex_id_R,
thread_id_Do, condvar_id_,
mutex_id_Do, thread_id_W,
condvar_id_W, mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_, condvar_id_S,
context_id, condvar_id,
thread_was_awaken,
thread_id, delayed_threads

$$\begin{aligned}
\text{acquire_mutex_make_condvar_wait_awoken_thread_runnable_step}(self) \triangleq & \wedge pc[self] = \text{"acquire_mutex_m} \\
& \wedge \text{Assert}(\text{thread_id_}[self] \neq H \\
& \quad \text{"Failure of assertion :} \\
& \wedge \text{Assert}(\text{Thread_Objects}[\text{thre} \\
& \quad \text{"Failure of assertion :} \\
& \wedge \text{Assert}(\text{thread_id_}[self] \notin \\
& \quad \text{Range}(\text{HiRTOS.Run} \\
& \quad \text{"Failure of assertion :} \\
& \wedge \wedge \text{HiRTOS}' = [\text{HiRTOS EX} \\
& \quad \wedge \text{Thread_Objects}' = [\text{Thre} \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \\
& \wedge \text{UNCHANGED } \langle \text{Mutex_Object} \\
& \quad \text{Condvar_Obj} \\
& \quad \text{Timer_Object} \\
& \quad \text{Global_Resou} \\
& \quad \text{stack,} \\
& \quad \text{thread_id_}, \\
& \quad \text{mutex_id_}, \\
& \quad \text{waking_up_th} \\
& \quad \text{owner_thread} \\
& \quad \text{thread_id_A,} \\
& \quad \text{mutex_id_A,}
\end{aligned}$$

$$\begin{aligned}
& \text{owner_thread_id_D,} \\
& \text{mutex_id_D,} \\
& \text{doing_condvar_wait_thread_id_R,} \\
& \text{mutex_id_R,} \\
& \text{thread_id_Do,} \\
& \text{condvar_id_Do,} \\
& \text{thread_id_W,} \\
& \text{condvar_id_W,} \\
& \text{mutex_id_L,} \\
& \text{condvar_id_L,} \\
& \text{do_context_switch_thread_id_W,} \\
& \text{awoken_thread_id_L,} \\
& \text{to_reacquire_thread_id_L,} \\
& \text{context_id_S,} \\
& \text{condvar_id_S,} \\
& \text{context_id_L,} \\
& \text{condvar_id_L,} \\
& \text{thread_was_awoken_thread_id_L,} \\
& \text{thread_id_L,} \\
& \text{delayed_thread_id_L}
\end{aligned}$$

$$\begin{aligned}
\text{acquire_mutex_wait_on_mutex_step}(self) \triangleq & \wedge pc[self] = \text{"acquire_mutex_wait_on_mutex_step"} \\
& \wedge \text{owner_thread_id_}' = [\text{owner_thread_id_} \text{ EXCEPT } ![self] = \text{owner_thread_id_}] \\
& \wedge \text{Assert}(\text{owner_thread_id_}'[self] \neq \text{thread_id_}[self], \\
& \quad \text{"Failure of assertion at line 270, column 10."}) \\
& \wedge \text{Mutex_Objects}' = [\text{Mutex_Objects} \text{ EXCEPT } ![\text{mutex_id_}[self] = \text{mutex_id_}]] \\
& \wedge \text{IF } \text{waking_up_thread_after_condvar_wait}[self] \\
& \quad \text{THEN } \wedge \text{Assert}(\text{thread_id_}[self] \neq \text{HiRTOS.Current_Thread_id_}, \\
& \quad \quad \text{"Failure of assertion at line 274, column 10."}) \\
& \quad \wedge \text{Assert}(\text{Thread_Objects}[\text{thread_id_}[self]].\text{State} = \text{Waiting}, \\
& \quad \quad \text{"Failure of assertion at line 275, column 10."}) \\
& \quad \wedge \text{UNCHANGED } \text{HiRTOS} \\
& \quad \text{ELSE } \wedge \text{Assert}(\text{thread_id_}[self] = \text{HiRTOS.Current_Thread_id_}, \\
& \quad \quad \text{"Failure of assertion at line 277, column 10."}) \\
& \quad \wedge \text{Assert}(\text{Thread_Objects}[\text{thread_id_}[self]].\text{State} = \text{Running}, \\
& \quad \quad \text{"Failure of assertion at line 278, column 10."}) \\
& \quad \wedge \text{HiRTOS}' = [\text{HiRTOS} \text{ EXCEPT } !.\text{Current_Thread_id_} = \text{thread_id_}[self]] \\
& \wedge \text{Thread_Objects}' = [\text{Thread_Objects} \text{ EXCEPT } ![\text{thread_id_}[self] = \text{thread_id_}]] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"acquire_mutex_check_if_priority_change_needed"}] \\
& \wedge \text{UNCHANGED } \langle \text{Condvar_Objects},
\end{aligned}$$

Timer_Objects,
Global_Resource_Available,
stack, thread_id_,
mutex_id_,
waking_up_thread_after_condvar_wait,
thread_id_A,
mutex_id_A,
owner_thread_id,
thread_id_D,
mutex_id_D,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R,
mutex_id_R,
thread_id_Do,
condvar_id_,
mutex_id_Do,
thread_id_W,
condvar_id_W,
mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id,
condvar_id,
thread_was_awaken,
thread_id,
delayed_threads)

$acquire_mutex_check_if_priority_inheritance_needed_step(self) \triangleq$
 $\wedge pc[self] = \text{"acquire_mutex_check_if_p}$
 $\wedge \text{IF } Thread_Objects[owner_thread_id,$
 $Thread_Objects[thread_id_][self$
 $\text{THEN } \wedge pc' = [pc \text{ EXCEPT } ![self$
 $\text{ELSE } \wedge pc' = [pc \text{ EXCEPT } ![self$
 $\wedge \text{UNCHANGED } \langle HiRTOS,$
 $Thread_Objects,$
 $Mutex_Objects,$
 $Condvar_Objects,$
 $Timer_Objects,$
 $Global_Resource_Ava$
 $stack,$
 $thread_id_,$

mutex_id_,
waking_up_thread_after
owner_thread_id_,
thread_id_A,
mutex_id_A,
owner_thread_id,
thread_id_D,
mutex_id_D,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R,
mutex_id_R,
thread_id_Do,
condvar_id_,
mutex_id_Do,
thread_id_W,
condvar_id_W,
mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id,
condvar_id,
thread_was_awaken,
thread_id,
delayed_threads

$acquire_mutex_priority_inheritance_step(self) \triangleq$ $\wedge pc[self] = \text{"acquire_mutex_priority_inheritance_step"}$
 $\wedge Mutex_Objects' = [Mutex_Objects \text{ EXCEPT } ![mutex_id_A]]$
 $\wedge \text{IF } Thread_Objects[owner_thread_id_A].State = \text{"Failed to acquire mutex"}$
 $\quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"acquire_mutex_priority_inheritance_step"}]$
 $\quad \text{ELSE } \wedge \text{IF } Thread_Objects[owner_thread_id_A].State = \text{"Failed to reacquire mutex"}$
 $\quad \quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"acquire_mutex_priority_inheritance_step"}]$
 $\quad \quad \text{ELSE } \wedge Assert(Thread_Objects[owner_thread_id_A].State = \text{"Failed to reacquire mutex"})$
 $\quad \quad \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"acquire_mutex_priority_inheritance_step"}]$
 $\wedge \text{UNCHANGED } \langle HiRTOS,$
 $\quad Thread_Objects,$
 $\quad Condvar_Objects,$
 $\quad Timer_Objects,$
 $\quad Global_Resource_Available,$
 $\quad stack,$

$thread_id_$,
 $mutex_id_$,
 $waking_up_thread_after_condvar_wait$,
 $owner_thread_id_$,
 $thread_id_A$,
 $mutex_id_A$,
 $owner_thread_id$,
 $thread_id_D$,
 $mutex_id_D$,
 $doing_condvar_wait$,
 $awoken_thread_id_$,
 $thread_id_R$,
 $mutex_id_R$,
 $thread_id_Do$,
 $condvar_id_$,
 $mutex_id_Do$,
 $thread_id_W$,
 $condvar_id_W$,
 $mutex_id$,
 $condvar_id_D$,
 $do_context_switch$,
 $awoken_thread_id$,
 $to_reacquire_mutex_id$,
 $context_id_$,
 $condvar_id_S$,
 $context_id$,
 $condvar_id$,
 $thread_was_awaken$,
 $thread_id$,
 $delayed_threads$

$acquire_mutex_priority_inheritance_if_mutex_owner_runnable_step(self) \triangleq$
 $\wedge pc[self] = \text{"acquire_mutex_priority_inheritance_if_mutex_owner_runnable_step"}$
 $\wedge \text{Assert}((Thread_Objects[t] \neq self) \rightarrow \text{"Failure of assertion"})$
 $\wedge \wedge HiRTOS' = [HiRTOS]$
 $\wedge Thread_Objects' = [Thread_Objects]$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = pc]$
 $\wedge \text{UNCHANGED } \langle Mutex_Objects, Condvar_Objects, Timer_Objects, Global_Resources, stack, thread_id, mutex_id \rangle$

$$\begin{aligned} acquire_mutex_priority_inheritance_if_mutex_owner_blocked_on_mutex_step(self) \triangleq & \wedge pc[self] = \text{"acquire_mutex_priority_inheritance_if_mutex_owner_blocked_on_mutex_step"} \\ & \wedge Assert((Thread_ID \neq Mutex_Owner_ID) \vee \neg \text{"Failure on priority inheritance"}) \\ & \wedge \wedge Mutex_Object \end{aligned}$$

26

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$$\begin{aligned}
 \text{acquire_mutex_priority_inheritance_if_mutex_owner_blocked_on_condvar_step}(\text{self}) \triangleq & \wedge \text{pc}[\text{self}] = \text{"acq"} \\
 & \wedge \text{Assert}((\text{Thread_Obj} \neq \text{"Failure"} \\
 & \wedge \wedge \text{Mutex_Obj}
 \end{aligned}$$

$$\begin{aligned}
 & \wedge \text{Thread_Obj} \\
 & \wedge \text{pc}' = [\text{pc} \text{ EXCE} \\
 & \wedge \text{UNCHANGED } \langle .
 \end{aligned}$$

$$\begin{aligned}
\text{acquire_mutex_priority_inheritance_update_prio_step}(\text{self}) \triangleq & \wedge pc[\text{self}] = \text{"acquire_mutex_priority_inheritance_update_prio_step"} \\
& \wedge \text{Thread_Objects}' = [\text{Thread_Objects} \text{ EXCEPT } \text{self}] \\
& \wedge pc' = [pc \text{ EXCEPT } ![\text{self}] = \text{"acquire_mutex_priority_inheritance_update_prio_step"}] \\
& \wedge \text{UNCHANGED } \langle \text{HiRTOS}, \\
& \quad \text{Mutex_Objects}, \\
& \quad \text{Condvar_Objects}, \\
& \quad \text{Timer_Objects}, \\
& \quad \text{Global_Resource_Availability}, \\
& \quad \text{stack}, \\
& \quad \text{thread_id_}, \\
& \quad \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_critical_section_} \rangle
\end{aligned}$$

$acquire_mutex_check_if_synchronous_context_switch_needed_step(self) \triangleq$

$\wedge pc[self] = \text{"acquire_mutex_ch"}$
 $\wedge \text{IF } \neg wakening_up_thread_after$
 $\quad \text{THEN } \wedge pc' = [pc \text{ EXCE}]$
 $\quad \text{ELSE } \wedge pc' = [pc \text{ EXCE}]$
 $\wedge \text{UNCHANGED } \langle HiRTOS,$
 $\quad Thread_Object,$
 $\quad Mutex_Object,$
 $\quad Condvar_Obj,$
 $\quad Timer_Object,$
 $\quad Global_Resou$
 $\quad stack,$
 $\quad thread_id_,$
 $\quad mutex_id_,$
 $\quad wakening_up_th$
 $\quad owner_thread$
 $\quad thread_id_A,$
 $\quad mutex_id_A,$

$$\begin{aligned}
& \text{owner_thread_id_D,} \\
& \text{mutex_id_D,} \\
& \text{doing_condvar_wait_thread_id_D,} \\
& \text{awoken_thread_id_R,} \\
& \text{mutex_id_R,} \\
& \text{thread_id_Do,} \\
& \text{condvar_id_D,} \\
& \text{mutex_id_Do,} \\
& \text{thread_id_W,} \\
& \text{condvar_id_V,} \\
& \text{mutex_id_D,} \\
& \text{condvar_id_D,} \\
& \text{do_context_switch_thread_id_D,} \\
& \text{awoken_thread_id_R,} \\
& \text{to_reacquire_thread_id_D,} \\
& \text{context_id_D,} \\
& \text{condvar_id_S,} \\
& \text{context_id_D,} \\
& \text{condvar_id_D,} \\
& \text{thread_was_awoken_thread_id_D,} \\
& \text{thread_id_D,} \\
& \text{delayed_thread_id_D} \\
\end{aligned}$$

$$\begin{aligned}
\text{acquire_mutex_synchronous_context_switch_step}(self) &\triangleq \wedge pc[self] = \text{“acquire_mutex_synchronous_context_switch_step”} \\
&\wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure_id_D, \\
&\quad pc \\
&\quad \circ stack[sel \\
&\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{“check_time_slice”} \\
&\wedge \text{UNCHANGED } \langle HiRTOS, \\
&\quad Thread_Objects, \\
&\quad Mutex_Objects, \\
&\quad Condvar_Objects, \\
&\quad Timer_Objects, \\
&\quad Global_Resource_Available, \\
&\quad thread_id_D, \\
&\quad mutex_id_D, \\
&\quad waking_up_thread_after_condvar_wait_thread_id_D, \\
&\quad owner_thread_id_D, \\
&\quad thread_id_A, \\
&\quad mutex_id_A, \\
&\quad owner_thread_id_D, \\
&\quad thread_id_D, \\
&\quad mutex_id_D,
\end{aligned}$$

doing_condvar_wait,
awoken_thread_id_,
thread_id_R,
mutex_id_R,
thread_id_Do,
condvar_id_,
mutex_id_Do,
thread_id_W,
condvar_id_W,
mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id,
condvar_id,
thread_was_awaken,
thread_id,
delayed_threads

$do_acquire_mutex_return_step(self) \triangleq$ $\wedge pc[self] = \text{"do_acquire_mutex_return_step"}$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = Head(stack[self]).pc]$
 $\wedge owner_thread_id_ = [owner_thread_id_ \text{ EXCEPT } ![self] = Head(stack[self]).thread_id_]$
 $\wedge thread_id_ = [thread_id_ \text{ EXCEPT } ![self] = Head(stack[self]).thread_id_]$
 $\wedge mutex_id_ = [mutex_id_ \text{ EXCEPT } ![self] = Head(stack[self]).mutex_id_]$
 $\wedge waking_up_thread_after_condvar_wait' = [waking_up_thread_after_condvar_wait' \text{ EXCEPT } ![self] = Head(stack[self]).waking_up_thread_after_condvar_wait']$
 $\wedge stack' = [stack \text{ EXCEPT } ![self] = Tail(stack[self])]$
 $\wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects,$
 $\quad Mutex_Objects,$
 $\quad Condvar_Objects,$
 $\quad Timer_Objects,$
 $\quad Global_Resource_Available,$
 $\quad thread_id_A, mutex_id_A,$
 $\quad owner_thread_id,$
 $\quad thread_id_D, mutex_id_D,$
 $\quad doing_condvar_wait,$
 $\quad awoken_thread_id,$
 $\quad thread_id_R, mutex_id_R,$
 $\quad thread_id_Do,$
 $\quad condvar_id_, mutex_id_Do,$
 $\quad thread_id_W,$
 $\quad condvar_id_W, mutex_id,$
 $\quad condvar_id_D,$

$do_context_switch,$
 $awoken_thread_id,$
 $to_reacquire_mutex_id,$
 $context_id,$
 $condvar_id_S, context_id,$
 $condvar_id,$
 $thread_was_awaken,$
 $thread_id,$
 $delayed_threads\}$

$Do_Acquire_Mutex(self) \triangleq acquire_mutex_step_(\text{self})$
 $\quad \vee acquire_mutex_acquire_step(\text{self})$
 $\quad \vee acquire_mutex_make_condvar_wait_awoken_thread_runnable_step(\text{self})$
 $\quad \vee acquire_mutex_wait_on_mutex_step(\text{self})$
 $\quad \vee acquire_mutex_check_if_priority_inheritance_needed_step(\text{self})$
 $\quad \vee acquire_mutex_priority_inheritance_step(\text{self})$
 $\quad \vee acquire_mutex_priority_inheritance_if_mutex_owner_runnable_step(\text{self})$
 $\quad \vee acquire_mutex_priority_inheritance_if_mutex_owner_blocked_on_mutex_step(\text{self})$
 $\quad \vee acquire_mutex_priority_inheritance_if_mutex_owner_blocked_on_condvar_step(\text{self})$
 $\quad \vee acquire_mutex_priority_inheritance_update_prio_step(\text{self})$
 $\quad \vee acquire_mutex_check_if_synchronous_context_switch_needed_step(\text{self})$
 $\quad \vee acquire_mutex_synchronous_context_switch_step(\text{self})$
 $\quad \vee do_acquire_mutex_return_step(\text{self})$

$enter_critical_section_step_(\text{self}) \triangleq \wedge pc[\text{self}] = \text{"enter_critical_section_step_"} \wedge$
 $HiRTOS.Interrupts_Enabled \wedge$
 $(thread_id_A[\text{self}] \in Threads \Rightarrow$
 $\quad Thread_Objects[thread_id_A[\text{self}]].State = \text{"Running"})$
 $\wedge HiRTOS' = [HiRTOS \text{ EXCEPT } !.Interrupts_Enabled = \text{FALSE}]$
 $\wedge Assert(HiRTOS'.Current_Thread_Id = thread_id_A[\text{self}],$
 $\quad \text{"Failure of assertion at line 338, column 7."})$
 $\wedge \wedge mutex_id_\' = [mutex_id_ \text{ EXCEPT } ![self] = mutex_id_A[\text{self}]]$
 $\wedge stack\' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"Do_Acquire_Mutex"},$
 $\quad pc \mapsto \text{"exit_critical_section_step_"},$
 $\quad owner_thread_id_ \mapsto owner_thread_id_A[\text{self}],$
 $\quad thread_id_ \mapsto thread_id_A[\text{self}],$
 $\quad mutex_id_ \mapsto mutex_id_A[\text{self}],$
 $\quad waking_up_thread_after_condvar_wait_ \mapsto waking_up_thread_after_condvar_wait_A[\text{self}],$
 $\quad \circ stack[\text{self}]] \rangle]$
 $\wedge thread_id_\' = [thread_id_ \text{ EXCEPT } ![self] = thread_id_A[\text{self}]]$
 $\wedge waking_up_thread_after_condvar_wait\' = [waking_up_thread_after_condvar_wait_ \text{ EXCEPT } ![self] = \text{"Invalid_thread_id_"}]$
 $\wedge owner_thread_id_\' = [owner_thread_id_ \text{ EXCEPT } ![self] = \text{"Invalid_thread_id_"}]$
 $\wedge pc\' = [pc \text{ EXCEPT } ![self] = \text{"acquire_mutex_step_"}]$
 $\wedge \text{UNCHANGED } \langle Thread_Objects,$
 $\quad Mutex_Objects,$

Condvar_Objects,
Timer_Objects,
Global_Resource_Available,
thread_id_A, mutex_id_A,
owner_thread_id,
thread_id_D, mutex_id_D,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R, mutex_id_R,
thread_id_Do,
condvar_id_, mutex_id_Do,
thread_id_W,
condvar_id_W, mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S, context_id,
condvar_id,
thread_was_awaken,
thread_id,
delayed_threads

$exit_critical_section_step_(\text{self}) \triangleq$ $\wedge pc[\text{self}] = \text{"exit_critical_section_step_"} \\$
 $\wedge HiRTOS' = [HiRTOS \text{ EXCEPT } !.Interrupts_Enabled = \text{TRUE}] \\$
 $\wedge pc' = [pc \text{ EXCEPT } ![\text{self}] = \text{"acquire_mutex_return_step"}] \\$
 $\wedge \text{UNCHANGED } \langle Thread_Objects, \\$
 $Mutex_Objects, \\$
 $Condvar_Objects, \\$
 $Timer_Objects, \\$
 $Global_Resource_Available, \\$
 $stack, thread_id_, \\$
 $mutex_id_, \\$
 $waking_up_thread_after_condvar_wait, \\$
 $owner_thread_id_, \\$
 $thread_id_A, mutex_id_A, \\$
 $owner_thread_id, \\$
 $thread_id_D, mutex_id_D, \\$
 $doing_condvar_wait, \\$
 $awoken_thread_id_, \\$
 $thread_id_R, mutex_id_R, \\$
 $thread_id_Do, condvar_id_, \\$
 $mutex_id_Do, thread_id_W, \\$
 $condvar_id_W, mutex_id,$

$$\begin{aligned}
& \text{condvar_id_D,} \\
& \text{do_context_switch,} \\
& \text{awoken_thread_id,} \\
& \text{to_reacquire_mutex_id,} \\
& \text{context_id_}, \text{condvar_id_S,} \\
& \text{context_id, condvar_id,} \\
& \text{thread_was_awaken,} \\
& \text{thread_id,} \\
& \text{delayed_threads}) \\
\text{acquire_mutex_return_step}(self) & \triangleq \wedge pc[self] = \text{"acquire_mutex_return_step"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{Head}(\text{stack}[self]).pc] \\
& \wedge \text{owner_thread_id}' = [\text{owner_thread_id} \text{ EXCEPT } ![self] = \text{Head}(\text{stack}[self]).\text{thread_id_A}'] \\
& \wedge \text{thread_id_A}' = [\text{thread_id_A} \text{ EXCEPT } ![self] = \text{Head}(\text{stack}[self]).\text{thread_id_A}'] \\
& \wedge \text{mutex_id_A}' = [\text{mutex_id_A} \text{ EXCEPT } ![self] = \text{Head}(\text{stack}[self]).\text{mutex_id_A}'] \\
& \wedge \text{stack}' = [\text{stack} \text{ EXCEPT } ![self] = \text{Tail}(\text{stack}[self])] \\
& \wedge \text{UNCHANGED } \langle \text{HiRTOS, Thread_Objects,} \\
& \quad \text{Mutex_Objects,} \\
& \quad \text{Condvar_Objects,} \\
& \quad \text{Timer_Objects,} \\
& \quad \text{Global_Resource_Available,} \\
& \quad \text{thread_id_}, \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait,} \\
& \quad \text{owner_thread_id_}, \\
& \quad \text{thread_id_D, mutex_id_D,} \\
& \quad \text{doing_condvar_wait,} \\
& \quad \text{awoken_thread_id_}, \\
& \quad \text{thread_id_R, mutex_id_R,} \\
& \quad \text{thread_id_Do, condvar_id_}, \\
& \quad \text{mutex_id_Do, thread_id_W,} \\
& \quad \text{condvar_id_W, mutex_id,} \\
& \quad \text{condvar_id_D,} \\
& \quad \text{do_context_switch,} \\
& \quad \text{awoken_thread_id,} \\
& \quad \text{to_reacquire_mutex_id,} \\
& \quad \text{context_id_}, \text{condvar_id_S,} \\
& \quad \text{context_id, condvar_id,} \\
& \quad \text{thread_was_awaken,} \\
& \quad \text{thread_id, delayed_threads} \rangle \\
\text{Acquire_Mutex}(self) & \triangleq \text{enter_critical_section_step_}(self) \\
& \quad \vee \text{exit_critical_section_step_}(self) \\
& \quad \vee \text{acquire_mutex_return_step}(self) \\
\text{release_mutex_step_}(self) & \triangleq \wedge pc[self] = \text{"release_mutex_step_"} \\
& \quad \wedge \text{Assert}(\neg \text{HiRTOS.Interrupts_Enabled},
\end{aligned}$$

“Failure of assertion at line 350, column 7.”)
 $\wedge \text{Assert}(\text{Mutex_Objects}[\text{mutex_id_D}[\text{self}]].\text{Owner_Thread_Id} = \text{thread_id_D}[\text{self}])$
 “Failure of assertion at line 351, column 7.”)
 $\wedge \text{Assert}(\text{Thread_Objects}[\text{thread_id_D}[\text{self}]].\text{Owned_Mutexes} \neq \langle \rangle,$
 “Failure of assertion at line 352, column 7.”)
 $\wedge \text{Assert}(\text{Head}(\text{Thread_Objects}[\text{thread_id_D}[\text{self}]].\text{Owned_Mutexes}) = \text{mutex_id_D}[\text{self}])$
 “Failure of assertion at line 353, column 7.”)
 $\wedge \text{Thread_Objects}' = [\text{Thread_Objects} \text{ EXCEPT } ![\text{thread_id_D}[\text{self}]].\text{Owned_Mutexes}]$
 $\wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![\text{self}] = \text{“release_mutex_restore_priority_step”}]$
 $\wedge \text{UNCHANGED } \langle \text{HiRTOS}, \text{Mutex_Objects},$
 $\text{Condvar_Objects}, \text{Timer_Objects},$
 $\text{Global_Resource_Available}, \text{stack},$
 $\text{thread_id_}, \text{mutex_id_},$
 $\text{waking_up_thread_after_condvar_wait},$
 $\text{owner_thread_id_}, \text{thread_id_A},$
 $\text{mutex_id_A}, \text{owner_thread_id_},$
 $\text{thread_id_D}, \text{mutex_id_D},$
 $\text{doing_condvar_wait},$
 $\text{awoken_thread_id_}, \text{thread_id_R},$
 $\text{mutex_id_R}, \text{thread_id_Do},$
 $\text{condvar_id_}, \text{mutex_id_Do},$
 $\text{thread_id_W}, \text{condvar_id_W},$
 $\text{mutex_id_}, \text{condvar_id_D},$
 $\text{do_context_switch},$
 $\text{awoken_thread_id_},$
 $\text{to_reacquire_mutex_id_},$
 $\text{context_id_}, \text{condvar_id_S},$
 $\text{context_id_}, \text{condvar_id_},$
 $\text{thread_was_awaken}, \text{thread_id_},$
 $\text{delayed_threads} \rangle$

$\text{release_mutex_restore_priority_step}(\text{self}) \triangleq$

$\wedge \text{pc}[\text{self}] = \text{“release_mutex_restore_priority_step”}$
 $\wedge \text{IF } \text{Thread_Objects}[\text{thread_id_D}[\text{self}]].\text{Owned_Mutexes} \neq \langle \rangle$
 $\text{ THEN } \wedge \text{LET } \text{prev_mutex_obj} \triangleq \text{Mutex_Objects}[\text{Head}(\text{Thread_Objects}[\text{thread_id_D}[\text{self}]].\text{Owned_Mutexes})]$
 $\text{ IF } \text{prev_mutex_obj}.\text{Last_Inherited_Priority} < \text{thread_id_D}[\text{self}].\text{Priority}$
 $\text{ THEN } \wedge \text{Thread_Objects}' = [\text{Thread_Objects} \text{ EXCEPT } \text{prev_mutex_obj} \text{ AND } \text{thread_id_D}[\text{self}]]$
 $\text{ ELSE } \wedge \text{TRUE}$
 $\wedge \text{UNCHANGED } \text{Thread_Objects}$
 $\text{ ELSE } \wedge \text{Thread_Objects}' = [\text{Thread_Objects} \text{ EXCEPT } \text{thread_id_D}[\text{self}]]$
 $\wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![\text{self}] = \text{“release_mutex_check_if_mutex_held_by_self”}]$
 $\wedge \text{UNCHANGED } \langle \text{HiRTOS},$
 $\text{Mutex_Objects},$
 $\text{Condvar_Objects},$
 $\text{Timer_Objects},$
 $\text{Global_Resource_Available},$

stack, thread_id_,
mutex_id_,
waking_up_thread_after_condvar_wait,
owner_thread_id_,
thread_id_A,
mutex_id_A,
owner_thread_id,
thread_id_D,
mutex_id_D,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R,
mutex_id_R,
thread_id_Do,
condvar_id_,
mutex_id_Do,
thread_id_W,
condvar_id_W,
mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id,
condvar_id,
thread_was_awaken,
thread_id,
delayed_threads)

$release_mutex_check_if_mutex_waiters_step(self) \triangleq$ $\wedge pc[self] = \text{"release_mutex_check_if_mutex_waiters_s"}$
 $\wedge \text{IF } Is_Thread_Priority_Queue_Empty(Mutex_Obj$
 $\text{THEN } \wedge Mutex_Objects' = [Mutex_Objects \text{ EX}$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"do_releas$
 $\text{ELSE } \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"release_n}$
 $\wedge \text{UNCHANGED } Mutex_Objects$
 $\wedge \text{UNCHANGED } \langle HiRTOS,$
 Thread_Objects,
 Condvar_Objects,
 Timer_Objects,
 $\text{Global_Resource_Available,}$
 stack,
 thread_id_,
 mutex_id_,

$$\begin{aligned}
\text{release_mutex_wakeup_mutex_waiter_step}(self) &\triangleq \\
&\wedge pc[self] = \text{"release_mutex_wakeup_mutex_waiter_step"} \\
&\wedge \text{awoken_thread_id}' = [\text{awoken_thread_id} \text{ EXCEPT } ![\text{awoken_thread_id}]] \\
&\wedge \text{Assert}(\text{Thread_Objects}[\text{awoken_thread_id}'[self]].\text{Runnable}) \\
&\quad \text{"Failure of assertion at line 374, column 10."}) \\
&\wedge \wedge \text{HiRTOS}' = [\text{HiRTOS} \text{ EXCEPT } !.\text{Runnable_Thread_Objects}] \\
&\quad \wedge \text{Mutex_Objects}' = [\text{Mutex_Objects} \text{ EXCEPT } ![\text{mutex_waiter_id}]] \\
&\quad \wedge \wedge \text{Thread_Objects}' = [\text{Thread_Objects} \text{ EXCEPT } ![\text{awoken_thread_id}]] \\
&\quad \wedge \wedge \wedge \text{Condvar_Objects}' = [\text{Condvar_Objects} \text{ EXCEPT } ![\text{awoken_thread_id}]] \\
&\quad \wedge \wedge \wedge \text{Timer_Objects}' = [\text{Timer_Objects} \text{ EXCEPT } ![\text{awoken_thread_id}]] \\
&\quad \wedge \wedge \wedge \text{Global_Resource_Available}' = [\text{Global_Resource_Available} \text{ EXCEPT } ![\text{awoken_thread_id}]] \\
&\wedge \text{IF } \neg \text{doing_condvar_wait}[self] \\
&\quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT } ![\text{self}]] = \text{"release_mutex_wakeup_mutex_waiter_step"} \\
&\quad \text{ELSE } \wedge pc' = [pc \text{ EXCEPT } ![\text{self}]] = \text{"do_release_mutex_wakeup_mutex_waiter_step"} \\
&\wedge \text{UNCHANGED } \langle \text{Condvar_Objects}, \\
&\quad \text{Timer_Objects}, \\
&\quad \text{Global_Resource_Available},
\end{aligned}$$

stack,
thread_id_,
mutex_id_,
waking_up_thread_after_condvar_wait,
owner_thread_id_,
thread_id_A,
mutex_id_A,
owner_thread_id,
thread_id_D,
mutex_id_D,
doing_condvar_wait,
thread_id_R,
mutex_id_R,
thread_id_Do,
condvar_id_,
mutex_id_Do,
thread_id_W,
condvar_id_W,
mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id,
condvar_id,
thread_was_awaken,
thread_id,
delayed_threads

$release_mutex_synchronous_context_switch_step(self) \triangleq$ $\wedge pc[self] = \text{"release_mutex_synchronous_context_switch_step"}$
 $\wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure_name, pc, \dots] \circ stack[self] \rangle$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"check_time_slice_expired"}$
 $\wedge \text{UNCHANGED } \langle HiRTOS,$
 $\quad Thread_Objects,$
 $\quad Mutex_Objects,$
 $\quad Condvar_Objects,$
 $\quad Timer_Objects,$
 $\quad Global_Resource_Available,$
 $\quad thread_id_,$
 $\quad mutex_id_,$
 $\quad waking_up_thread_after_condvar_wait \rangle$

owner_thread_id_,
thread_id_A,
mutex_id_A,
owner_thread_id,
thread_id_D,
mutex_id_D,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R,
mutex_id_R,
thread_id_Do,
condvar_id_,
mutex_id_Do,
thread_id_W,
condvar_id_W,
mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id,
condvar_id,
thread_was_awaken,
thread_id,
delayed_threads

$do_release_mutex_return_step(self) \triangleq$ $\wedge pc[self] = \text{"do_release_mutex_return_step"}$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = Head(stack[self]).pc]$
 $\wedge awoken_thread_id_ ' = [awoken_thread_id_ \text{ EXCEPT } ![self] = Head(stack[self]).awoken_thread_id_]$
 $\wedge thread_id_D' = [thread_id_D \text{ EXCEPT } ![self] = Head(stack[self]).thread_id_D]$
 $\wedge mutex_id_D' = [mutex_id_D \text{ EXCEPT } ![self] = Head(stack[self]).mutex_id_D]$
 $\wedge doing_condvar_wait' = [doing_condvar_wait \text{ EXCEPT } ![self] = Head(stack[self]).doing_condvar_wait]$
 $\wedge stack' = [stack \text{ EXCEPT } ![self] = Tail(stack[self])]$
 $\wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects,$
 $\quad Mutex_Objects,$
 $\quad Condvar_Objects,$
 $\quad Timer_Objects,$
 $\quad Global_Resource_Available,$
 $\quad thread_id_ , mutex_id_ ,$
 $\quad waking_up_thread_after_condvar_wait,$
 $\quad owner_thread_id_ ,$
 $\quad thread_id_A, mutex_id_A,$
 $\quad owner_thread_id,$

$thread_id_R$, $mutex_id_R$,
 $thread_id_Do$,
 $condvar_id_$, $mutex_id_Do$,
 $thread_id_W$,
 $condvar_id_W$, $mutex_id$,
 $condvar_id_D$,
 $do_context_switch$,
 $awoken_thread_id$,
 $to_reacquire_mutex_id$,
 $context_id_$,
 $condvar_id_S$, $context_id$,
 $condvar_id$,
 $thread_was_awaken$,
 $thread_id$,
 $delayed_threads$)

$Do_Release_Mutex(self) \triangleq$ $release_mutex_step_ (self)$
 $\vee release_mutex_restore_priority_step (self)$
 $\vee release_mutex_check_if_mutex_waiters_step (self)$
 $\vee release_mutex_wakeup_mutex_waiter_step (self)$
 $\vee release_mutex_synchronous_context_switch_step (self)$
 $\vee do_release_mutex_return_step (self)$

$enter_critical_section_step_R(self) \triangleq$ $\wedge pc[self] = \text{"enter_critical_section_step_R"}$
 $\wedge HiRTOS.Interrupts_Enabled \wedge$
 $(thread_id_R[self] \in Threads \Rightarrow$
 $Thread_Objects[thread_id_R[self]].State = \text{"Running"})$
 $\wedge HiRTOS' = [HiRTOS \text{ EXCEPT } !.Interrupts_Enabled = \text{FALSE}]$
 $\wedge Assert(HiRTOS'.Current_Thread_Id = thread_id_R[self],$
 $\text{"Failure of assertion at line 399, column 7."})$
 $\wedge \wedge doing_condvar_wait' = [doing_condvar_wait \text{ EXCEPT } ![self] =$
 $\wedge mutex_id_D' = [mutex_id_D \text{ EXCEPT } ![self] = mutex_id_R[s$
 $\wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"Do_Release_}$
 $pc \mapsto \text{"exit_critical_}$
 $awoken_thread_id_ \mapsto aw$
 $thread_id_D \mapsto thread_id.$
 $mutex_id_D \mapsto mutex_id.$
 $doing_condvar_wait \mapsto do$
 $\circ stack[self]]$
 $\wedge thread_id_D' = [thread_id_D \text{ EXCEPT } ![self] = thread_id_R[s$
 $\wedge awoken_thread_id_ ' = [awoken_thread_id_ \text{ EXCEPT } ![self] = \text{"Inv$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"release_mutex_step_"}]$
 $\wedge \text{UNCHANGED } \langle Thread_Objects,$
 $Mutex_Objects,$
 $Condvar_Objects,$

Timer_Objects,
Global_Resource_Available,
thread_id_, mutex_id_,
waking_up_thread_after_condvar_wait,
owner_thread_id_,
thread_id_A, mutex_id_A,
owner_thread_id,
thread_id_R, mutex_id_R,
thread_id_Do,
condvar_id_,
mutex_id_Do,
thread_id_W,
condvar_id_W, mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id, condvar_id,
thread_was_awaken,
thread_id,
delayed_threads

$exit_critical_section_step_R(self) \triangleq$ $\wedge pc[self] = \text{"exit_critical_section_step_R"}$
 $\wedge HiRTOS' = [HiRTOS \text{ EXCEPT } !.Interrupts_Enabled = \text{TRUE}]$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"release_mutex_return_step"}]$
 $\wedge \text{UNCHANGED } \langle Thread_Objects,$
 $\quad Mutex_Objects,$
 $\quad Condvar_Objects,$
 $\quad Timer_Objects,$
 $\quad Global_Resource_Available,$
 $\quad stack, thread_id_,$
 $\quad mutex_id_,$
 $\quad waking_up_thread_after_condvar_wait,$
 $\quad owner_thread_id_,$
 $\quad thread_id_A, mutex_id_A,$
 $\quad owner_thread_id,$
 $\quad thread_id_D, mutex_id_D,$
 $\quad doing_condvar_wait,$
 $\quad awoken_thread_id_,$
 $\quad thread_id_R, mutex_id_R,$
 $\quad thread_id_Do,$
 $\quad condvar_id_, mutex_id_Do,$
 $\quad thread_id_W,$

$$\begin{aligned}
& \text{condvar_id_W, mutex_id,} \\
& \text{condvar_id_D,} \\
& \text{do_context_switch,} \\
& \text{awoken_thread_id,} \\
& \text{to_reacquire_mutex_id,} \\
& \text{context_id_}, \\
& \text{condvar_id_S, context_id,} \\
& \text{condvar_id,} \\
& \text{thread_was_awaken,} \\
& \text{thread_id,} \\
& \text{delayed_threads} \rangle \\
\text{release_mutex_return_step}(self) \triangleq & \wedge pc[self] = \text{"release_mutex_return_step"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{Head}(\text{stack}[self]).pc] \\
& \wedge \text{thread_id_R}' = [\text{thread_id_R} \text{ EXCEPT } ![self] = \text{Head}(\text{stack}[self]).th} \\
& \wedge \text{mutex_id_R}' = [\text{mutex_id_R} \text{ EXCEPT } ![self] = \text{Head}(\text{stack}[self]).m} \\
& \wedge \text{stack}' = [\text{stack} \text{ EXCEPT } ![self] = \text{Tail}(\text{stack}[self])] \\
& \wedge \text{UNCHANGED } \langle \text{HiRTOS, Thread_Objects,} \\
& \quad \text{Mutex_Objects,} \\
& \quad \text{Condvar_Objects,} \\
& \quad \text{Timer_Objects,} \\
& \quad \text{Global_Resource_Available,} \\
& \quad \text{thread_id_}, \text{ mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait,} \\
& \quad \text{owner_thread_id_}, \\
& \quad \text{thread_id_A, mutex_id_A,} \\
& \quad \text{owner_thread_id,} \\
& \quad \text{thread_id_D, mutex_id_D,} \\
& \quad \text{doing_condvar_wait,} \\
& \quad \text{awoken_thread_id_}, \\
& \quad \text{thread_id_Do, condvar_id_}, \\
& \quad \text{mutex_id_Do, thread_id_W,} \\
& \quad \text{condvar_id_W, mutex_id,} \\
& \quad \text{condvar_id_D,} \\
& \quad \text{do_context_switch,} \\
& \quad \text{awoken_thread_id,} \\
& \quad \text{to_reacquire_mutex_id,} \\
& \quad \text{context_id_}, \text{ condvar_id_S,} \\
& \quad \text{context_id, condvar_id,} \\
& \quad \text{thread_was_awaken,} \\
& \quad \text{thread_id, delayed_threads} \rangle \\
\text{Release_Mutex}(self) \triangleq & \text{enter_critical_section_step_R}(self) \\
& \vee \text{exit_critical_section_step_R}(self) \\
& \vee \text{release_mutex_return_step}(self)
\end{aligned}$$

$$\begin{aligned}
wait_on_condvar_wait_step(self) &\triangleq \wedge pc[self] = \text{"wait_on_condvar_wait_step"} \\
&\wedge Assert(\neg HiRTOS.Interrupts_Enabled, \\
&\quad \text{"Failure of assertion at line 410, column 7."}) \\
&\wedge \wedge Condvar_Objects' = [Condvar_Objects \text{ EXCEPT } ![condvar_id_S] \\
&\quad \wedge Thread_Objects' = [Thread_Objects \text{ EXCEPT } ![thread_id_Do[sel \\
&\hspace{18em}![thread_id_Do[sel \\
&\hspace{18em}![thread_id_Do[sel \\
&\wedge HiRTOS' = [HiRTOS \text{ EXCEPT } !.Current_Thread_Id = \text{"Invalid_T"} \\
&\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"wait_on_condvar_release_mutex_step"}] \\
&\wedge UNCHANGED \langle Mutex_Objects, \\
&\quad Timer_Objects, \\
&\quad Global_Resource_Available, \\
&\quad stack, thread_id__, \\
&\quad mutex_id__, \\
&\quad waking_up_thread_after_condvar_wait, \\
&\quad owner_thread_id__, \\
&\quad thread_id_A, mutex_id_A, \\
&\quad owner_thread_id, \\
&\quad thread_id_D, mutex_id_D, \\
&\quad doing_condvar_wait, \\
&\quad awoken_thread_id__, \\
&\quad thread_id_R, mutex_id_R, \\
&\quad thread_id_Do, condvar_id__, \\
&\quad mutex_id_Do, thread_id_W, \\
&\quad condvar_id_W, mutex_id, \\
&\quad condvar_id_D, \\
&\quad do_context_switch, \\
&\quad awoken_thread_id, \\
&\quad to_reacquire_mutex_id, \\
&\quad context_id__, condvar_id_S, \\
&\quad context_id, condvar_id, \\
&\quad thread_was_awaken, \\
&\quad thread_id, delayed_threads \rangle \\
\\
wait_on_condvar_release_mutex_step(self) &\triangleq \wedge pc[self] = \text{"wait_on_condvar_release_mutex_step"} \\
&\wedge IF mutex_id_Do[self] \neq \text{"Invalid_Mutex_Id"} \\
&\quad THEN \wedge \wedge doing_condvar_wait' = [doing_condvar_u \\
&\hspace{6em}\wedge mutex_id_D' = [mutex_id_D \text{ EXCEPT } ![s \\
&\hspace{6em}\wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [proced \\
&\hspace{18em}pc \\
&\hspace{18em}awoke \\
&\hspace{18em}thread \\
&\hspace{18em}mutex \\
&\hspace{18em}doing- \\
&\hspace{18em}\circ stack
\end{aligned}$$

$$\begin{aligned}
& \wedge \text{thread_id_}D' = [\text{thread_id_}D \text{ EXCEPT } ![self]] \\
& \wedge \text{awoken_thread_id_}' = [\text{awoken_thread_id_}] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self]] = \text{"release_mutex_s"} \\
\text{ELSE } & \wedge pc' = [pc \text{ EXCEPT } ![self]] = \text{"wait_on_condvar_s"} \\
& \wedge \text{UNCHANGED } \langle \text{stack,} \\
& \quad \text{thread_id_}D, \\
& \quad \text{mutex_id_}D, \\
& \quad \text{doing_condvar_wait,} \\
& \quad \text{awoken_thread_id_} \rangle \\
& \wedge \text{UNCHANGED } \langle \text{HiRTOS,} \\
& \quad \text{Thread_Objects,} \\
& \quad \text{Mutex_Objects,} \\
& \quad \text{Condvar_Objects,} \\
& \quad \text{Timer_Objects,} \\
& \quad \text{Global_Resource_Available,} \\
& \quad \text{thread_id_}, \\
& \quad \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait,} \\
& \quad \text{owner_thread_id_}, \\
& \quad \text{thread_id_}A, \\
& \quad \text{mutex_id_}A, \\
& \quad \text{owner_thread_id,} \\
& \quad \text{thread_id_}R, \\
& \quad \text{mutex_id_}R, \\
& \quad \text{thread_id_}Do, \\
& \quad \text{condvar_id_}, \\
& \quad \text{mutex_id_}Do, \\
& \quad \text{thread_id_}W, \\
& \quad \text{condvar_id_}W, \\
& \quad \text{mutex_id,} \\
& \quad \text{condvar_id_}D, \\
& \quad \text{do_context_switch,} \\
& \quad \text{awoken_thread_id,} \\
& \quad \text{to_reacquire_mutex_id,} \\
& \quad \text{context_id_}, \\
& \quad \text{condvar_id_}S, \\
& \quad \text{context_id,} \\
& \quad \text{condvar_id,} \\
& \quad \text{thread_was_awaken,} \\
& \quad \text{thread_id,} \\
& \quad \text{delayed_threads} \rangle \\
\text{wait_on_condvar_synchronous_context_switch_step}(self) & \triangleq \wedge pc[self] = \text{"wait_on_condvar_synchronous_co"} \\
& \wedge \text{stack}' = [\text{stack} \text{ EXCEPT } ![self]] = \langle [\text{procedu} \\
& \quad pc
\end{aligned}$$

$$\begin{aligned}
& \circ stack[\\
\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"check_time_sli} \\
\wedge \text{UNCHANGED } \langle & \text{HiRTOS,} \\
& \text{Thread_Objects,} \\
& \text{Mutex_Objects,} \\
& \text{Condvar_Objects,} \\
& \text{Timer_Objects,} \\
& \text{Global_Resource_Available,} \\
& \text{thread_id_}, \\
& \text{mutex_id_}, \\
& \text{waking_up_thread_after_com} \\
& \text{owner_thread_id_}, \\
& \text{thread_id_A,} \\
& \text{mutex_id_A,} \\
& \text{owner_thread_id,} \\
& \text{thread_id_D,} \\
& \text{mutex_id_D,} \\
& \text{doing_condvar_wait,} \\
& \text{awoken_thread_id_}, \\
& \text{thread_id_R,} \\
& \text{mutex_id_R,} \\
& \text{thread_id_Do,} \\
& \text{condvar_id_}, \\
& \text{mutex_id_Do,} \\
& \text{thread_id_W,} \\
& \text{condvar_id_W,} \\
& \text{mutex_id,} \\
& \text{condvar_id_D,} \\
& \text{do_context_switch,} \\
& \text{awoken_thread_id,} \\
& \text{to_reacquire_mutex_id,} \\
& \text{context_id_}, \\
& \text{condvar_id_S,} \\
& \text{context_id,} \\
& \text{condvar_id,} \\
& \text{thread_was_awaken,} \\
& \text{thread_id,} \\
& \text{delayed_threads} \rangle
\end{aligned}$$

$$\begin{aligned}
do_wait_on_condvar_return_step(self) &\triangleq \wedge pc[self] = \text{"do_wait_on_condvar_return_step"} \\
&\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{Head}(stack[self]).pc] \\
&\wedge thread_id_Do' = [thread_id_Do \text{ EXCEPT } ![self] = \text{Head}(stack \\
&\wedge condvar_id_ ' = [condvar_id_ \text{ EXCEPT } ![self] = \text{Head}(stack \\
&\wedge mutex_id_Do' = [mutex_id_Do \text{ EXCEPT } ![self] = \text{Head}(stack \\
&\wedge stack' = [stack \text{ EXCEPT } ![self] = \text{Tail}(stack[self])]
\end{aligned}$$

\wedge UNCHANGED \langle *HiRTOS*, *Thread_Objects*,
Mutex_Objects,
Condvar_Objects,
Timer_Objects,
Global_Resource_Available,
thread_id_, *mutex_id_*,
waking_up_thread_after_condvar_wait,
owner_thread_id_,
thread_id_A,
mutex_id_A,
owner_thread_id,
thread_id_D,
mutex_id_D,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R,
mutex_id_R,
thread_id_W,
condvar_id_W, *mutex_id*,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id, *condvar_id*,
thread_was_awaken,
thread_id,
delayed_threads \rangle

$Do_Wait_On_Condvar(self) \triangleq$ *wait_on_condvar_wait_step*(*self*)
 \vee *wait_on_condvar_release_mutex_step*(*self*)
 \vee *wait_on_condvar_synchronous_context_switch_step*(*self*)
 \vee *do_wait_on_condvar_return_step*(*self*)

$enter_critical_section_step_W(self) \triangleq$ \wedge *pc*[*self*] = "enter_critical_section_step_W"
 \wedge *HiRTOS.Interrupts_Enabled* \wedge
 $(thread_id_W[self] \in Threads \Rightarrow$
 $\quad Thread_Objects[thread_id_W[self]].State = "Running")$
 \wedge *HiRTOS'* = [*HiRTOS* EXCEPT !*Interrupts_Enabled* = FALSE]
 \wedge \wedge *condvar_id_*' = [*condvar_id* EXCEPT ![*self*] = *condvar*]
 \wedge *mutex_id_Do*' = [*mutex_id_Do* EXCEPT ![*self*] = *mutex_id*]
 \wedge *stack*' = [*stack* EXCEPT ![*self*] = \langle [*procedure* \mapsto "Do_Wait_On_Condvar"
 $\quad pc \mapsto$ "exit_critical_section"
 $\quad thread_id_Do \mapsto thread_id_W[self]$ \rangle

$$\begin{aligned}
& \text{condvar_id_} \mapsto \text{condvar_} \\
& \text{mutex_id_Do} \mapsto \text{mutex_} \\
& \quad \circ \text{stack[self]} \\
& \wedge \text{thread_id_Do}' = [\text{thread_id_Do} \text{ EXCEPT } ![\text{self}] = \text{thread_id_}] \\
& \wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![\text{self}] = \text{"wait_on_condvar_wait_step"}] \\
& \wedge \text{UNCHANGED } \langle \text{Thread_Objects,} \\
& \quad \text{Mutex_Objects,} \\
& \quad \text{Condvar_Objects,} \\
& \quad \text{Timer_Objects,} \\
& \quad \text{Global_Resource_Available,} \\
& \quad \text{thread_id_}, \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait,} \\
& \quad \text{owner_thread_id_}, \\
& \quad \text{thread_id_A, mutex_id_A,} \\
& \quad \text{owner_thread_id,} \\
& \quad \text{thread_id_D, mutex_id_D,} \\
& \quad \text{doing_condvar_wait,} \\
& \quad \text{awoken_thread_id_}, \\
& \quad \text{thread_id_R, mutex_id_R,} \\
& \quad \text{thread_id_W,} \\
& \quad \text{condvar_id_W, mutex_id,} \\
& \quad \text{condvar_id_D,} \\
& \quad \text{do_context_switch,} \\
& \quad \text{awoken_thread_id,} \\
& \quad \text{to_reacquire_mutex_id,} \\
& \quad \text{context_id_}, \\
& \quad \text{condvar_id_S,} \\
& \quad \text{context_id, condvar_id,} \\
& \quad \text{thread_was_awaken,} \\
& \quad \text{thread_id,} \\
& \quad \text{delayed_threads} \rangle \\
& \text{exit_critical_section_step_W}(self) \triangleq \wedge \text{pc[self]} = \text{"exit_critical_section_step_W"} \\
& \wedge \text{HiRTOS}' = [\text{HiRTOS} \text{ EXCEPT } !.\text{Interrupts_Enabled} = \text{TRUE}] \\
& \wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![\text{self}] = \text{"wait_on_condvar_return_step"}] \\
& \wedge \text{UNCHANGED } \langle \text{Thread_Objects,} \\
& \quad \text{Mutex_Objects,} \\
& \quad \text{Condvar_Objects,} \\
& \quad \text{Timer_Objects,} \\
& \quad \text{Global_Resource_Available,} \\
& \quad \text{stack, thread_id_}, \\
& \quad \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait,} \\
& \quad \text{owner_thread_id_}, \\
& \quad \text{thread_id_A, mutex_id_A,}
\end{aligned}$$

owner_thread_id,
thread_id_D, mutex_id_D,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R, mutex_id_R,
thread_id_Do,
condvar_id_, mutex_id_Do,
thread_id_W,
condvar_id_W, mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S, context_id,
condvar_id,
thread_was_awaken,
thread_id,
delayed_threads)

$wait_on_condvar_return_step(self) \triangleq$ $\wedge pc[self] = \text{"wait_on_condvar_return_step"}$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = Head(stack[self]).pc]$
 $\wedge thread_id_W' = [thread_id_W \text{ EXCEPT } ![self] = Head(stack[self]).thread_id_W]$
 $\wedge condvar_id_W' = [condvar_id_W \text{ EXCEPT } ![self] = Head(stack[self]).condvar_id_W]$
 $\wedge mutex_id' = [mutex_id \text{ EXCEPT } ![self] = Head(stack[self]).mutex_id]$
 $\wedge stack' = [stack \text{ EXCEPT } ![self] = Tail(stack[self])]$
 $\wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects,$
 $\quad Mutex_Objects,$
 $\quad Condvar_Objects,$
 $\quad Timer_Objects,$
 $\quad Global_Resource_Available,$
 $\quad thread_id_, mutex_id_,$
 $\quad waking_up_thread_after_condvar_wait,$
 $\quad owner_thread_id_,$
 $\quad thread_id_A, mutex_id_A,$
 $\quad owner_thread_id,$
 $\quad thread_id_D, mutex_id_D,$
 $\quad doing_condvar_wait,$
 $\quad awoken_thread_id_,$
 $\quad thread_id_R, mutex_id_R,$
 $\quad thread_id_Do, condvar_id_,$
 $\quad mutex_id_Do, condvar_id_D,$
 $\quad do_context_switch,$
 $\quad awoken_thread_id,$
 $\quad to_reacquire_mutex_id,$

$context_id_$, $condvar_id_S$,
 $context_id$, $condvar_id$,
 $thread_was_awaken$,
 $thread_id$,
 $delayed_threads$

$Wait_On_Condvar(self) \triangleq enter_critical_section_step_W(self)$
 $\quad \vee exit_critical_section_step_W(self)$
 $\quad \vee wait_on_condvar_return_step(self)$

$signal_condvar_step(self) \triangleq \wedge pc[self] = \text{"signal_condvar_step"}$
 $\wedge Assert(\neg HiRTOS.Interrupts_Enabled,$
 $\quad \text{"Failure of assertion at line 446, column 7."})$
 $\wedge IF \neg Is_Thread_Priority_Queue_Empty(Condvar_Objects[condvar_id_D[sel]$
 $\quad THEN \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"signal_condvar_wakeup_waiter_step"}$
 $\quad ELSE \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"do_condvar_signal_return_step"}$
 $\wedge UNCHANGED \langle HiRTOS, Thread_Objects,$
 $\quad Mutex_Objects, Condvar_Objects,$
 $\quad Timer_Objects,$
 $\quad Global_Resource_Available, stack,$
 $\quad thread_id_$, $mutex_id_$,
 $\quad waking_up_thread_after_condvar_wait,$
 $\quad owner_thread_id_$, $thread_id_A$,
 $\quad mutex_id_A$, $owner_thread_id$,
 $\quad thread_id_D$, $mutex_id_D$,
 $\quad doing_condvar_wait,$
 $\quad awoken_thread_id_$, $thread_id_R$,
 $\quad mutex_id_R$, $thread_id_Do$,
 $\quad condvar_id_$, $mutex_id_Do$,
 $\quad thread_id_W$, $condvar_id_W$,
 $\quad mutex_id$, $condvar_id_D$,
 $\quad do_context_switch,$
 $\quad awoken_thread_id$,
 $\quad to_reacquire_mutex_id$,
 $\quad context_id_$, $condvar_id_S$,
 $\quad context_id$, $condvar_id$,
 $\quad thread_was_awaken$, $thread_id$,
 $\quad delayed_threads \rangle$

$signal_condvar_wakeup_waiter_step(self) \triangleq \wedge pc[self] = \text{"signal_condvar_wakeup_waiter_step"}$
 $\wedge awoken_thread_id' = [awoken_thread_id \text{ EXCEPT } ![self] =$
 $\wedge Condvar_Objects' = [Condvar_Objects \text{ EXCEPT } ![condvar$
 $\wedge Assert(awoken_thread_id'[self] \neq HiRTOS.Current_Thre$
 $\quad \text{"Failure of assertion at line 454, column 10."})$
 $\wedge Assert(Thread_Objects[awoken_thread_id'[self]].Waiting-$
 $\quad \text{"Failure of assertion at line 455, column 10."})$

$$\begin{aligned}
& \wedge \text{Assert}(\text{Thread_Objects}[\text{awoken_thread_id}'[self]].\text{Waiting_} \\
& \quad \text{"Failure of assertion at line 456, column 10."}) \\
& \wedge \text{to_reacquire_mutex_id}' = [\text{to_reacquire_mutex_id} \text{ EXCEPT } ![\text{awoken_thread_id}]] \\
& \wedge \text{Thread_Objects}' = [\text{Thread_Objects} \text{ EXCEPT } ![\text{awoken_thread_id}]] \\
& \wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![\text{self}]] = \text{"signal_condvar_check_if_mutex_"} \\
& \wedge \text{UNCHANGED } \langle \text{HiRTOS}, \\
& \quad \text{Mutex_Objects}, \\
& \quad \text{Timer_Objects}, \\
& \quad \text{Global_Resource_Available}, \\
& \quad \text{stack}, \text{thread_id_}, \\
& \quad \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait}, \\
& \quad \text{owner_thread_id_}, \\
& \quad \text{thread_id_A}, \\
& \quad \text{mutex_id_A}, \\
& \quad \text{owner_thread_id}, \\
& \quad \text{thread_id_D}, \\
& \quad \text{mutex_id_D}, \\
& \quad \text{doing_condvar_wait}, \\
& \quad \text{awoken_thread_id_}, \\
& \quad \text{thread_id_R}, \\
& \quad \text{mutex_id_R}, \\
& \quad \text{thread_id_Do}, \\
& \quad \text{condvar_id_}, \\
& \quad \text{mutex_id_Do}, \\
& \quad \text{thread_id_W}, \\
& \quad \text{condvar_id_W}, \\
& \quad \text{mutex_id}, \\
& \quad \text{condvar_id_D}, \\
& \quad \text{do_context_switch}, \\
& \quad \text{context_id_}, \\
& \quad \text{condvar_id_S}, \\
& \quad \text{context_id}, \\
& \quad \text{condvar_id}, \\
& \quad \text{thread_was_awaken}, \\
& \quad \text{thread_id}, \\
& \quad \text{delayed_threads} \rangle \\
& \text{signal_condvar_check_if_mutex_reacquire_needed_step}(self) \triangleq \wedge \text{pc}[self] = \text{"signal_condvar_check_if_mutex_"} \\
& \quad \wedge \text{IF } \text{to_reacquire_mutex_id}[self] \neq \text{"Invalid"} \\
& \quad \quad \text{THEN } \wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![\text{self}]] = \text{"signal_condvar_check_if_mutex_"} \\
& \quad \quad \text{ELSE } \wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![\text{self}]] = \text{"signal_condvar_check_if_mutex_"} \\
& \quad \wedge \text{UNCHANGED } \langle \text{HiRTOS}, \\
& \quad \quad \text{Thread_Objects},
\end{aligned}$$

Mutex_Objects,
Condvar_Objects,
Timer_Objects,
Global_Resource_Availab
stack,
thread_id_,
mutex_id_,
waking_up_thread_after_
owner_thread_id_,
thread_id_A,
mutex_id_A,
owner_thread_id,
thread_id_D,
mutex_id_D,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R,
mutex_id_R,
thread_id_Do,
condvar_id_,
mutex_id_Do,
thread_id_W,
condvar_id_W,
mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id,
condvar_id,
thread_was_awaken,
thread_id,
delayed_threads

$$\begin{aligned}
\text{signal_condvar_reacquire_mutex_step}(self) \triangleq & \wedge pc[self] = \text{"signal_condvar_reacquire_mutex_step"} \\
& \wedge \wedge mutex_id_ ' = [mutex_id_ \text{ EXCEPT } ![self] = to_reacquire_mutex_id_] \\
& \wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"Do_context_switch"}, \\
& \quad pc \mapsto \text{"signal_condvar_reacquire_mutex_step"}, \\
& \quad owner_thread_id_ \mapsto owner_thread_id_ , \\
& \quad thread_id_ \mapsto thread_id_ , \\
& \quad mutex_id_ \mapsto mutex_id_ , \\
& \quad waking_up_thread_after_owner_thread_id_ \mapsto waking_up_thread_after_owner_thread_id_ , \\
& \quad \circ stack[self]] \rangle
\end{aligned}$$

$$\begin{aligned}
& \wedge \text{thread_id_}' = [\text{thread_id_} \text{ EXCEPT } ![self] = \text{awoken_thread_id_}] \\
& \wedge \text{waking_up_thread_after_condvar_wait}' = [\text{waking_up_thread_after_condvar_wait_}] \\
& \wedge \text{owner_thread_id_}' = [\text{owner_thread_id_} \text{ EXCEPT } ![self]] \\
& \wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![self] = \text{"acquire_mutex_step_"}] \\
& \wedge \text{UNCHANGED } \langle \text{HiRTOS}, \\
& \quad \text{Thread_Objects}, \\
& \quad \text{Mutex_Objects}, \\
& \quad \text{Condvar_Objects}, \\
& \quad \text{Timer_Objects}, \\
& \quad \text{Global_Resource_Available}, \\
& \quad \text{thread_id_A}, \\
& \quad \text{mutex_id_A}, \\
& \quad \text{owner_thread_id}, \\
& \quad \text{thread_id_D}, \\
& \quad \text{mutex_id_D}, \\
& \quad \text{doing_condvar_wait}, \\
& \quad \text{awoken_thread_id_}, \\
& \quad \text{thread_id_R}, \\
& \quad \text{mutex_id_R}, \\
& \quad \text{thread_id_Do}, \\
& \quad \text{condvar_id_}, \\
& \quad \text{mutex_id_Do}, \\
& \quad \text{thread_id_W}, \\
& \quad \text{condvar_id_W}, \\
& \quad \text{mutex_id}, \\
& \quad \text{condvar_id_D}, \\
& \quad \text{do_context_switch}, \\
& \quad \text{awoken_thread_id}, \\
& \quad \text{to_reacquire_mutex_id}, \\
& \quad \text{context_id_}, \\
& \quad \text{condvar_id_S}, \\
& \quad \text{context_id}, \\
& \quad \text{condvar_id}, \\
& \quad \text{thread_was_awaken}, \\
& \quad \text{thread_id}, \\
& \quad \text{delayed_threads} \rangle
\end{aligned}$$

$$\begin{aligned}
\text{signal_condvar_awoken_thread_runnable_step}(self) & \triangleq \wedge \text{pc}[self] = \text{"signal_condvar_awoken_thread_runnable_step_"} \\
& \wedge \wedge \text{HiRTOS}' = [\text{HiRTOS} \text{ EXCEPT } !. \text{Runnable_Thread_id_}] \\
& \wedge \text{Thread_Objects}' = [\text{Thread_Objects} \text{ EXCEPT } !. \text{Thread_id_}] \\
& \wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![self] = \text{"signal_condvar_check_"}] \\
& \wedge \text{UNCHANGED } \langle \text{Mutex_Objects}, \\
& \quad \text{Condvar_Objects}, \\
& \quad \text{Timer_Objects}, \\
& \quad \text{Global_Resource_Available},
\end{aligned}$$

```

stack,
thread_id_,
mutex_id_,
waking_up_thread_after_condvar_,
owner_thread_id_,
thread_id_A,
mutex_id_A,
owner_thread_id,
thread_id_D,
mutex_id_D,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R,
mutex_id_R,
thread_id_Do,
condvar_id_,
mutex_id_Do,
thread_id_W,
condvar_id_W,
mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id,
condvar_id,
thread_was_awaken,
thread_id,
delayed_threads>

```

```

signal_condvar_check_if_sync_context_switch_needed_step(self)  $\triangleq$   $\wedge$  pc[self] = "signal_condvar_check_if_
 $\wedge$  IF do_context_switch[self]
    THEN  $\wedge$  pc' = [pc EXCEPT ![sel
    ELSE  $\wedge$  pc' = [pc EXCEPT ![sel
 $\wedge$  UNCHANGED  $\langle$ HiRTOS,
    Thread_Objects,
    Mutex_Objects,
    Condvar_Objects,
    Timer_Objects,
    Global_Resource_Av
    stack,
    thread_id_,
    mutex_id_,

```


mutex_id_A,
owner_thread_id,
thread_id_D,
mutex_id_D,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R,
mutex_id_R,
thread_id_Do,
condvar_id_,
mutex_id_Do,
thread_id_W,
condvar_id_W,
mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id,
condvar_id,
thread_was_awaken,
thread_id,
delayed_threads)

$do_condvar_signal_return_step(self) \triangleq$ $\wedge pc[self] = \text{"do_condvar_signal_return_step"}$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = Head(stack[self]).pc]$
 $\wedge awoken_thread_id' = [awoken_thread_id \text{ EXCEPT } ![self] = Head(stack[self]).awoken_thread_id]$
 $\wedge to_reacquire_mutex_id' = [to_reacquire_mutex_id \text{ EXCEPT } ![self] = Head(stack[self]).to_reacquire_mutex_id]$
 $\wedge condvar_id_D' = [condvar_id_D \text{ EXCEPT } ![self] = Head(stack[self]).condvar_id_D]$
 $\wedge do_context_switch' = [do_context_switch \text{ EXCEPT } ![self] = Head(stack[self]).do_context_switch]$
 $\wedge stack' = [stack \text{ EXCEPT } ![self] = Tail(stack[self])]$
 $\wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects,$
 $\quad Mutex_Objects,$
 $\quad Condvar_Objects,$
 $\quad Timer_Objects,$
 $\quad Global_Resource_Available,$
 $\quad thread_id_, mutex_id_,$
 $\quad waking_up_thread_after_condvar_wait,$
 $\quad owner_thread_id_,$
 $\quad thread_id_A, mutex_id_A,$
 $\quad owner_thread_id,$
 $\quad thread_id_D, mutex_id_D,$
 $\quad doing_condvar_wait,$

$awoken_thread_id_,$
 $thread_id_R, mutex_id_R,$
 $thread_id_Do,$
 $condvar_id_,$
 $mutex_id_Do,$
 $thread_id_W,$
 $condvar_id_W, mutex_id,$
 $context_id_,$
 $condvar_id_S,$
 $context_id, condvar_id,$
 $thread_was_awaken,$
 $thread_id,$
 $delayed_threads\}$

$$\begin{aligned}
Do_Signal_Condvar(self) &\triangleq signal_condvar_step(self) \\
&\quad \vee signal_condvar_wakeup_waiter_step(self) \\
&\quad \vee signal_condvar_check_if_mutex_reacquire_needed_step(self) \\
&\quad \vee signal_condvar_reacquire_mutex_step(self) \\
&\quad \vee signal_condvar_awoken_thread_runnable_step(self) \\
&\quad \vee signal_condvar_check_if_sync_context_switch_needed_step(self) \\
&\quad \vee signal_condvar_synchronous_context_switch_step(self) \\
&\quad \vee do_condvar_signal_return_step(self) \\
enter_critical_section_step_S(self) &\triangleq \wedge pc[self] = \text{"enter_critical_section_step_S"} \\
&\quad \wedge HiRTOS.Interrupts_Enabled \wedge \\
&\quad (context_id_ [self] \in Threads \Rightarrow \\
&\quad \quad Thread_Objects[context_id_ [self]].State = \text{"Running"}) \\
&\quad \wedge HiRTOS' = [HiRTOS \text{ EXCEPT } !Interrupts_Enabled = \text{FALSE}] \\
&\quad \wedge \wedge condvar_id_D' = [condvar_id_D \text{ EXCEPT } ![self] = condvar_id_D] \\
&\quad \quad \wedge do_context_switch' = [do_context_switch \text{ EXCEPT } ![self] = TR] \\
&\quad \quad \wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"Do_Signal_C"}, \\
&\quad \quad \quad pc \mapsto \text{"exit_critical_section"}, \\
&\quad \quad \quad awoken_thread_id \mapsto awoken_thread_id, \\
&\quad \quad \quad to_reacquire_mutex_id \mapsto to_reacquire_mutex_id, \\
&\quad \quad \quad condvar_id_D \mapsto condvar_id_D, \\
&\quad \quad \quad do_context_switch \mapsto do_context_switch, \\
&\quad \quad \quad \circ stack[self] \rangle] \\
&\quad \wedge awoken_thread_id' = [awoken_thread_id \text{ EXCEPT } ![self] = \text{"Invalid"}] \\
&\quad \wedge to_reacquire_mutex_id' = [to_reacquire_mutex_id \text{ EXCEPT } ![self] = \text{"Invalid"}] \\
&\quad \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"signal_condvar_step"}] \\
&\quad \wedge \text{UNCHANGED } \langle Thread_Objects, \\
&\quad \quad Mutex_Objects, \\
&\quad \quad Condvar_Objects, \\
&\quad \quad Timer_Objects, \\
&\quad \quad Global_Resource_Available,
\end{aligned}$$

$thread_id_$, $mutex_id_$,
 $waking_up_thread_after_condvar_wait$,
 $owner_thread_id_$,
 $thread_id_A$, $mutex_id_A$,
 $owner_thread_id$,
 $thread_id_D$, $mutex_id_D$,
 $doing_condvar_wait$,
 $awoken_thread_id_$,
 $thread_id_R$, $mutex_id_R$,
 $thread_id_Do$,
 $condvar_id_$,
 $mutex_id_Do$,
 $thread_id_W$,
 $condvar_id_W$, $mutex_id$,
 $context_id_$,
 $condvar_id_S$,
 $context_id$, $condvar_id$,
 $thread_was_awaken$,
 $thread_id$,
 $delayed_threads$

$exit_critical_section_step_S(self) \triangleq$ $\wedge pc[self] = \text{"exit_critical_section_step_S"}$
 $\wedge HiRTOS' = [HiRTOS \text{ EXCEPT } !.Interrupts_Enabled = \text{TRUE}]$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"condvar_signaled_step"}]$
 $\wedge \text{UNCHANGED } \langle Thread_Objects,$
 $Mutex_Objects,$
 $Condvar_Objects,$
 $Timer_Objects,$
 $Global_Resource_Available,$
 $stack, thread_id_$,
 $mutex_id_$,
 $waking_up_thread_after_condvar_wait$,
 $owner_thread_id_$,
 $thread_id_A, mutex_id_A$,
 $owner_thread_id$,
 $thread_id_D, mutex_id_D$,
 $doing_condvar_wait$,
 $awoken_thread_id_$,
 $thread_id_R, mutex_id_R$,
 $thread_id_Do$,
 $condvar_id_$, $mutex_id_Do$,
 $thread_id_W$,
 $condvar_id_W, mutex_id$,
 $condvar_id_D$,
 $do_context_switch,$

$$\begin{aligned}
& \text{awoken_thread_id,} \\
& \text{to_reacquire_mutex_id,} \\
& \text{context_id_}, \\
& \text{condvar_id_S, context_id,} \\
& \text{condvar_id,} \\
& \text{thread_was_awaken,} \\
& \text{thread_id,} \\
& \text{delayed_threads} \rangle \\
\text{condvar_signaled_step}(self) & \triangleq \wedge pc[self] = \text{"condvar_signaled_step"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{Head}(\text{stack}[self]).pc] \\
& \wedge \text{context_id_}' = [\text{context_id_} \text{ EXCEPT } ![self] = \text{Head}(\text{stack}[self]).\text{context} \\
& \wedge \text{condvar_id_S}' = [\text{condvar_id_S} \text{ EXCEPT } ![self] = \text{Head}(\text{stack}[self]).\text{condvar_id_S}] \\
& \wedge \text{stack}' = [\text{stack} \text{ EXCEPT } ![self] = \text{Tail}(\text{stack}[self])] \\
& \wedge \text{UNCHANGED } \langle \text{HiRTOS, Thread_Objects,} \\
& \quad \text{Mutex_Objects, Condvar_Objects,} \\
& \quad \text{Timer_Objects,} \\
& \quad \text{Global_Resource_Available,} \\
& \quad \text{thread_id_}, \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait,} \\
& \quad \text{owner_thread_id_}, \text{thread_id_A,} \\
& \quad \text{mutex_id_A, owner_thread_id,} \\
& \quad \text{thread_id_D, mutex_id_D,} \\
& \quad \text{doing_condvar_wait,} \\
& \quad \text{awoken_thread_id_}, \text{thread_id_R,} \\
& \quad \text{mutex_id_R, thread_id_Do,} \\
& \quad \text{condvar_id_}, \text{mutex_id_Do,} \\
& \quad \text{thread_id_W, condvar_id_W,} \\
& \quad \text{mutex_id, condvar_id_D,} \\
& \quad \text{do_context_switch,} \\
& \quad \text{awoken_thread_id,} \\
& \quad \text{to_reacquire_mutex_id,} \\
& \quad \text{context_id, condvar_id,} \\
& \quad \text{thread_was_awaken, thread_id,} \\
& \quad \text{delayed_threads} \rangle \\
\text{Signal_Condvar}(self) & \triangleq \text{enter_critical_section_step_S}(self) \\
& \vee \text{exit_critical_section_step_S}(self) \\
& \vee \text{condvar_signaled_step}(self) \\
\text{enter_critical_section_step_B}(self) & \triangleq \wedge pc[self] = \text{"enter_critical_section_step_B"} \\
& \wedge \text{HiRTOS.Interrupts_Enabled} \wedge \\
& \quad (\text{context_id}[self] \in \text{Threads} \Rightarrow \\
& \quad \quad \text{Thread_Objects}[\text{context_id}[self]].\text{State} = \text{"Running"}) \\
& \wedge \text{HiRTOS}' = [\text{HiRTOS} \text{ EXCEPT } !.\text{Interrupts_Enabled} = \text{FALSE}] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"broadcast_condvar_step"}]
\end{aligned}$$

\wedge UNCHANGED \langle *Thread_Objects*,
Mutex_Objects,
Condvar_Objects,
Timer_Objects,
Global_Resource_Available,
stack, *thread_id_*,
mutex_id_,
waking_up_thread_after_condvar_wait,
owner_thread_id_,
thread_id_A, *mutex_id_A*,
owner_thread_id,
thread_id_D, *mutex_id_D*,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R, *mutex_id_R*,
thread_id_Do,
condvar_id_,
mutex_id_Do,
thread_id_W,
condvar_id_W, *mutex_id*,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id, *condvar_id*,
thread_was_awaken,
thread_id,
delayed_threads \rangle

broadcast_condvar_step(self) \triangleq \wedge *pc[self]* = "broadcast_condvar_step"
 \wedge IF \neg *Is_Thread_Priority_Queue_Empty*(*Condvar_Objects*[*condvar_id*][*s*])
THEN \wedge *pc'* = [*pc* EXCEPT !*self*] = "broadcast_condvar_wakeup_wait"
ELSE \wedge *pc'* = [*pc* EXCEPT !*self*] = "broadcast_condvar_check_if_sync"
 \wedge UNCHANGED \langle *HiRTOS*, *Thread_Objects*,
Mutex_Objects, *Condvar_Objects*,
Timer_Objects,
Global_Resource_Available,
stack, *thread_id_*, *mutex_id_*,
waking_up_thread_after_condvar_wait,
owner_thread_id_, *thread_id_A*,
mutex_id_A, *owner_thread_id*,
thread_id_D, *mutex_id_D*,
doing_condvar_wait,

$$\begin{aligned}
& \text{awoken_thread_id_}, \text{thread_id_R}, \\
& \text{mutex_id_R}, \text{thread_id_Do}, \\
& \text{condvar_id_}, \text{mutex_id_Do}, \\
& \text{thread_id_W}, \text{condvar_id_W}, \\
& \text{mutex_id_}, \text{condvar_id_D}, \\
& \text{do_context_switch}, \\
& \text{awoken_thread_id_}, \\
& \text{to_reacquire_mutex_id_}, \\
& \text{context_id_}, \text{condvar_id_S}, \\
& \text{context_id_}, \text{condvar_id_}, \\
& \text{thread_was_awaken}, \text{thread_id_}, \\
& \text{delayed_threads}) \\
\text{broadcast_condvar_wakeup_waiter_step}(\text{self}) & \triangleq \wedge \text{pc}[\text{self}] = \text{"broadcast_condvar_wakeup_waiter_step"} \\
& \wedge \wedge \text{condvar_id_D}' = [\text{condvar_id_D} \text{ EXCEPT } ![\text{self}] = \text{condvar_id_D}] \\
& \wedge \text{do_context_switch}' = [\text{do_context_switch} \text{ EXCEPT } ![\text{self}] = \text{do_context_switch}] \\
& \wedge \text{stack}' = [\text{stack} \text{ EXCEPT } ![\text{self}] = \langle [\text{procedure} \mapsto \text{"D"}, \\
& \quad \text{pc} \mapsto \text{"br"}, \\
& \quad \text{awoken_thread_id_}, \\
& \quad \text{to_reacquire_mutex_id_}, \\
& \quad \text{condvar_id_D} \mapsto \text{condvar_id_D}, \\
& \quad \text{do_context_switch} \mapsto \text{do_context_switch}, \\
& \quad \circ \text{stack}[\text{self}]] \rangle] \\
& \wedge \text{awoken_thread_id}' = [\text{awoken_thread_id} \text{ EXCEPT } ![\text{self}] = \text{awoken_thread_id}] \\
& \wedge \text{to_reacquire_mutex_id}' = [\text{to_reacquire_mutex_id} \text{ EXCEPT } ![\text{self}] = \text{to_reacquire_mutex_id}] \\
& \wedge \text{pc}' = [\text{pc} \text{ EXCEPT } ![\text{self}] = \text{"signal_condvar_step"}] \\
& \wedge \text{UNCHANGED } \langle \text{HiRTOS}, \\
& \quad \text{Thread_Objects}, \\
& \quad \text{Mutex_Objects}, \\
& \quad \text{Condvar_Objects}, \\
& \quad \text{Timer_Objects}, \\
& \quad \text{Global_Resource_Available}, \\
& \quad \text{thread_id_}, \\
& \quad \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait}, \\
& \quad \text{owner_thread_id_}, \\
& \quad \text{thread_id_A}, \\
& \quad \text{mutex_id_A}, \\
& \quad \text{owner_thread_id}, \\
& \quad \text{thread_id_D}, \\
& \quad \text{mutex_id_D}, \\
& \quad \text{doing_condvar_wait}, \\
& \quad \text{awoken_thread_id_}, \\
& \quad \text{thread_id_R}, \\
& \quad \text{mutex_id_R},
\end{aligned}$$

$$\begin{aligned}
& \text{thread_id_Do,} \\
& \text{condvar_id_}, \\
& \text{mutex_id_Do,} \\
& \text{thread_id_W,} \\
& \text{condvar_id_W,} \\
& \text{mutex_id,} \\
& \text{context_id_}, \\
& \text{condvar_id_S,} \\
& \text{context_id,} \\
& \text{condvar_id,} \\
& \text{thread_was_awaken,} \\
& \text{thread_id,} \\
& \text{delayed_threads} \rangle \\
\text{broadcast_condvar_after_waking_up_one_waiter_step}(self) \triangleq & \wedge pc[self] = \text{"broadcast_condvar_after_waking_up_one_waiter_step"}, \\
& \wedge \text{thread_was_awaken}' = [\text{thread_was_awaken}], \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"broadcast_condvar_after_waking_up_one_waiter_step"}], \\
& \wedge \text{UNCHANGED } \langle \text{HiRTOS,} \\
& \quad \text{Thread_Objects,} \\
& \quad \text{Mutex_Objects,} \\
& \quad \text{Condvar_Objects,} \\
& \quad \text{Timer_Objects,} \\
& \quad \text{Global_Resource_Available,} \\
& \quad \text{stack,} \\
& \quad \text{thread_id_}, \\
& \quad \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait,} \\
& \quad \text{owner_thread_id_}, \\
& \quad \text{thread_id_A,} \\
& \quad \text{mutex_id_A,} \\
& \quad \text{owner_thread_id,} \\
& \quad \text{thread_id_D,} \\
& \quad \text{mutex_id_D,} \\
& \quad \text{doing_condvar_wait,} \\
& \quad \text{awoken_thread_id_}, \\
& \quad \text{thread_id_R,} \\
& \quad \text{mutex_id_R,} \\
& \quad \text{thread_id_Do,} \\
& \quad \text{condvar_id_}, \\
& \quad \text{mutex_id_Do,} \\
& \quad \text{thread_id_W,} \\
& \quad \text{condvar_id_W,} \\
& \quad \text{mutex_id,} \\
& \quad \text{condvar_id_D,} \\
& \quad \text{do_context_switch,}
\end{aligned}$$

$$\begin{aligned}
& \text{awoken_thread_id,} \\
& \text{to_reacquire_mutex_id,} \\
& \text{context_id_}, \\
& \text{condvar_id_S,} \\
& \text{context_id,} \\
& \text{condvar_id,} \\
& \text{thread_id,} \\
& \text{delayed_threads}\rangle \\
\text{broadcast_condvar_check_if_sync_context_switch_needed_step}(self) \triangleq & \wedge pc[self] = \text{"broadcast_condvar_ch} \\
& \wedge \text{IF } context_id[self] \in \text{Threads} \wedge \\
& \quad \text{THEN } \wedge pc' = [pc \text{ EXCEPT !} \\
& \quad \text{ELSE } \wedge pc' = [pc \text{ EXCEPT !} \\
& \wedge \text{UNCHANGED } \langle \text{HiRTOS,} \\
& \quad \text{Thread_Objects,} \\
& \quad \text{Mutex_Objects,} \\
& \quad \text{Condvar_Objects,} \\
& \quad \text{Timer_Objects,} \\
& \quad \text{Global_Resource_} \\
& \quad \text{stack,} \\
& \quad \text{thread_id_}, \\
& \quad \text{mutex_id_}, \\
& \quad \text{waking_up_thread} \\
& \quad \text{owner_thread_id_} \\
& \quad \text{thread_id_A,} \\
& \quad \text{mutex_id_A,} \\
& \quad \text{owner_thread_id,} \\
& \quad \text{thread_id_D,} \\
& \quad \text{mutex_id_D,} \\
& \quad \text{doing_condvar_w} \\
& \quad \text{awoken_thread_id} \\
& \quad \text{thread_id_R,} \\
& \quad \text{mutex_id_R,} \\
& \quad \text{thread_id_Do,} \\
& \quad \text{condvar_id_}, \\
& \quad \text{mutex_id_Do,} \\
& \quad \text{thread_id_W,} \\
& \quad \text{condvar_id_W,} \\
& \quad \text{mutex_id,} \\
& \quad \text{condvar_id_D,} \\
& \quad \text{do_context_switch} \\
& \quad \text{awoken_thread_id} \\
& \quad \text{to_reacquire_mute} \\
& \quad \text{context_id_}, \\
& \quad \text{condvar_id_S,}
\end{aligned}$$

$$\begin{aligned}
& \text{context_id,} \\
& \text{condvar_id,} \\
& \text{thread_was_awake,} \\
& \text{thread_id,} \\
& \text{delayed_threads} \rangle \\
\text{broadcast_condvar_synchronous_context_switch_step}(self) \triangleq & \wedge pc[self] = \text{"broadcast_condvar_synchronous_context_switch_step"}, \\
& \wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procid_id, \\
& \quad pc \\
& \quad \circ stack] \rangle \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"check_time_out"}, \\
& \wedge \text{UNCHANGED } \langle \text{HiRTOS,} \\
& \quad \text{Thread_Objects,} \\
& \quad \text{Mutex_Objects,} \\
& \quad \text{Condvar_Objects,} \\
& \quad \text{Timer_Objects,} \\
& \quad \text{Global_Resource_Available,} \\
& \quad \text{thread_id_}, \\
& \quad \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_context_switch,} \\
& \quad \text{owner_thread_id_}, \\
& \quad \text{thread_id_A,} \\
& \quad \text{mutex_id_A,} \\
& \quad \text{owner_thread_id,} \\
& \quad \text{thread_id_D,} \\
& \quad \text{mutex_id_D,} \\
& \quad \text{doing_condvar_wait,} \\
& \quad \text{awoken_thread_id_}, \\
& \quad \text{thread_id_R,} \\
& \quad \text{mutex_id_R,} \\
& \quad \text{thread_id_Do,} \\
& \quad \text{condvar_id_}, \\
& \quad \text{mutex_id_Do,} \\
& \quad \text{thread_id_W,} \\
& \quad \text{condvar_id_W,} \\
& \quad \text{mutex_id,} \\
& \quad \text{condvar_id_D,} \\
& \quad \text{do_context_switch,} \\
& \quad \text{awoken_thread_id,} \\
& \quad \text{to_reacquire_mutex_id,} \\
& \quad \text{context_id_}, \\
& \quad \text{condvar_id_S,} \\
& \quad \text{context_id,} \\
& \quad \text{condvar_id,} \\
& \quad \text{thread_was_awaken,}
\end{aligned}$$

$$\begin{aligned}
& \text{thread_id,} \\
& \text{delayed_threads} \rangle \\
\text{exit_critical_section_step_B}(self) & \triangleq \wedge pc[self] = \text{"exit_critical_section_step_B"} \\
& \wedge HiRTOS' = [HiRTOS \text{ EXCEPT } !.Interrupts_Enabled = \text{TRUE}] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"condvar_broadcasted_step"}] \\
& \wedge \text{UNCHANGED } \langle Thread_Objects, \\
& \quad Mutex_Objects, \\
& \quad Condvar_Objects, \\
& \quad Timer_Objects, \\
& \quad Global_Resource_Available, \\
& \quad stack, thread_id_-, \\
& \quad mutex_id_-, \\
& \quad waking_up_thread_after_condvar_wait, \\
& \quad owner_thread_id_-, \\
& \quad thread_id_A, mutex_id_A, \\
& \quad owner_thread_id, \\
& \quad thread_id_D, mutex_id_D, \\
& \quad doing_condvar_wait, \\
& \quad awoken_thread_id_-, \\
& \quad thread_id_R, mutex_id_R, \\
& \quad thread_id_Do, \\
& \quad condvar_id_-, mutex_id_Do, \\
& \quad thread_id_W, \\
& \quad condvar_id_W, mutex_id, \\
& \quad condvar_id_D, \\
& \quad do_context_switch, \\
& \quad awoken_thread_id, \\
& \quad to_reacquire_mutex_id, \\
& \quad context_id_-, \\
& \quad condvar_id_S, context_id, \\
& \quad condvar_id, \\
& \quad thread_was_awaken, \\
& \quad thread_id, \\
& \quad delayed_threads \rangle \\
\text{condvar_broadcasted_step}(self) & \triangleq \wedge pc[self] = \text{"condvar_broadcasted_step"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{Head}(stack[self]).pc] \\
& \wedge thread_was_awaken' = [thread_was_awaken \text{ EXCEPT } ![self] = \text{Head}(stack[self]).thread_was_awaken] \\
& \wedge context_id' = [context_id \text{ EXCEPT } ![self] = \text{Head}(stack[self]).context_id] \\
& \wedge condvar_id' = [condvar_id \text{ EXCEPT } ![self] = \text{Head}(stack[self]).condvar_id] \\
& \wedge stack' = [stack \text{ EXCEPT } ![self] = \text{Tail}(stack[self])] \\
& \wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects, \\
& \quad Mutex_Objects, \\
& \quad Condvar_Objects,
\end{aligned}$$

Timer_Objects,
Global_Resource_Available,
thread_id_, mutex_id_,
waking_up_thread_after_condvar_wait,
owner_thread_id_,
thread_id_A, mutex_id_A,
owner_thread_id, thread_id_D,
mutex_id_D,
doing_condvar_wait,
awoken_thread_id_,
thread_id_R, mutex_id_R,
thread_id_Do, condvar_id_,
mutex_id_Do, thread_id_W,
condvar_id_W, mutex_id,
condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_, condvar_id_S,
thread_id, delayed_threads

$Broadcast_Condvar(self) \triangleq$ *enter_critical_section_step_B(self)*
 \vee *broadcast_condvar_step(self)*
 \vee *broadcast_condvar_wakeup_waiter_step(self)*
 \vee *broadcast_condvar_after_waking_up_one_waiter_step(self)*
 \vee *broadcast_condvar_check_if_sync_context_switch_needed_step(self)*
 \vee *broadcast_condvar_synchronous_context_switch_step(self)*
 \vee *exit_critical_section_step_B(self)*
 \vee *condvar_broadcasted_step(self)*

$enter_critical_section_step_D(self) \triangleq$ $\wedge pc[self] = \text{"enter_critical_section_step_D"}$
 $\wedge HiRTOS.Interrupts_Enabled \wedge$
 $(thread_id[self] \in Threads \Rightarrow$
 $\quad Thread_Objects[thread_id[self]].State = \text{"Running"})$
 $\wedge HiRTOS' = [HiRTOS \text{ EXCEPT } !.Interrupts_Enabled = \text{FALSE}]$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"delay_until_step"}]$
 $\wedge \text{UNCHANGED } \langle Thread_Objects,$
 $\quad Mutex_Objects,$
 $\quad Condvar_Objects,$
 $\quad Timer_Objects,$
 $\quad Global_Resource_Available,$
 $\quad stack, thread_id_,$
 $\quad mutex_id_,$
 $\quad waking_up_thread_after_condvar_wait,$
 $\quad owner_thread_id_,$

$thread_id_A, mutex_id_A,$
 $owner_thread_id,$
 $thread_id_D, mutex_id_D,$
 $doing_condvar_wait,$
 $awoken_thread_id_,$
 $thread_id_R, mutex_id_R,$
 $thread_id_Do,$
 $condvar_id_,$
 $mutex_id_Do,$
 $thread_id_W,$
 $condvar_id_W, mutex_id,$
 $condvar_id_D,$
 $do_context_switch,$
 $awoken_thread_id,$
 $to_reacquire_mutex_id,$
 $context_id_,$
 $condvar_id_S,$
 $context_id, condvar_id,$
 $thread_was_awaken,$
 $thread_id,$
 $delayed_threads\rangle$

$delay_until_step(self) \triangleq \wedge pc[self] = \text{"delay_until_step"}$
 $\wedge Timer_Objects' = [Timer_Objects \text{ EXCEPT } ![Thread_Objects[thread_id[self]].$
 $\wedge \wedge condvar_id_ ' = [condvar_id_ \text{ EXCEPT } ![self] = Thread_Objects[thread_id[self]].$
 $\wedge mutex_id_Do' = [mutex_id_Do \text{ EXCEPT } ![self] = \text{"Invalid_Mutex_Id"}]$
 $\wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"Do_Wait_On_Condvar"},$
 $pc \mapsto \text{"exit_critical_section_step_1"},$
 $thread_id_Do \mapsto thread_id_Do[self],$
 $condvar_id_ \mapsto condvar_id_ [self],$
 $mutex_id_Do \mapsto mutex_id_Do[self]]\rangle$
 $\circ stack[self]]$
 $\wedge thread_id_Do' = [thread_id_Do \text{ EXCEPT } ![self] = thread_id[self]]$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"wait_on_condvar_wait_step"}]$
 $\wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects,$
 $Mutex_Objects, Condvar_Objects,$
 $Global_Resource_Available,$
 $thread_id_ , mutex_id_ ,$
 $waking_up_thread_after_condvar_wait,$
 $owner_thread_id_ , thread_id_A,$
 $mutex_id_A, owner_thread_id,$
 $thread_id_D, mutex_id_D,$
 $doing_condvar_wait,$
 $awoken_thread_id_ , thread_id_R,$
 $mutex_id_R, thread_id_W,$

$$\begin{aligned}
& \text{condvar_id_W, mutex_id, condvar_id_D,} \\
& \text{do_context_switch, awoken_thread_id,} \\
& \text{to_reacquire_mutex_id, context_id_}, \\
& \text{condvar_id_S, context_id, condvar_id,} \\
& \text{thread_was_awaken, thread_id,} \\
& \text{delayed_threads} \rangle \\
\text{exit_critical_section_step_D}(self) \triangleq & \wedge pc[self] = \text{"exit_critical_section_step_D"} \\
& \wedge HiRTOS' = [HiRTOS \text{ EXCEPT } !.Interrupts_Enabled = \text{TRUE}] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"after_delay_until_step"}] \\
& \wedge \text{UNCHANGED } \langle Thread_Objects, \\
& \quad Mutex_Objects, \\
& \quad Condvar_Objects, \\
& \quad Timer_Objects, \\
& \quad Global_Resource_Available, \\
& \quad stack, thread_id_ , \\
& \quad mutex_id_ , \\
& \quad waking_up_thread_after_condvar_wait, \\
& \quad owner_thread_id_ , \\
& \quad thread_id_A, mutex_id_A, \\
& \quad owner_thread_id, \\
& \quad thread_id_D, mutex_id_D, \\
& \quad doing_condvar_wait, \\
& \quad awoken_thread_id_ , \\
& \quad thread_id_R, mutex_id_R, \\
& \quad thread_id_Do, \\
& \quad condvar_id_ , mutex_id_Do, \\
& \quad thread_id_W, \\
& \quad condvar_id_W, mutex_id, \\
& \quad condvar_id_D, \\
& \quad do_context_switch, \\
& \quad awoken_thread_id, \\
& \quad to_reacquire_mutex_id, \\
& \quad context_id_ , \\
& \quad condvar_id_S, context_id, \\
& \quad condvar_id, \\
& \quad thread_was_awaken, \\
& \quad thread_id, \\
& \quad delayed_threads \rangle \\
\text{after_delay_until_step}(self) \triangleq & \wedge pc[self] = \text{"after_delay_until_step"} \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{Head}(stack[self]).pc] \\
& \wedge thread_id' = [thread_id \text{ EXCEPT } ![self] = \text{Head}(stack[self]).thread_id] \\
& \wedge stack' = [stack \text{ EXCEPT } ![self] = \text{Tail}(stack[self])] \\
& \wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects,
\end{aligned}$$

Mutex_Objects, Condvar_Objects,
Timer_Objects,
Global_Resource_Available,
thread_id_, mutex_id_,
waking_up_thread_after_condvar_wait,
owner_thread_id_, thread_id_A,
mutex_id_A, owner_thread_id,
thread_id_D, mutex_id_D,
doing_condvar_wait,
awoken_thread_id_, thread_id_R,
mutex_id_R, thread_id_Do,
condvar_id_, mutex_id_Do,
thread_id_W, condvar_id_W,
mutex_id, condvar_id_D,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_, condvar_id_S,
context_id, condvar_id,
thread_was_awaken,
delayed_threads}

$Delay_Until(self) \triangleq$ *enter_critical_section_step_D(self)*
 \vee *delay_until_step(self)*
 \vee *exit_critical_section_step_D(self)*
 \vee *after_delay_until_step(self)*

$thread_state_machine_next_state_loop(self) \triangleq$ $\wedge pc[self] = \text{"thread_state_machine_next_state_loop"}$
 $\wedge Thread_Objects[self].State = \text{"Running"} \wedge HiRTOS.Inte$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"context_switch0"}]$
 $\wedge \text{UNCHANGED } \langle HiRTOS,$
 $\quad Thread_Objects,$
 $\quad Mutex_Objects,$
 $\quad Condvar_Objects,$
 $\quad Timer_Objects,$
 $\quad Global_Resource_Available,$
 $\quad stack,$
 $\quad thread_id_,$
 $\quad mutex_id_,$
 $\quad waking_up_thread_after_condvar_wait,$
 $\quad owner_thread_id_,$
 $\quad thread_id_A,$
 $\quad mutex_id_A,$
 $\quad owner_thread_id,$
 $\quad thread_id_D,$

$mutex_id_D,$
 $doing_condvar_wait,$
 $awoken_thread_id_,$
 $thread_id_R,$
 $mutex_id_R,$
 $thread_id_Do,$
 $condvar_id_,$
 $mutex_id_Do,$
 $thread_id_W,$
 $condvar_id_W,$
 $mutex_id,$
 $condvar_id_D,$
 $do_context_switch,$
 $awoken_thread_id,$
 $to_reacquire_mutex_id,$
 $context_id_,$
 $condvar_id_S,$
 $context_id,$
 $condvar_id,$
 $thread_was_awaken,$
 $thread_id,$
 $delayed_threads\}$

$$\begin{aligned}
context_switch0(self) \triangleq & \wedge pc[self] = \text{"context_switch0"} \\
& \wedge \vee \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"acquire_mutex_step"}] \\
& \wedge \text{UNCHANGED } \langle Global_Resource_Available, stack, context_id_ , condvar_id_ \rangle \\
& \vee \wedge Global_Resource_Available' = \text{TRUE} \\
& \wedge \vee \wedge \wedge condvar_id_S' = [condvar_id_S \text{ EXCEPT } ![self] = \text{"condvar1"}] \\
& \wedge context_id_ ' = [context_id_ \text{ EXCEPT } ![self] = self] \\
& \wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"Signal_Condvar"} \\
& \quad pc \mapsto \text{"thread_iteration"} \\
& \quad context_id_ \mapsto context_id_ [self] \\
& \quad condvar_id_S \mapsto condvar_id_ [self] \\
& \quad \circ stack[self]] \rangle \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"enter_critical_section_step_S"}] \\
& \wedge \text{UNCHANGED } \langle context_id, condvar_id, thread_was_awaken \rangle \\
& \vee \wedge \wedge condvar_id' = [condvar_id \text{ EXCEPT } ![self] = \text{"condvar1"}] \\
& \wedge context_id' = [context_id \text{ EXCEPT } ![self] = self] \\
& \wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"Broadcast_Conc"} \\
& \quad pc \mapsto \text{"thread_iteration"} \\
& \quad thread_was_awaken \mapsto thread_was_awaken \\
& \quad context_id \mapsto context_id [self] \\
& \quad condvar_id \mapsto condvar_id [self] \\
& \quad \circ stack[self]] \rangle \\
& \wedge thread_was_awaken' = [thread_was_awaken \text{ EXCEPT } ![self] = \text{FALSE}]
\end{aligned}$$

$$\begin{aligned}
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"enter_critical_section_step_B"}] \\
& \wedge \text{UNCHANGED } \langle context_id_ , condvar_id_S \rangle \\
& \wedge \text{UNCHANGED } thread_id \\
\vee \wedge \wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"Delay_Until"}, \\
& \quad pc \mapsto \text{"thread_iteration_com"}, \\
& \quad thread_id \mapsto thread_id[self]] \rangle \\
& \quad \circ stack[self]] \\
& \wedge thread_id' = [thread_id \text{ EXCEPT } ![self] = self] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"enter_critical_section_step_D"}] \\
& \wedge \text{UNCHANGED } \langle Global_Resource_Available, context_id_ , condvar_id_S, co \\
\wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects, Mutex_Objects, \\
& \quad Condvar_Objects, Timer_Objects, \\
& \quad thread_id_ , mutex_id_ , \\
& \quad waking_up_thread_after_condvar_wait, \\
& \quad owner_thread_id_ , thread_id_A, \\
& \quad mutex_id_A, owner_thread_id, \\
& \quad thread_id_D, mutex_id_D, \\
& \quad doing_condvar_wait, awoken_thread_id_ , \\
& \quad thread_id_R, mutex_id_R, thread_id_Do, \\
& \quad condvar_id_ , mutex_id_Do, thread_id_W, \\
& \quad condvar_id_W, mutex_id, condvar_id_D, \\
& \quad do_context_switch, awoken_thread_id, \\
& \quad to_reacquire_mutex_id, \\
& \quad delayed_threads \rangle
\end{aligned}$$

$$\begin{aligned}
acquire_mutex_step(self) &\triangleq \wedge pc[self] = \text{"acquire_mutex_step"} \\
&\wedge \wedge mutex_id_A' = [mutex_id_A \text{ EXCEPT } ![self] = \text{"mutex1"}] \\
&\wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"Acquire_Mutex"}, \\
& \quad pc \mapsto \text{"context_switch1"}, \\
& \quad owner_thread_id \mapsto owner_thread_id, \\
& \quad thread_id_A \mapsto thread_id_A[self], \\
& \quad mutex_id_A \mapsto mutex_id_A[self]] \rangle \\
& \quad \circ stack[self]] \\
&\wedge thread_id_A' = [thread_id_A \text{ EXCEPT } ![self] = self] \\
&\wedge owner_thread_id' = [owner_thread_id \text{ EXCEPT } ![self] = \text{"Invalid_Thread_Id"}] \\
&\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"enter_critical_section_step_"}] \\
&\wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects, \\
& \quad Mutex_Objects, Condvar_Objects, \\
& \quad Timer_Objects, \\
& \quad Global_Resource_Available, \\
& \quad thread_id_ , mutex_id_ , \\
& \quad waking_up_thread_after_condvar_wait, \\
& \quad owner_thread_id_ , thread_id_D, \\
& \quad mutex_id_D, doing_condvar_wait, \\
& \quad awoken_thread_id_ , thread_id_R,
\end{aligned}$$

mutex_id_R, *thread_id_Do*,
condvar_id_, *mutex_id_Do*,
thread_id_W, *condvar_id_W*,
mutex_id, *condvar_id_D*,
do_context_switch,
awoken_thread_id,
to_reacquire_mutex_id, *context_id_*,
condvar_id_S, *context_id*,
condvar_id, *thread_was_awaken*,
thread_id, *delayed_threads*

$context_switch1(self) \triangleq$ $\wedge pc[self] = \text{"context_switch1"}$
 $\wedge Thread_Objects[self].State = \text{"Running"} \wedge HiRTOS.Interrupts_Enabled$
 $\wedge Assert((Mutex_Objects["mutex1"].Owner_Thread_Id = self),$
 $\quad \text{"Failure of assertion at line 554, column 13."})$
 $\wedge \vee \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"waiting_for_resource_step"}]$
 $\quad \vee \wedge TRUE$
 $\quad \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"release_mutex_step"}]$
 $\wedge UNCHANGED \langle HiRTOS, Thread_Objects, Mutex_Objects,$
 $\quad Condvar_Objects, Timer_Objects,$
 $\quad Global_Resource_Available, stack,$
 $\quad thread_id_ , mutex_id_ ,$
 $\quad waking_up_thread_after_condvar_wait,$
 $\quad owner_thread_id_ , thread_id_A,$
 $\quad mutex_id_A, owner_thread_id,$
 $\quad thread_id_D, mutex_id_D,$
 $\quad doing_condvar_wait, awoken_thread_id_ ,$
 $\quad thread_id_R, mutex_id_R, thread_id_Do,$
 $\quad condvar_id_ , mutex_id_Do, thread_id_W,$
 $\quad condvar_id_W, mutex_id, condvar_id_D,$
 $\quad do_context_switch, awoken_thread_id,$
 $\quad to_reacquire_mutex_id, context_id_ ,$
 $\quad condvar_id_S, context_id, condvar_id,$
 $\quad thread_was_awaken, thread_id,$
 $\quad delayed_threads \rangle$

$waiting_for_resource_step(self) \triangleq$ $\wedge pc[self] = \text{"waiting_for_resource_step"}$
 $\wedge IF \neg Global_Resource_Available$
 $\quad THEN \wedge \wedge condvar_id_W' = [condvar_id_W \text{ EXCEPT } ![self] =$
 $\quad \quad \wedge mutex_id' = [mutex_id \text{ EXCEPT } ![self] = \text{"mutex1"}]$
 $\quad \quad \wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"Waiting"},$
 $\quad \quad \quad pc \mapsto \text{"context_switch"},$
 $\quad \quad \quad thread_id_W \mapsto$
 $\quad \quad \quad condvar_id_W \mapsto$
 $\quad \quad \quad mutex_id \mapsto mu$

$$\begin{aligned}
& \circ \text{stack}[self]] \\
& \wedge \text{thread_id_}W' = [\text{thread_id_}W \text{ EXCEPT } ![self] = self] \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"enter_critical_section_step_V"}] \\
& \wedge \text{UNCHANGED } Global_Resource_Available \\
\text{ELSE } & \wedge Global_Resource_Available' = \text{FALSE} \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"release_mutex_step"}] \\
& \wedge \text{UNCHANGED } \langle \text{stack}, \\
& \quad \text{thread_id_}W, \\
& \quad \text{condvar_id_}W, \\
& \quad \text{mutex_id} \rangle \\
& \wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects, \\
& \quad Mutex_Objects, \\
& \quad Condvar_Objects, \\
& \quad Timer_Objects, \text{thread_id_}, \\
& \quad \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait}, \\
& \quad \text{owner_thread_id_}, \\
& \quad \text{thread_id_}A, \text{mutex_id_}A, \\
& \quad \text{owner_thread_id}, \\
& \quad \text{thread_id_}D, \text{mutex_id_}D, \\
& \quad \text{doing_condvar_wait}, \\
& \quad \text{awoken_thread_id_}, \\
& \quad \text{thread_id_}R, \text{mutex_id_}R, \\
& \quad \text{thread_id_}Do, \text{condvar_id_}, \\
& \quad \text{mutex_id_}Do, \text{condvar_id_}D, \\
& \quad \text{do_context_switch}, \\
& \quad \text{awoken_thread_id}, \\
& \quad \text{to_reacquire_mutex_id}, \\
& \quad \text{context_id_}, \text{condvar_id_}S, \\
& \quad \text{context_id}, \text{condvar_id}, \\
& \quad \text{thread_was_awaken}, \\
& \quad \text{thread_id}, \text{delayed_threads} \rangle \\
\text{context_switch2}(self) & \triangleq \wedge pc[self] = \text{"context_switch2"} \\
& \wedge Thread_Objects[self].State = \text{"Running"} \wedge HiRTOS.Interrupts_Enabled \\
& \wedge \text{Assert}(Mutex_Objects["mutex1"].Owner_Thread_Id = self, \\
& \quad \text{"Failure of assertion at line 562, column 21."}) \\
& \wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"waiting_for_resource_step"}] \\
& \wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects, Mutex_Objects, \\
& \quad Condvar_Objects, Timer_Objects, \\
& \quad Global_Resource_Available, \text{stack}, \\
& \quad \text{thread_id_}, \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait}, \\
& \quad \text{owner_thread_id_}, \text{thread_id_}A, \\
& \quad \text{mutex_id_}A, \text{owner_thread_id},
\end{aligned}$$

$thread_id_D, mutex_id_D,$
 $doing_condvar_wait, awoken_thread_id_,$
 $thread_id_R, mutex_id_R, thread_id_Do,$
 $condvar_id_ , mutex_id_Do, thread_id_W,$
 $condvar_id_W, mutex_id, condvar_id_D,$
 $do_context_switch, awoken_thread_id,$
 $to_reacquire_mutex_id, context_id_ ,$
 $condvar_id_S, context_id, condvar_id,$
 $thread_was_awaken, thread_id,$
 $delayed_threads\}$

$release_mutex_step(self) \triangleq \wedge pc[self] = \text{"release_mutex_step"}$
 $\wedge \wedge mutex_id_R' = [mutex_id_R \text{ EXCEPT } ![self] = \text{"mutex1"}]$
 $\wedge stack' = [stack \text{ EXCEPT } ![self] = \langle [procedure \mapsto \text{"Release_Mutex"},$
 $pc \mapsto \text{"thread_iteration_compl"},$
 $thread_id_R \mapsto thread_id_R[self],$
 $mutex_id_R \mapsto mutex_id_R[self]] \rangle$
 $\circ stack[self]]$
 $\wedge thread_id_R' = [thread_id_R \text{ EXCEPT } ![self] = self]$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"enter_critical_section_step_R"}]$
 $\wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects,$
 $Mutex_Objects, Condvar_Objects,$
 $Timer_Objects,$
 $Global_Resource_Available,$
 $thread_id_ , mutex_id_ ,$
 $waking_up_thread_after_condvar_wait,$
 $owner_thread_id_ , thread_id_A,$
 $mutex_id_A, owner_thread_id,$
 $thread_id_D, mutex_id_D,$
 $doing_condvar_wait,$
 $awoken_thread_id_ , thread_id_Do,$
 $condvar_id_ , mutex_id_Do,$
 $thread_id_W, condvar_id_W,$
 $mutex_id, condvar_id_D,$
 $do_context_switch,$
 $awoken_thread_id,$
 $to_reacquire_mutex_id, context_id_ ,$
 $condvar_id_S, context_id,$
 $condvar_id, thread_was_awaken,$
 $thread_id, delayed_threads\}$

$thread_iteration_completed_step(self) \triangleq \wedge pc[self] = \text{"thread_iteration_completed_step"}$
 $\wedge \text{TRUE}$
 $\wedge pc' = [pc \text{ EXCEPT } ![self] = \text{"thread_state_machine_next_state_I"}]$
 $\wedge \text{UNCHANGED } \langle HiRTOS,$

Thread_Objects,
Mutex_Objects,
Condvar_Objects,
Timer_Objects,
Global_Resource_Available,
stack, thread_id_,
mutex_id_,
waking-up-thread-after-condvar-wait,
owner_thread_id_,
thread_id_A,
mutex_id_A,
owner_thread_id,
thread_id_D,
mutex_id_D,
doing-condvar-wait,
awoken_thread_id_,
thread_id_R,
mutex_id_R,
thread_id_Do,
condvar_id_,
mutex_id_Do,
thread_id_W,
condvar_id_W,
mutex_id,
condvar_id_D,
do-context-switch,
awoken_thread_id,
to_reacquire_mutex_id,
context_id_,
condvar_id_S,
context_id,
condvar_id,
thread_was_awaken,
thread_id,
delayed_threads

$Thread_State_Machine(self) \triangleq thread_state_machine_next_state_loop(self)$
 $\vee context_switch0(self)$
 $\vee acquire_mutex_step(self)$
 $\vee context_switch1(self)$
 $\vee waiting_for_resource_step(self)$
 $\vee context_switch2(self)$
 $\vee release_mutex_step(self)$
 $\vee thread_iteration_completed_step(self)$

$$\begin{aligned}
idle_thread_next_state_loop \triangleq & \wedge pc["Idle_Thread"] = "idle_thread_next_state_loop" \\
& \wedge Thread_Objects["Idle_Thread"].State = "Running" \wedge HiRTOS.Interrupts \\
& \wedge pc' = [pc \text{ EXCEPT } !["Idle_Thread"] = "idle_thread_next_state_loop"] \\
& \wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects, \\
& \quad Mutex_Objects, Condvar_Objects, \\
& \quad Timer_Objects, \\
& \quad Global_Resource_Available, \\
& \quad stack, thread_id_-, mutex_id_-, \\
& \quad waking_up_thread_after_condvar_wait, \\
& \quad owner_thread_id_-, thread_id_A, \\
& \quad mutex_id_A, owner_thread_id, \\
& \quad thread_id_D, mutex_id_D, \\
& \quad doing_condvar_wait, \\
& \quad awoken_thread_id_-, thread_id_R, \\
& \quad mutex_id_R, thread_id_Do, \\
& \quad condvar_id_-, mutex_id_Do, \\
& \quad thread_id_W, condvar_id_W, \\
& \quad mutex_id, condvar_id_D, \\
& \quad do_context_switch, \\
& \quad awoken_thread_id, \\
& \quad to_reacquire_mutex_id, \\
& \quad context_id_-, condvar_id_S, \\
& \quad context_id, condvar_id, \\
& \quad thread_was_awaken, thread_id, \\
& \quad delayed_threads \rangle
\end{aligned}$$

$$Idle_Thread \triangleq idle_thread_next_state_loop$$

$$\begin{aligned}
timer_interrupt_next_state_loop \triangleq & \wedge pc["Timer_Interrupt"] = "timer_interrupt_next_state_loop" \\
& \wedge pc' = [pc \text{ EXCEPT } !["Timer_Interrupt"] = "enter_critical_section_step"] \\
& \wedge \text{UNCHANGED } \langle HiRTOS, Thread_Objects, \\
& \quad Mutex_Objects, \\
& \quad Condvar_Objects, \\
& \quad Timer_Objects, \\
& \quad Global_Resource_Available, \\
& \quad stack, thread_id_-, \\
& \quad mutex_id_-, \\
& \quad waking_up_thread_after_condvar_wait, \\
& \quad owner_thread_id_-, \\
& \quad thread_id_A, mutex_id_A, \\
& \quad owner_thread_id, \\
& \quad thread_id_D, mutex_id_D, \\
& \quad doing_condvar_wait, \\
& \quad awoken_thread_id_-, \\
& \quad thread_id_R, mutex_id_R,
\end{aligned}$$

$thread_id_Do, condvar_id_,$
 $mutex_id_Do, thread_id_W,$
 $condvar_id_W, mutex_id,$
 $condvar_id_D,$
 $do_context_switch,$
 $awoken_thread_id,$
 $to_reacquire_mutex_id,$
 $context_id_ , condvar_id_S,$
 $context_id, condvar_id,$
 $thread_was_awaken,$
 $thread_id, delayed_threads\}$

$enter_critical_section_step_T \triangleq \wedge pc["Timer_Interrupt"] = "enter_critical_section_step_T"$
 $\wedge HiRTOS.Interrupts_Enabled \wedge$
 $(["Timer_Interrupt"] \in Threads \Rightarrow$
 $Thread_Objects["Timer_Interrupt"].State = "Running")$
 $\wedge HiRTOS' = [HiRTOS \text{ EXCEPT } !.Interrupts_Enabled = FALSE]$
 $\wedge pc' = [pc \text{ EXCEPT } !["Timer_Interrupt"] = "track_time_slice"]$
 $\wedge \text{UNCHANGED } \langle Thread_Objects, Mutex_Objects,$
 $Condvar_Objects,$
 $Timer_Objects,$
 $Global_Resource_Available,$
 $stack, thread_id_ , mutex_id_ ,$
 $waking_up_thread_after_condvar_wait,$
 $owner_thread_id_ , thread_id_A,$
 $mutex_id_A, owner_thread_id,$
 $thread_id_D, mutex_id_D,$
 $doing_condvar_wait,$
 $awoken_thread_id_ ,$
 $thread_id_R, mutex_id_R,$
 $thread_id_Do, condvar_id_ ,$
 $mutex_id_Do, thread_id_W,$
 $condvar_id_W, mutex_id,$
 $condvar_id_D,$
 $do_context_switch,$
 $awoken_thread_id,$
 $to_reacquire_mutex_id,$
 $context_id_ , condvar_id_S,$
 $context_id, condvar_id,$
 $thread_was_awaken, thread_id,$
 $delayed_threads\}$

$track_time_slice \triangleq \wedge pc["Timer_Interrupt"] = "track_time_slice"$
 $\wedge \text{IF } HiRTOS.Current_Thread_Id \neq "Invalid_Thread_Id"$
 $\text{THEN } \wedge \text{Assert}(\neg Thread_Objects[HiRTOS.Current_Thread_Id].ghost_Time_Slice)$

$$\begin{array}{l}
\text{"Failure of assertion at line 606, column 13.")} \\
\wedge \textit{Thread_Objects}' = [\textit{Thread_Objects} \text{ EXCEPT } ![\textit{HiRTOS.Current_Thread_Id}]] \\
\text{ELSE } \wedge \text{TRUE} \\
\wedge \text{UNCHANGED } \textit{Thread_Objects} \\
\wedge \textit{delayed_threads}' = \{t \in \textit{Threads} \setminus \{\text{"Idle_Thread"}\} : \\
\quad \textit{Timer_Objects}[\textit{Thread_Objects}'[t].\textit{Builtin_Timer_Id}].\textit{State} = \text{"timer_interrupt_pending"}\} \\
\wedge \textit{pc}' = [\textit{pc} \text{ EXCEPT } ![\text{"Timer_Interrupt"}] = \text{"wakeup_delay_until_waiters"}] \\
\wedge \text{UNCHANGED } \langle \textit{HiRTOS}, \textit{Mutex_Objects}, \textit{Condvar_Objects}, \\
\quad \textit{Timer_Objects}, \textit{Global_Resource_Available}, \\
\quad \textit{stack}, \textit{thread_id}_-, \textit{mutex_id}_-, \\
\quad \textit{waking_up_thread_after_condvar_wait}, \\
\quad \textit{owner_thread_id}_-, \textit{thread_id}_A, \textit{mutex_id}_A, \\
\quad \textit{owner_thread_id}, \textit{thread_id}_D, \textit{mutex_id}_D, \\
\quad \textit{doing_condvar_wait}, \textit{awoken_thread_id}_-, \\
\quad \textit{thread_id}_R, \textit{mutex_id}_R, \textit{thread_id}_Do, \\
\quad \textit{condvar_id}_-, \textit{mutex_id}_Do, \textit{thread_id}_W, \\
\quad \textit{condvar_id}_W, \textit{mutex_id}_-, \textit{condvar_id}_D, \\
\quad \textit{do_context_switch}, \textit{awoken_thread_id}, \\
\quad \textit{to_reacquire_mutex_id}, \textit{context_id}_-, \\
\quad \textit{condvar_id}_S, \textit{context_id}, \textit{condvar_id}, \\
\quad \textit{thread_was_awaken}, \textit{thread_id} \rangle \\
\\
\textit{wakeup_delay_until_waiters} \triangleq \wedge \textit{pc}[\text{"Timer_Interrupt"}] = \text{"wakeup_delay_until_waiters"} \\
\wedge \text{IF } \textit{delayed_threads} \neq \{\} \\
\quad \text{THEN } \wedge \exists t \in \textit{delayed_threads} : \\
\quad \quad \wedge \textit{delayed_threads}' = \textit{delayed_threads} \setminus \{t\} \\
\quad \quad \wedge \textit{Timer_Objects}' = [\textit{Timer_Objects} \text{ EXCEPT } ![\textit{Thread_Objects}'[t].\textit{Builtin_Timer_Id}]] \\
\quad \quad \wedge \wedge \textit{condvar_id}_D' = [\textit{condvar_id}_D \text{ EXCEPT } ![\text{"Timer_Interrupt"}]] \\
\quad \quad \quad \wedge \textit{do_context_switch}' = [\textit{do_context_switch} \text{ EXCEPT } ![\text{"Timer_Interrupt"}]] \\
\quad \quad \quad \wedge \textit{stack}' = [\textit{stack} \text{ EXCEPT } ![\text{"Timer_Interrupt"}]] = \langle [\textit{prologue}], \\
\quad \quad \quad \quad \textit{pc}, \\
\quad \quad \quad \quad \textit{awoken_thread_id}, \\
\quad \quad \quad \quad \textit{to_reacquire_mutex_id}, \\
\quad \quad \quad \quad \textit{condvar_id}_D, \\
\quad \quad \quad \quad \textit{do_context_switch}, \\
\quad \quad \quad \quad \textit{awoken_thread_id}, \\
\quad \quad \quad \quad \textit{to_reacquire_mutex_id}, \\
\quad \quad \quad \quad \textit{context_id}, \\
\quad \quad \quad \quad \textit{condvar_id}_S, \\
\quad \quad \quad \quad \textit{thread_was_awaken} \rangle \\
\quad \quad \wedge \textit{awoken_thread_id}' = [\textit{awoken_thread_id} \text{ EXCEPT } ![\text{"Timer_Interrupt"}]] \\
\quad \quad \wedge \textit{to_reacquire_mutex_id}' = [\textit{to_reacquire_mutex_id} \text{ EXCEPT } ![\text{"Timer_Interrupt"}]] \\
\quad \quad \wedge \textit{pc}' = [\textit{pc} \text{ EXCEPT } ![\text{"Timer_Interrupt"}]] = \text{"signal_condvar_wait"} \\
\text{ELSE } \wedge \textit{pc}' = [\textit{pc} \text{ EXCEPT } ![\text{"Timer_Interrupt"}]] = \text{"timer_interrupt_as_applied"} \\
\wedge \text{UNCHANGED } \langle \textit{Timer_Objects}, \textit{stack}, \\
\quad \textit{condvar_id}_D, \\
\quad \textit{do_context_switch}, \\
\quad \textit{awoken_thread_id}, \\
\quad \textit{to_reacquire_mutex_id}, \\
\quad \textit{context_id}, \\
\quad \textit{condvar_id}_S, \\
\quad \textit{thread_was_awaken} \rangle
\end{array}$$

$$\begin{aligned}
& \text{delayed_threads} \rangle \\
\wedge \text{UNCHANGED } \langle & \text{HiRTOS, Thread_Objects,} \\
& \text{Mutex_Objects, Condvar_Objects,} \\
& \text{Global_Resource_Available,} \\
& \text{thread_id_}, \text{mutex_id_}, \\
& \text{waking_up_thread_after_condvar_wait,} \\
& \text{owner_thread_id_}, \text{thread_id_A,} \\
& \text{mutex_id_A, owner_thread_id,} \\
& \text{thread_id_D, mutex_id_D,} \\
& \text{doing_condvar_wait,} \\
& \text{awoken_thread_id_}, \text{thread_id_R,} \\
& \text{mutex_id_R, thread_id_Do,} \\
& \text{condvar_id_}, \text{mutex_id_Do,} \\
& \text{thread_id_W, condvar_id_W,} \\
& \text{mutex_id, context_id_}, \\
& \text{condvar_id_S, context_id,} \\
& \text{condvar_id, thread_was_awaken,} \\
& \text{thread_id} \rangle \\
\text{timer_interrupt_asynchronous_context_switch_step} \triangleq & \wedge pc["\text{Timer_Interrupt}"] = "\text{timer_interrupt_asynchronous_context_switch_step}" \\
& \wedge \text{stack}' = [\text{stack} \text{ EXCEPT } !["\text{Timer_Interrupt}"]] = \langle [\text{stack} \text{ EXCEPT } !["\text{Timer_Interrupt}"]] \rangle \\
& \wedge pc' = [pc \text{ EXCEPT } !["\text{Timer_Interrupt}"]] = "\text{check_timer_interrupt_asynchronous_context_switch_step}" \\
& \wedge \text{UNCHANGED } \langle \text{HiRTOS,} \\
& \text{Thread_Objects,} \\
& \text{Mutex_Objects,} \\
& \text{Condvar_Objects,} \\
& \text{Timer_Objects,} \\
& \text{Global_Resource_Available,} \\
& \text{thread_id_}, \\
& \text{mutex_id_}, \\
& \text{waking_up_thread_after_condvar_wait,} \\
& \text{owner_thread_id_}, \\
& \text{thread_id_A,} \\
& \text{mutex_id_A,} \\
& \text{owner_thread_id,} \\
& \text{thread_id_D,} \\
& \text{mutex_id_D,} \\
& \text{doing_condvar_wait,} \\
& \text{awoken_thread_id_}, \\
& \text{thread_id_R,} \\
& \text{mutex_id_R,} \\
& \text{thread_id_Do,} \\
& \text{condvar_id_},
\end{aligned}$$

$$\begin{aligned}
& \text{mutex_id_Do,} \\
& \text{thread_id_W,} \\
& \text{condvar_id_W,} \\
& \text{mutex_id,} \\
& \text{condvar_id_D,} \\
& \text{do_context_switch,} \\
& \text{awoken_thread_id,} \\
& \text{to_reacquire_mutex_id,} \\
& \text{context_id_}, \\
& \text{condvar_id_S,} \\
& \text{context_id,} \\
& \text{condvar_id,} \\
& \text{thread_was_awaken,} \\
& \text{thread_id,} \\
& \text{delayed_threads} \rangle \\
\text{exit_critical_section_step_T} & \triangleq \wedge pc["\text{Timer_Interrupt}"] = "\text{exit_critical_section_step_T}" \\
& \wedge \text{HiRTOS}' = [\text{HiRTOS} \text{ EXCEPT } !.\text{Interrupts_Enabled} = \text{TRUE}] \\
& \wedge pc' = [pc \text{ EXCEPT } !["\text{Timer_Interrupt}"]] = "\text{timer_interrupt_next_state_loop}" \\
& \wedge \text{UNCHANGED } \langle \text{Thread_Objects, Mutex_Objects,} \\
& \quad \text{Condvar_Objects, Timer_Objects,} \\
& \quad \text{Global_Resource_Available,} \\
& \quad \text{stack, thread_id_}, \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait,} \\
& \quad \text{owner_thread_id_}, \text{thread_id_A,} \\
& \quad \text{mutex_id_A, owner_thread_id,} \\
& \quad \text{thread_id_D, mutex_id_D,} \\
& \quad \text{doing_condvar_wait,} \\
& \quad \text{awoken_thread_id_}, \text{thread_id_R,} \\
& \quad \text{mutex_id_R, thread_id_Do,} \\
& \quad \text{condvar_id_}, \text{mutex_id_Do,} \\
& \quad \text{thread_id_W, condvar_id_W,} \\
& \quad \text{mutex_id, condvar_id_D,} \\
& \quad \text{do_context_switch,} \\
& \quad \text{awoken_thread_id,} \\
& \quad \text{to_reacquire_mutex_id,} \\
& \quad \text{context_id_}, \text{condvar_id_S,} \\
& \quad \text{context_id, condvar_id,} \\
& \quad \text{thread_was_awaken, thread_id,} \\
& \quad \text{delayed_threads} \rangle \\
\text{Timer_Interrupt} & \triangleq \text{timer_interrupt_next_state_loop} \\
& \vee \text{enter_critical_section_step_T} \vee \text{track_time_slice} \\
& \vee \text{wakeup_delay_until_waiters} \\
& \vee \text{timer_interrupt_asynchronous_context_switch_step}
\end{aligned}$$

$\vee \text{exit_critical_section_step_}T$

$$\begin{aligned} \text{other_interrupt_next_state_loop} \triangleq & \wedge pc["\text{Other_Interrupt}"] = \text{"other_interrupt_next_state_loop"} \\ & \wedge pc' = [pc \text{ EXCEPT } !["\text{Other_Interrupt}"] = \text{"enter_critical_section_step"}] \\ & \wedge \text{UNCHANGED } \langle \text{HiRTOS}, \text{Thread_Objects}, \\ & \quad \text{Mutex_Objects}, \\ & \quad \text{Condvar_Objects}, \\ & \quad \text{Timer_Objects}, \\ & \quad \text{Global_Resource_Available}, \\ & \quad \text{stack}, \text{thread_id_}, \\ & \quad \text{mutex_id_}, \\ & \quad \text{waking_up_thread_after_condvar_wait}, \\ & \quad \text{owner_thread_id_}, \\ & \quad \text{thread_id_}A, \text{mutex_id_}A, \\ & \quad \text{owner_thread_id_}, \\ & \quad \text{thread_id_}D, \text{mutex_id_}D, \\ & \quad \text{doing_condvar_wait}, \\ & \quad \text{awoken_thread_id_}, \\ & \quad \text{thread_id_}R, \text{mutex_id_}R, \\ & \quad \text{thread_id_}Do, \text{condvar_id_}, \\ & \quad \text{mutex_id_}Do, \text{thread_id_}W, \\ & \quad \text{condvar_id_}W, \text{mutex_id_}, \\ & \quad \text{condvar_id_}D, \\ & \quad \text{do_context_switch}, \\ & \quad \text{awoken_thread_id_}, \\ & \quad \text{to_reacquire_mutex_id_}, \\ & \quad \text{context_id_}, \text{condvar_id_}S, \\ & \quad \text{context_id_}, \text{condvar_id_}, \\ & \quad \text{thread_was_awaken}, \\ & \quad \text{thread_id_}, \text{delayed_threads} \rangle \end{aligned}$$

$$\begin{aligned} \text{enter_critical_section_step} \triangleq & \wedge pc["\text{Other_Interrupt}"] = \text{"enter_critical_section_step"} \\ & \wedge \text{HiRTOS.Interrupts_Enabled} \wedge \\ & \quad (\text{"Other_Interrupt"} \in \text{Threads} \Rightarrow \\ & \quad \quad \text{Thread_Objects}["\text{Other_Interrupt}"].\text{State} = \text{"Running"}) \\ & \wedge \text{HiRTOS}' = [\text{HiRTOS} \text{ EXCEPT } !.\text{Interrupts_Enabled} = \text{FALSE}] \\ & \wedge pc' = [pc \text{ EXCEPT } !["\text{Other_Interrupt}"] = \text{"other_interrupt_asynchronous_co..."}] \\ & \wedge \text{UNCHANGED } \langle \text{Thread_Objects}, \text{Mutex_Objects}, \\ & \quad \text{Condvar_Objects}, \text{Timer_Objects}, \\ & \quad \text{Global_Resource_Available}, \\ & \quad \text{stack}, \text{thread_id_}, \text{mutex_id_}, \\ & \quad \text{waking_up_thread_after_condvar_wait}, \\ & \quad \text{owner_thread_id_}, \text{thread_id_}A, \\ & \quad \text{mutex_id_}A, \text{owner_thread_id_}, \\ & \quad \text{thread_id_}D, \text{mutex_id_}D, \end{aligned}$$

$$\begin{aligned}
& \text{do_context_switch,} \\
& \text{awoken_thread_id,} \\
& \text{to_reacquire_mutex_id,} \\
& \text{context_id_}, \\
& \text{condvar_id_S,} \\
& \text{context_id,} \\
& \text{condvar_id,} \\
& \text{thread_was_awaken,} \\
& \text{thread_id,} \\
& \text{delayed_threads} \rangle \\
\text{exit_critical_section_step} & \triangleq \wedge pc["\text{Other_Interrupt}"] = "\text{exit_critical_section_step}" \\
& \wedge \text{HiRTOS}' = [\text{HiRTOS} \text{ EXCEPT } !.\text{Interrupts_Enabled} = \text{TRUE}] \\
& \wedge pc' = [pc \text{ EXCEPT } !["\text{Other_Interrupt}"] = "\text{other_interrupt_next_state_loop}"] \\
& \wedge \text{UNCHANGED } \langle \text{Thread_Objects, Mutex_Objects,} \\
& \quad \text{Condvar_Objects, Timer_Objects,} \\
& \quad \text{Global_Resource_Available, stack,} \\
& \quad \text{thread_id_}, \text{mutex_id_}, \\
& \quad \text{waking_up_thread_after_condvar_wait,} \\
& \quad \text{owner_thread_id_}, \text{thread_id_A,} \\
& \quad \text{mutex_id_A, owner_thread_id,} \\
& \quad \text{thread_id_D, mutex_id_D,} \\
& \quad \text{doing_condvar_wait,} \\
& \quad \text{awoken_thread_id_}, \text{thread_id_R,} \\
& \quad \text{mutex_id_R, thread_id_Do,} \\
& \quad \text{condvar_id_}, \text{mutex_id_Do,} \\
& \quad \text{thread_id_W, condvar_id_W,} \\
& \quad \text{mutex_id, condvar_id_D,} \\
& \quad \text{do_context_switch,} \\
& \quad \text{awoken_thread_id,} \\
& \quad \text{to_reacquire_mutex_id,} \\
& \quad \text{context_id_}, \text{condvar_id_S,} \\
& \quad \text{context_id, condvar_id,} \\
& \quad \text{thread_was_awaken, thread_id,} \\
& \quad \text{delayed_threads} \rangle \\
\text{Other_Interrupt} & \triangleq \text{other_interrupt_next_state_loop} \\
& \vee \text{enter_critical_section_step} \\
& \vee \text{other_interrupt_asynchronous_context_switch_step} \\
& \vee \text{exit_critical_section_step} \\
\text{Next} & \triangleq \text{Idle_Thread} \vee \text{Timer_Interrupt} \vee \text{Other_Interrupt} \\
& \vee (\exists \text{self} \in \text{ProcSet} : \vee \text{Run_Thread_Scheduler}(\text{self}) \\
& \quad \vee \text{Do_Acquire_Mutex}(\text{self}) \\
& \quad \vee \text{Acquire_Mutex}(\text{self}) \\
& \quad \vee \text{Do_Release_Mutex}(\text{self}))
\end{aligned}$$

$$\begin{aligned}
& \vee \text{Release_Mutex}(self) \\
& \vee \text{Do_Wait_On_Condvar}(self) \\
& \vee \text{Wait_On_Condvar}(self) \\
& \vee \text{Do_Signal_Condvar}(self) \\
& \vee \text{Signal_Condvar}(self) \\
& \vee \text{Broadcast_Condvar}(self) \\
& \vee \text{Delay_Until}(self)) \\
& \vee (\exists self \in \text{Threads} \setminus \{\text{"Idle_Thread"}\} : \text{Thread_State_Machine}(self)) \\
\text{Spec} \triangleq & \wedge \text{Init} \wedge \Box[\text{Next}]_{vars} \\
& \wedge \forall self \in \text{Threads} \setminus \{\text{"Idle_Thread"}\} : \wedge \text{WF}_{vars}(\text{Thread_State_Machine}(self)) \\
& \wedge \text{WF}_{vars}(\text{Acquire_Mutex}(self)) \\
& \wedge \text{WF}_{vars}(\text{Idle_Thread}) \\
& \wedge \wedge \text{WF}_{vars}(\text{Timer_Interrupt}) \\
& \wedge \text{WF}_{vars}(\text{Do_Signal_Condvar}(\text{"Timer_Interrupt"})) \\
& \wedge \text{WF}_{vars}(\text{Run_Thread_Scheduler}(\text{"Timer_Interrupt"})) \\
& \wedge \text{WF}_{vars}(\text{Do_Acquire_Mutex}(\text{"Timer_Interrupt"})) \\
& \wedge \wedge \text{WF}_{vars}(\text{Other_Interrupt}) \\
& \wedge \text{WF}_{vars}(\text{Run_Thread_Scheduler}(\text{"Other_Interrupt"}))
\end{aligned}$$

END TRANSLATION

Correctness Properties

$$\begin{aligned}
\text{TypeInvariant} & \triangleq \\
& \wedge \text{HiRTOS} \in \text{HiRTOS_Type} \\
& \wedge \text{Thread_Objects} \in [\text{Threads} \rightarrow \text{Thread_Object_Type}] \\
& \wedge \text{Mutex_Objects} \in [\text{Mutexes} \rightarrow \text{Mutex_Object_Type}] \\
& \wedge \text{Condvar_Objects} \in [\text{Condvars} \rightarrow \text{Condvar_Object_Type}] \\
& \wedge \text{Timer_Objects} \in [\text{Timers} \rightarrow \text{Timer_Object_Type}]
\end{aligned}$$

There can be at most only one "running" thread

$$\begin{aligned}
\text{SafetyInvariant1} & \triangleq \\
& \text{HiRTOS.Interrupts_Enabled} \Rightarrow \\
& \text{IF } \text{HiRTOS.Current_Thread_Id} \neq \text{"Invalid_Thread_Id"} \text{ THEN} \\
& \quad \wedge \text{Cardinality}(\{t \in \text{Threads} : \text{Thread_Objects}[t].\text{State} = \text{"Running"}\}) = 1 \\
& \quad \wedge \text{HiRTOS.Current_Thread_Id} = \\
& \quad \quad \text{CHOOSE } t \in \text{Threads} : \text{Thread_Objects}[t].\text{State} = \text{"Running"} \\
& \text{ELSE} \\
& \quad \{t \in \text{Threads} : \text{Thread_Objects}[t].\text{State} = \text{"Running"}\} = \{\}
\end{aligned}$$

Running thread not in any queue

$$\begin{aligned}
\text{SafetyInvariant2} & \triangleq \\
& (\text{HiRTOS.Interrupts_Enabled} \wedge
\end{aligned}$$

$$\begin{aligned}
& \text{HiRTOS.Current_Thread_Id} \neq \text{"Invalid_Thread_Id"} \Rightarrow \\
& (\wedge \text{Thread_Objects}[\text{HiRTOS.Current_Thread_Id}].\text{State} = \text{"Running"} \\
& \wedge \text{Thread_Objects}[\text{HiRTOS.Current_Thread_Id}].\text{Waiting_On_Condvar_Id} = \text{"Invalid_Condvar_Id"} \\
& \wedge \text{Thread_Objects}[\text{HiRTOS.Current_Thread_Id}].\text{Waiting_On_Mutex_Id} = \text{"Invalid_Mutex_Id"} \\
& \wedge \neg \text{Is_Thread_In_Priority_Queue}(\text{HiRTOS.Runnable_Threads_Queue}, \text{HiRTOS.Current_Thread_Id}) \\
& \wedge \forall m \in \text{Mutexes} : \\
& \quad \neg \text{Is_Thread_In_Priority_Queue}(\text{Mutex_Objects}[m].\text{Waiting_Threads_Queue}, \text{HiRTOS.Current_Thread_Id}) \\
& \wedge \forall cv \in \text{Condvars} : \\
& \quad \neg \text{Is_Thread_In_Priority_Queue}(\text{Condvar_Objects}[cv].\text{Waiting_Threads_Queue}, \text{HiRTOS.Current_Thread_Id}) \\
&)
\end{aligned}$$

All *Runnable* threads are in the *Runnable* threads queue and no other queue

SafetyInvariant3 \triangleq

$$\begin{aligned}
& \text{HiRTOS.Interrupts_Enabled} \Rightarrow \\
& \forall t \in \text{Threads} : \text{Thread_Objects}[t].\text{State} = \text{"Runnable"} \Rightarrow \\
& \wedge \text{Thread_Objects}[t].\text{Waiting_On_Condvar_Id} = \text{"Invalid_Condvar_Id"} \\
& \wedge \text{Thread_Objects}[t].\text{Waiting_On_Mutex_Id} = \text{"Invalid_Mutex_Id"} \\
& \wedge \text{Is_Thread_In_Priority_Queue}(\text{HiRTOS.Runnable_Threads_Queue}, t) \\
& \wedge \text{Is_Thread_In_Priority_Queue_In_Only_One_Queue}(\text{HiRTOS.Runnable_Threads_Queue}, t) \\
& \wedge \forall m \in \text{Mutexes} : \\
& \quad \neg \text{Is_Thread_In_Priority_Queue}(\text{Mutex_Objects}[m].\text{Waiting_Threads_Queue}, t) \\
& \wedge \forall cv \in \text{Condvars} : \\
& \quad \neg \text{Is_Thread_In_Priority_Queue}(\text{Condvar_Objects}[cv].\text{Waiting_Threads_Queue}, t)
\end{aligned}$$

Each thread blocked on a mutex in only one mutex's wait queue and no other queue

SafetyInvariant4 \triangleq

$$\begin{aligned}
& \text{HiRTOS.Interrupts_Enabled} \Rightarrow \\
& \forall t \in \text{Threads} : \\
& \quad \text{LET} \\
& \quad \quad \text{thread_obj} \triangleq \text{Thread_Objects}[t] \\
& \quad \text{IN} \\
& \quad \text{thread_obj.State} = \text{"Blocked_On_Mutex"} \Rightarrow \\
& \quad \wedge \text{thread_obj.Waiting_On_Mutex_Id} \neq \text{"Invalid_Mutex_Id"} \\
& \quad \wedge \text{thread_obj.Waiting_On_Condvar_Id} = \text{"Invalid_Condvar_Id"} \\
& \quad \wedge (\text{LET} \\
& \quad \quad \text{mutex_obj} \triangleq \text{Mutex_Objects}[\text{thread_obj.Waiting_On_Mutex_Id}] \\
& \quad \text{IN} \\
& \quad \quad \wedge \text{Is_Thread_In_Priority_Queue}(\text{mutex_obj.Waiting_Threads_Queue}, t) \\
& \quad \quad \wedge \text{Is_Thread_In_Priority_Queue_In_Only_One_Queue}(\text{mutex_obj.Waiting_Threads_Queue}, t) \\
& \quad \wedge \neg \text{Is_Thread_In_Priority_Queue}(\text{HiRTOS.Runnable_Threads_Queue}, t) \\
& \quad \wedge \forall m \in \text{Mutexes} \setminus \{\text{thread_obj.Waiting_On_Mutex_Id}\} : \\
& \quad \quad \neg \text{Is_Thread_In_Priority_Queue}(\text{Mutex_Objects}[m].\text{Waiting_Threads_Queue}, t) \\
& \quad \wedge \forall cv \in \text{Condvars} : \\
& \quad \quad \neg \text{Is_Thread_In_Priority_Queue}(\text{Condvar_Objects}[cv].\text{Waiting_Threads_Queue}, t)
\end{aligned}$$

Each thread blocked on a condvar is in only one condvar's wait queue and no other queue

$SafetyInvariant5 \triangleq$
 $HiRTOS.Interrupts_Enabled \Rightarrow$
 $\forall t \in Threads :$
 LET
 $thread_obj \triangleq Thread_Objects[t]$
 IN
 $thread_obj.State = \text{"Blocked_On_Condvar"} \Rightarrow$
 $\wedge thread_obj.Waiting_On_Condvar_Id \neq \text{"Invalid_Condvar_Id"}$
 $\wedge thread_obj.Waiting_On_Mutex_Id = \text{"Invalid_Mutex_Id"}$
 $\wedge (LET$
 $condvar_obj \triangleq Condvar_Objects[thread_obj.Waiting_On_Condvar_Id]$
 IN
 $\wedge Is_Thread_In_Priority_Queue(condvar_obj.Waiting_Threads_Queue, t)$
 $\wedge Is_Thread_In_Priority_Queue_In_Only_One_Queue(condvar_obj.Waiting_Threads_Queue, t)$
 $\wedge \neg Is_Thread_In_Priority_Queue(HiRTOS.Runnable_Threads_Queue, t)$
 $\wedge \forall cv \in Condvars \setminus \{thread_obj.Waiting_On_Condvar_Id\} :$
 $\neg Is_Thread_In_Priority_Queue(Condvar_Objects[cv].Waiting_Threads_Queue, t)$
 $\wedge \forall m \in Mutexes :$
 $\neg Is_Thread_In_Priority_Queue(Mutex_Objects[m].Waiting_Threads_Queue, t)$

Each mutex that is currently owned by a thread must be in the list of mutexes owned by that thread

$SafetyInvariant6 \triangleq$
 $HiRTOS.Interrupts_Enabled \Rightarrow$
 $\forall m \in Mutexes :$
 LET
 $t \triangleq Mutex_Objects[m].Owner_Thread_Id$
 IN
 $t \neq \text{"Invalid_Thread_Id"} \Rightarrow$
 $m \in Range(Thread_Objects[t].Owned_Mutexes)$

If a mutex is not owned by a thread, its wait queue should be empty

$SafetyInvariant7 \triangleq$
 $HiRTOS.Interrupts_Enabled \Rightarrow$
 $\forall m \in Mutexes :$
 $Mutex_Objects[m].Owner_Thread_Id = \text{"Invalid_Thread_Id"} \Rightarrow$
 $Is_Thread_Priority_Queue_Empty(Mutex_Objects[m].Waiting_Threads_Queue)$

The thread owning a mutex can never have lower priority than any thread waiting for the mutex

$SafetyInvariant8 \triangleq$
 $(HiRTOS.Interrupts_Enabled \wedge$
 $HiRTOS.Current_Thread_Id \neq \text{"Invalid_Thread_Id"}) \Rightarrow$
 $\forall m \in Mutexes :$
 LET
 $t \triangleq Mutex_Objects[m].Owner_Thread_Id$
 $prio_queue \triangleq Mutex_Objects[m].Waiting_Threads_Queue$

$$\begin{aligned}
& \text{IN} \\
& (t \neq \text{"Invalid_Thread_Id"} \wedge \neg \text{Is_Thread_Priority_Queue_Empty}(\text{prio_queue})) \Rightarrow \\
& \quad \forall wt \in \text{UNION } \{ \text{Range}(q) : q \in \text{Range}(\text{prio_queue}) \} : \\
& \quad \quad \text{Thread_Objects}[wt].\text{State} = \text{"Blocked_On_Mutex"} \wedge \\
& \quad \quad \text{Thread_Objects}[t].\text{Current_Priority} \geq \text{Thread_Objects}[wt].\text{Current_Priority}
\end{aligned}$$

A thread not owning any mutex and not waiting on a condvar always has its current priority set to its base priority

$$\begin{aligned}
& \text{SafetyInvariant9} \triangleq \\
& \quad \text{HiRTOS.Interrupts_Enabled} \Rightarrow \\
& \quad \forall t \in \text{Threads} : \\
& \quad \quad (\text{Thread_Objects}[t].\text{Owned_Mutexes} = \langle \rangle \wedge \text{Thread_Objects}[t].\text{State} \neq \text{"Blocked_On_Condvar"}) \Rightarrow \\
& \quad \quad \text{Thread_Objects}[t].\text{Current_Priority} = \text{Thread_Objects}[t].\text{Base_Priority}
\end{aligned}$$

$$\begin{aligned}
& \text{LivenessProperty1} \triangleq \\
& \quad \neg \text{HiRTOS.Interrupts_Enabled} \Rightarrow \Diamond \text{HiRTOS.Interrupts_Enabled}
\end{aligned}$$

$$\begin{aligned}
& \text{LivenessProperty2} \triangleq \\
& \quad \forall t \in \text{Threads} \setminus \{ \text{"Idle_Thread"} \}, p \in \text{Valid_Thread_Priority_Type} \setminus \{0\} : \\
& \quad \quad (\text{Thread_Objects}[t].\text{Current_Priority} = p \wedge \text{Thread_Objects}[t].\text{State} = \text{"Runnable"}) \Rightarrow \\
& \quad \quad \Diamond (\text{Thread_Objects}[t].\text{State} = \text{"Running"})
\end{aligned}$$

$$\begin{aligned}
& \text{LivenessProperty3} \triangleq \\
& \quad \forall t \in \text{Threads} \setminus \{ \text{"Idle_Thread"} \} : \\
& \quad \quad \Diamond (pc[t] = \text{"thread_iteration_completed_step"})
\end{aligned}$$

$$\begin{aligned}
& \text{LivenessProperty4} \triangleq \\
& \quad \forall t \in \text{Threads} : \\
& \quad \quad \text{Thread_Objects}[t].\text{State} = \text{"Waiting_On_Mutex"} \Rightarrow \Diamond (\text{Thread_Objects}[t].\text{State} = \text{"Runnable"})
\end{aligned}$$

$$\begin{aligned}
& \text{LivenessProperty5} \triangleq \\
& \quad \forall t \in \text{Threads} : \\
& \quad \quad \text{Thread_Objects}[t].\text{State} = \text{"Waiting_On_Condvar"} \Rightarrow \Diamond (\text{Thread_Objects}[t].\text{State} = \text{"Runnable"})
\end{aligned}$$

THEOREM $\text{Spec} \Rightarrow \Box \text{TypeInvariant}$
 THEOREM $\text{Spec} \Rightarrow \Box \text{SafetyInvariant1}$
 THEOREM $\text{Spec} \Rightarrow \Box \text{SafetyInvariant2}$
 THEOREM $\text{Spec} \Rightarrow \Box \text{SafetyInvariant3}$
 THEOREM $\text{Spec} \Rightarrow \Box \text{SafetyInvariant4}$
 THEOREM $\text{Spec} \Rightarrow \Box \text{SafetyInvariant5}$
 THEOREM $\text{Spec} \Rightarrow \Box \text{SafetyInvariant6}$
 THEOREM $\text{Spec} \Rightarrow \Box \text{SafetyInvariant7}$
 THEOREM $\text{Spec} \Rightarrow \Box \text{SafetyInvariant8}$
 THEOREM $\text{Spec} \Rightarrow \Box \text{SafetyInvariant9}$
 THEOREM $\text{Spec} \Rightarrow \Box \text{LivenessProperty1}$
 THEOREM $\text{Spec} \Rightarrow \Box \text{LivenessProperty2}$
 THEOREM $\text{Spec} \Rightarrow \Box \text{LivenessProperty3}$

THEOREM $Spec \Rightarrow \Box LivenessProperty4$

THEOREM $Spec \Rightarrow \Box LivenessProperty5$
