

Bitcell

wpg, lpg, wpd, lpd, wpu, lpu

N/K Buffer Chains

Nstages, NFanOut

Kstages, KFanOut

Timing Block

dlyWLstages, dlyWLFanOut

ICLKstages, ICLKFanOut

NSAPRECstages, NSAPRECFanOut

WLENstages, WLENFanOut

SAEstages, SAEFanOut

WENstages, WENFanOut

NPRECHstages, NPRECHFanOut

Sense Amp

SAwen, SAlen, SAweql, SAl eql, SAwpsa, SAlpsa, SAwnsa, SAlnsa, SAwbl, SAlbl, SAsapc, SAlsapc, SAwpNAND, SAlpNAND, SAwnNAND, SAlnNAND

Column DEMUX

wpcCD, lpcCD, wpTXCD, lpTXCD, wnTXCD, lnTXCD

IOBlock

TSIwpINV, TSIwnINV, TSIwpEN, TSIwnEN, TSIlpINV, TSIl nINV, TSIlpEN, TSIl nEN

IOwpINV, IOwnINV, IOlpINV, IOlnINV

Top Level Timing

CSELstages, CSELFanOut