## TASE User Guide

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## 1 Quick Start

## 2.1.1 device/BIN

The subPU/PD/PG.scs files can be used to describe the SRAM specific pull-up, pull-

...
// Half cell
subckt HALFCELL (IN OUT BL WL VDD VBP VSS VBN)
MP (OUT IN VDD VBP) P\_TRANSISTOR width=wpu length=lpu
MN (OUT IN VSS VBN) N\_TRANSISTOR width=wpd length=lpd