

Related topics

Carbon film resistor, metallic film resistor, PTC, NTC; Z diode, avalanche effect, Zener effect, charge carrier generation, free path, Mathies rule.

Principle

The temperature dependence of an electrical parameter (e.g. resistance, conducting-state voltage, blocking voltage) of different components is determined. To do this, the immersion probe set is immersed in a water bath and the resistance is measured at regular temperature intervals.

Equipment

Immersion probes f. determining α	07163.00	1
Immersion thermostat TC10	08492.93	1
Bath for thermostat, Makrolon	08487.02	1
Accessory set for TC10	08492.01	1
Digital multimeter	07128.00	1
Power supply 0-12 V DC/6 V, 12 V AC	13505.93	1
PEK carbon resistor 1 W 5% 4.7 kOhm	39104.27	1
Connection box	06030.23	1
Connecting cord, $l = 500$ mm, blue	07361.04	1

Connecting cord, $l = 750$ mm, red	07362.01	2
Connecting cord, $l = 750$ mm, blue	07362.04	2

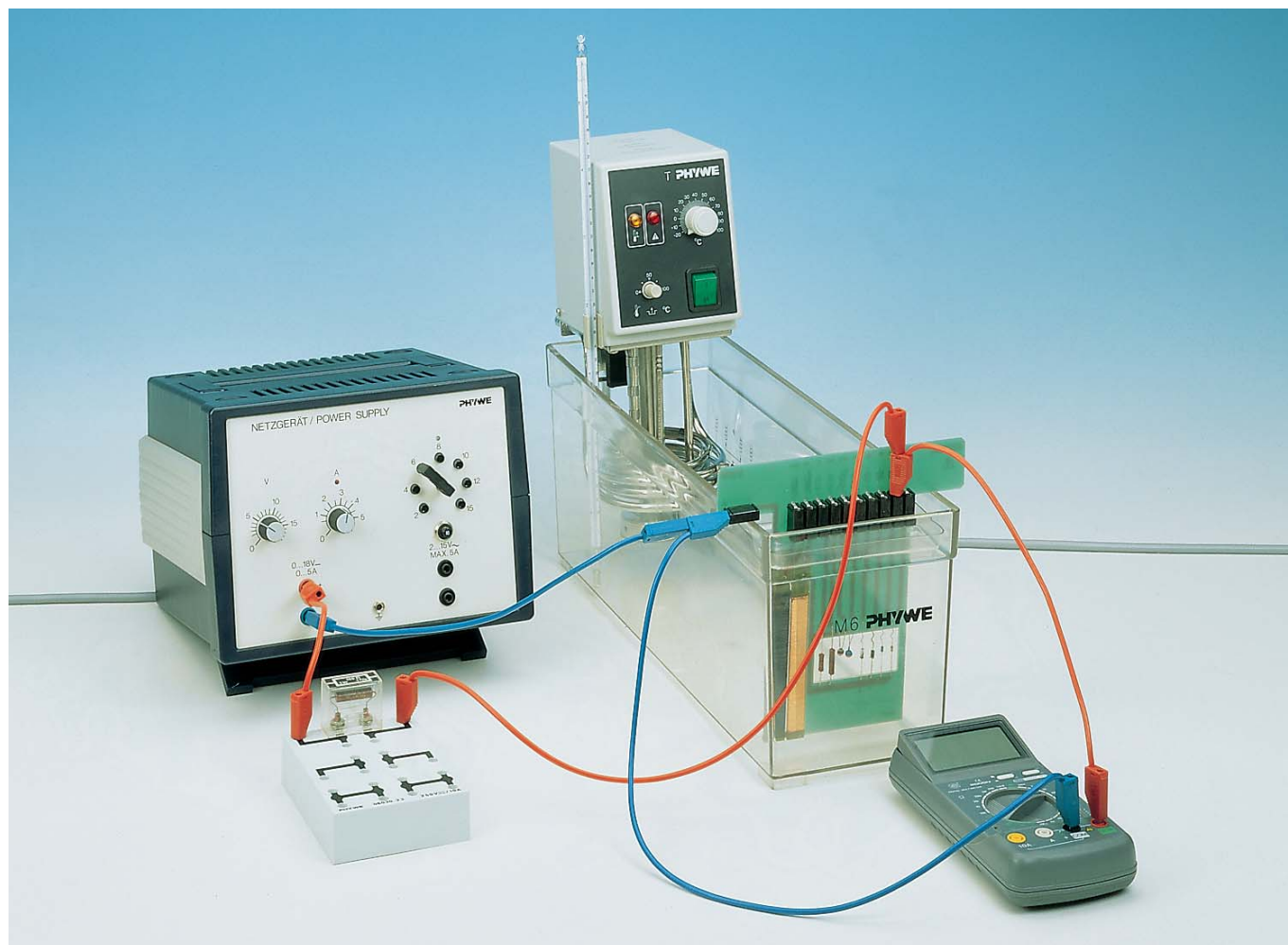
Tasks

1. Measurement of the temperature dependence of the resistance of different electrical components.
2. Measurement of the temperature dependence of the conducting state voltage of semiconducting diodes.
3. Measurement of the temperature dependence of the voltage in the Zener and the avalanche effects.

Set-up and procedure

1. Place the immersion probe set, which is enclosed in a watertight plastic bag, into the water bath. The resistance values for the PTC, NTC, metallic film and carbon film resistors, as well as the Cu and CuNi wire resistors, can be measured directly with the digital hand multimeter (circuit diagram, Fig. 2). To do this, connect the multimeter to the ground jack, which is connected to all the components, and the jack located under the symbol corresponding to the respective component. Note the different resistance values, and plot them as a function of temperature.

Fig. 1. Photograph of the experimental set-up.



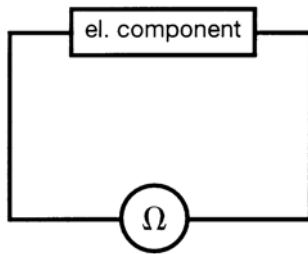


Fig. 2

2. In order to measure the conducting-state voltage of the semiconducting diodes, connect them to a voltage of 10 V. Connect a 4.7Ω resistor in series with the component. Set a voltage of 10 V on the universal power supply, and adjust the current limiter to its maximum value. Measure the voltage parallel to the component. Note the conducting-state voltage corresponding to the respective temperatures.

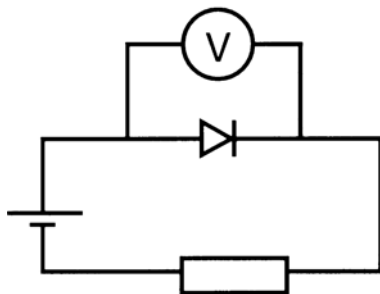


Fig. 3

3. Also measure the blocking voltage for the Zener and avalanche effects with the set-up illustrated in Fig. 3. However, the diodes have already been wired in the blocking direction through their placement in the immersion probe set.

Theory and Evaluation

In restricted temperature ranges the change in the resistance of the electrical components can be assumed to be linear. In these regions, the general formula for the dependence of the resistance on the temperature is valid

$$R(T) = R_{20} + R_{20} \cdot \alpha \cdot (T - 20^\circ\text{C})$$

where $R(T)$ = Resistance at temperature T

R_{20} = Resistance at 20°C

α = Temperature coefficient

T = Temperature at time of measurement

By rearranging and substituting the measured values the temperature coefficient can be determined using the formula.

1. In copper wire the free path of the electrons in the electron vapour, which contribute to charge transport, becomes shorter with increasing temperature. The change in resistance can be clearly seen: the resistance increases. The result is a positive temperature coefficient

$$\alpha_{\text{Cu}} = 5.3 \cdot 10^{-3} / \text{K}$$

The resistance of the CuNi wire is nearly constant over the measured range. This is in accordance with Mathies rule, which states that $R_{\text{tot}} = R_{20} + R(T)$. The change in the resistance with the temperature is very slight in the measured temperature range. Consequently, the absolute resistance (R_{20}) is predominant. This experiment provides a negative temperature coefficient of

$$\alpha_{\text{CuNi}} = -1.4 \cdot 10^{-4} / \text{K}$$

In the carbon-layer resistor, the absolute resistance is very high to begin with. The change with the temperature is, as is the case with CuNi, small and has practically no effect. A negative temperature coefficient results

$$\alpha_{\text{C}} = -2.3 \cdot 10^{-3} / \text{K}$$

The metallic layer resistor also has a relatively high absolute resistance at 20°C . And the change in the measured temperature range is even lower than for carbon. Thus, the temperature coefficient approaches zero.

$$\alpha_{\text{met}} \Rightarrow 0$$

The two NTC and PTC resistors consist of alloys. Depending on their compositions, great changes in resistance can be realised in a small temperature range. The curves that are recorded in this experiment can no longer be considered linear. They serve only to illustrate the behaviour of NTC and PTC resistors.

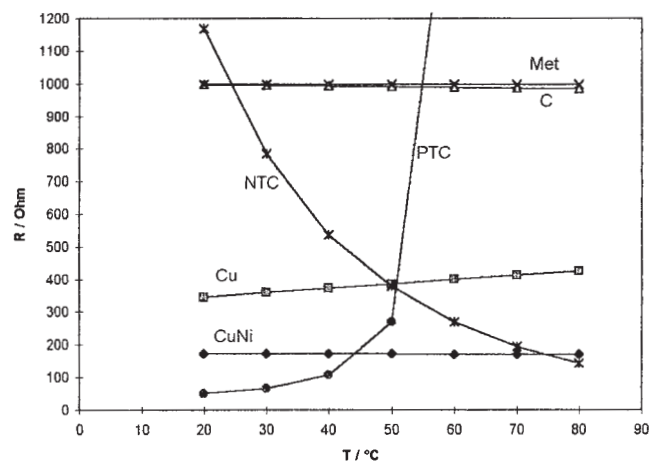


Fig. 4: Diagram of resistances.

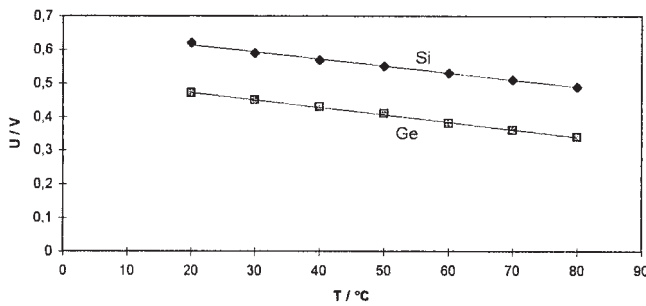


Fig. 5: Diagram for the semiconductors Si and Ge.

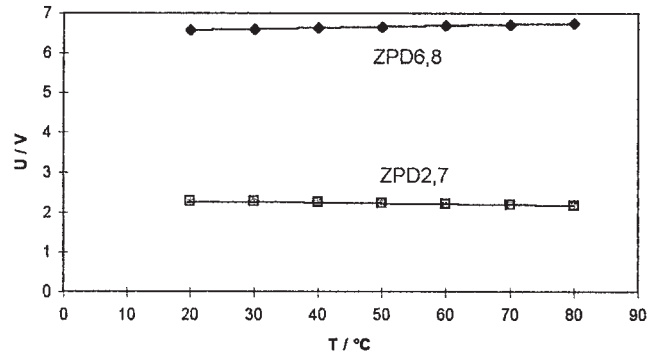


Fig. 6: Diagram for the Z-diodes

Literature values:

$$\alpha_{\text{Cu}} = 4.0 \cdot 10^{-3} / \text{K}$$

$$\alpha_{\text{CuNi}} = -3.0 \cdot 10^{-3} / \text{K}$$

$$\alpha_{\text{C}} = -2.4 \cdot 10^{-4} / \text{K}$$

$$\alpha_{\text{met}} \pm 0.50 \cdot 10^{-6} / \text{K}$$

$$\alpha_{\text{NTC}} = -6.15 \% / \text{K}$$

$$\alpha_{\text{PTC}} = 20 \% / \text{K}$$

The value for PTC is valid in the steepest region of the characteristic line.

- In semiconductors the number of charge carriers and the charge carrier density increases with temperature (charge carrier generation, electron-hole pair formation). From the law

$$\sigma = e \cdot n \cdot \mu$$

where σ = Intrinsic conductivity

e = Elementary charge

n = Charge carrier density

μ = Mobility

one can see that the intrinsic conductivity of the semiconductor thus increases. The mobility indeed decreases with increasing temperature, but the increase in the charge carrier density compensates for this effect. A definite drop in resistance is observed; this allows one to infer that there is a negative temperature coefficient. Through the calculation with the above-mentioned formula for the temperature dependence, rearranged for the voltage U_p , the following values are obtained.

$$\alpha_{\text{Si}} = -3.4 \cdot 10^{-3} / \text{K}$$

$$\alpha_{\text{Ge}} = -4.6 \cdot 10^{-3} / \text{K}$$

- At low voltages, around 3 V, a Zener breakdown occurs in Z diodes. As a result of the strong electric field, electron-hole pairs are spontaneously generated in the inner electron shells in the barrier-layer zone. Under the influence of the field charge carrier, they cross the barrier layer. A higher temperature increases the energy of the bound charge carriers. As a consequence, the Zener effect can occur at lower voltages. In the avalanche effect, the charge carriers are accelerated by the electric field to such a great degree that they in turn release other charge carriers on colliding with other atoms, which in turn are accelerated. The higher temperature shortens the free path, so that the voltage must increase with the temperature in order to continue to release charge carriers. From the calculations, the following values result for α :

$$\alpha_{\text{ZPD2.7}} = -7.3 \cdot 10^{-4} / \text{K}$$

$$\alpha_{\text{ZPD6.8}} = +4.5 \cdot 10^{-4} / \text{K}$$

Literature values:

$$\alpha_{\text{ZPD2.7}} = -9 \dots -4 \cdot 10^{-4} / \text{K}$$

$$\alpha_{\text{ZPD6.8}} = +2 \dots +7 \cdot 10^{-4} / \text{K}$$

