
SPECIFICATION

Code mobility in Networked Embedded System

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Group 4

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1 Project Outline

1.1 Organization

1.2 Project Description

1.3 Workpackages

1.4 Milestones and timeplan

2 Requirements

3 Specification and design

4 Implementation

5 Validation

6 Results and future plans

6.1 Purpose

The specification defines the goals that should meet the project "Code mobility in Networked Embedded system". It is written by project members listed on the title page to precisely identify the goals to meet at the end of the project. The purpose of the project is to build code mobility

platform on Embedded system engineering board [2]. Our goal is to develop the system that allows users to build and execute simple agent program on top of hardware ESE platform. To achieve the goal we propose to develop three layered structure: agent layer, platform layer, communication layer. Developer of the agent program should not aware of location of the services available on the platform and details about method to access the particular service. Given list of services agent is executed on a platform that is

6.2 Definitions, Acronyms, and Abbreviations

By *code mobility* we mean the capability of code to change the location where it is executed.

Strong code mobility is the ability to allow migration of both code and execution state to the destination, *weak* code mobility allows code transfer but it does not involve the transfer of the execution state.

Platform is a component that provides corresponding hardware services to

6.3 Background

The research has been done to use code mobility in distributed environment [1] and various application has been developed including [3] web application platform that allows people without major programming experience to develop the application as work-flow specification in graphical form. The use of code mobility is to "move the knowledge close to the resources" [4] and enable higher flexibility of accessing remote resources.

7 Requirements

Define requirements for the project "Code mobility in Networked Embedded system"

1 User roles

- 1.1 Application Developers (Tasks: Create control application in agent language, debug, test, prepare deployment packages)
- 1.2 Application Consumers (Tasks: Deploy control application on target system, fill valuable bug reports)
- 1.3 Plattform Developers (Tasks: Maintenance, Extensions, Porting to another target board)
- 1.4 Maybe: Application Designers (Tasks: Design control application)

Global Requirements:

2 Application development requirements:

- 2.1 The App Developer should be enabled to instantiate up to 4 agents on a single node, which are running concurrently.
- 2.2 The App Developer should be allowed to configure the execution scheduling of the agents via a prioritization of the agents.
- 2.3 The platform should provide a simple agent programming language to the App Developer in which the agents of an application can be developed.
- 2.4 The agent language should provide the App Developer with the possibility to reproduce its code on another node or on another board.
- 2.5 The agent language should provide the App Developer with the possibility to communicate with another agent on the same board.
- 2.6 The agent language should provide the App Developer with the possibility to access the node hardware.
- 2.7 The agent language should provide the App Developer with the possibility to implement loops.
- 2.8 The agent language should provide the App Developer with the possibility to compare variables.
- 2.9 The agent language should provide the App Developer with the possibility to perform addition, subtraction, multiplication and division on variables.
- 2.10 The agent language should provide the App Developer with the possibility to perform delays in the execution of code.
- 2.11 The platform should allow debugging of agents executions.
- 2.12 The platform should provide means for the creation of easily installable deployment packages.

3 Application Consumers requirements:

- 3.1 The platform should provide means to deploy the agent software on the target boards easily.
- 3.2 A tracing mechanism should be provided in order to ease the process of fault detection and to allow valuable bug descriptions.

4 Non-functional requirements

- 4.1 The platform should be open to extensions i.e adding new hardware.
- 4.2 The agent language should be extendable.
- 4.3 Scalability
- 4.4 Documentation
- 4.5 A platform tracing mechanism should be provided which allows for more efficient bugfixing.

5 What do the Application Designers need:

- 5.1 A description of the platform possibilities and limitations should be provided.
- 5.2 The platform should provide means for reducing the overall complexity of a system, by allowing encapsulation of different tasks.
- 5.3 The platform should provide configurable inter agent communication facilities.
- 5.4 The platform should provide means to enable standby scenarios by allowing dynamical code reproduction.
- 5.5 The platform should provide means for strong mobility, where an agent and its execution state are transferred to a new node or board and the execution on the new destination is started from the memorized state.
- 5.6 A description of a platform should provide a list of all available services

8 Specification

8.1 General

The following figure depicts the general outline of the code mobility project.

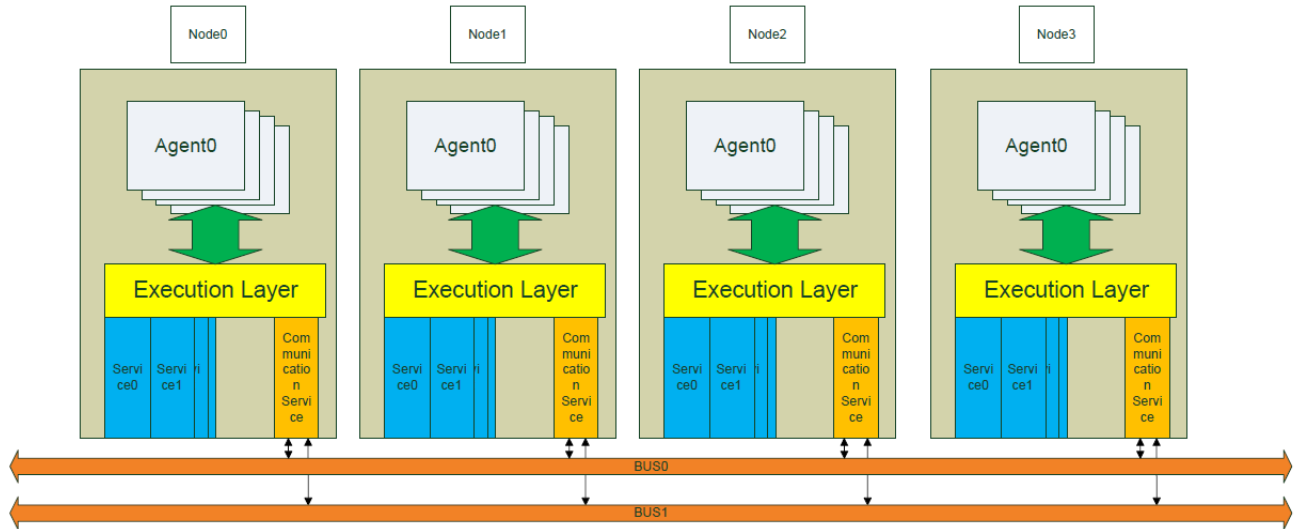


Figure 8.1: Overview

On each of the 4 nodes, which can be found on the ESE board, a virtualization platform will be deployed. This virtualization platform will be able to execute up to 4 agents concurrently. The agents will be programmed in a simplistic assembler like agent language. On the platform there will be an execution layer which is able to execute the agent language. The agents will be able to access the hardware attached to a node via services which are provided by the virtualization platform. Additionally the agents can reproduce themselves to another node or even board. Within the platform a scheduler will be responsible for providing execution time to each of the agents according to their priority.

8.2 Virtualization Platform

The main task of the virtualization platform is to interpret the agent language commands of the agents and to provide them access to the hardware attached to a node via well defined interfaces. Additionally the platform should allow the concurrent execution of the agents. Therefore some basic means for code and data protection for the agent memory is required. This is achieved by assigning each of the agents an own memory segment and not allowing any other agent to access any other memory but its own. If some collaboration between the agents is required this must be requested via the communication service. The metadata of an agent as its code and memory segment will be stored in a structure that is shown in the figure below.

Every agent has a unique id within the virtualization platform. Additionally a priority and a status for the scheduler are stored. Assigning these values for an agent lies within the scope of an agent developer. Reproducing an agent on a virtualization platform where the agent's id

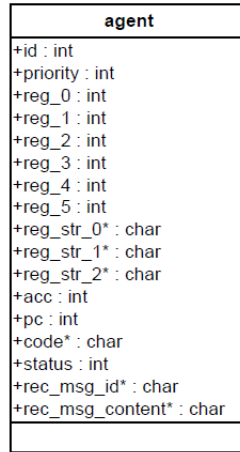


Figure 8.2: Agent structure

is already used will result in a denial of the reproduction by the platform. Every agent has 6 numerical general purpose registers used for the execution of the agent language. Additionally there are 3 char general purpose registers. The result of every agent language command will be written to the accumulator. There is also a program counter which is used for the execution of the agent and the numerical agent language representation is stored as well. The agent structure also contains a buffer for receiving messages from other agents and variable to store the id of the received message.

In order to reproduce the agent on another board or node the agent's structure needs to be serialized and transmitted via the communication layer.

Additionally the virtualization platform has to provide the agent developer with some means to deploy the agent executable to the virtualization platform, during compilation of the platform. During the initialization of the platform all deployed agents should be instantiated on the given platform.

8.3 Execution Layer

The execution layer is responsible for the execution of an agent which is written in the agent language and later translated to agent opcodes. The agent language provides means for:

- storing values to the general purpose registers
- comparing the contents of the general purpose registers
- performing basic mathematical functions like addition, subtraction, multiplication and division
- a jump operation
- reproduction and cloning functions
- sleep, delay and terminate functions
- functions to access the hardware attached to a node

If a function of the agent language returns a value, this value will be stored in the accumulator, where it can be used later on for further operations e.g. comparison etc.

The basic workflow of the execution layer as soon it is called by the scheduler is to read the next agent language opcode (all agent opcodes have a fixed length) as identified by the program counter, to decode it and to perform the function which is described by the opcode. Eventually the program counter value is changed and the control is returned back to the scheduler.

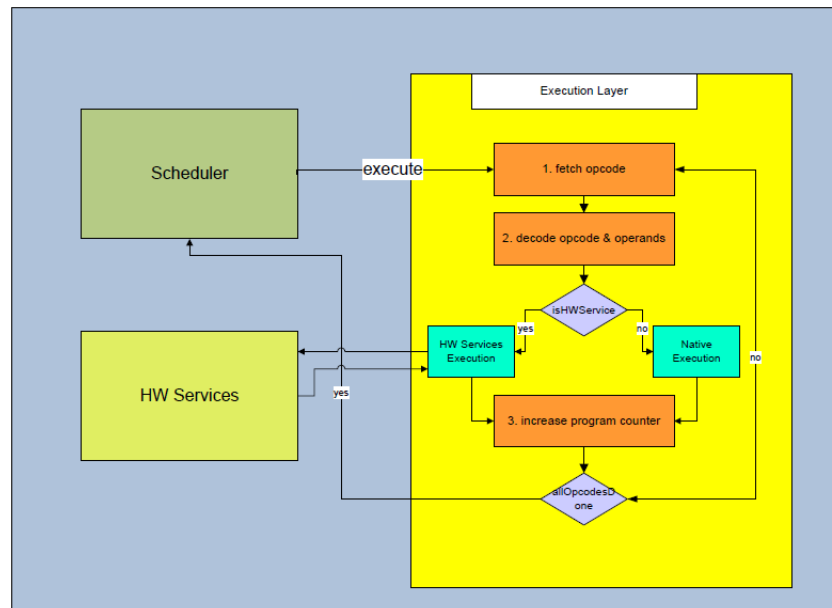


Figure 8.3: Execution Layer

The execution layer is called by the method `execute` which takes the following input parameters:

- Pointer to a specific agent structure
- Number of opcodes to execute

```

/**
    Function : execute
               excutes the next opcodes of an agent
    Returns :
               the amount of successfully executed opcoded
    Parameters :
               agent : agent status structure
               opcode: amount of opcodes to be executed
**/
uint8_t execute(agent_t* agent, uint8_t opcode)

```

Figure 8.4: Executing agent language opcode

8.4 Hardware Services

The virtualization platform provides access to the hardware attached to a node via according hardware device drivers. The methods of the device drivers are made public to the execution

layer which in turns allows the agents to access these methods. As the hardware supported by a node differs from node to node the virtualization platform should be able to discover during its initialization which hardware is supported on the node where it's running.

This will be achieved by defining a global set of function pointers within the virtualization platform. This set should contain all possible methods of all available device drivers. During initialization the platform will assign the according function pointer to a method provided by the device driver if the device is supported, otherwise the according function pointer will stay null.

```
typedef struct {

    void (*init_bargraph)(void);
    void (*set_bargraph)(uint8_t value);

    void (*clk_init)(void);
    uint32_t (*clk_get_time)(void);

    void (*init_cooler)(void);
    void (*set_cooler)(uint8_t duty_cycle);

    void (*DISPLAY_init)(void);
    void (*DISPLAY_drawBg)(uint16_t rgb);
    uint8_t (*DISPLAY_drawElement)(uint8_t row, uint8_t col,
                                   uint16_t rgb, DisplayObject_t object);

    void (*DISPLAY_drawBorder)(void);

    uint8_t (*DISPLAY_drawPoints)(uint8_t player_id,
                                  uint8_t points);

    void (*heater_init)(void);
    void (*heater_set)(uint8_t duty_cycle);

    void (*init_pushbutton0)(void (*callback)(void));
    void (*init_pushbutton1)(void (*callback)(void));

    void (*therm_init)(void);
    int16_t (*therm_get_temp)(uint8_t name);

} drivers_t;
```

Figure 8.5: Device drivers methods

All device drivers should support init methods, which will be called by the platform during its initialization. Even if a hardware device is not supported on a given node, there should be at least a dummy device driver for it present, providing an empty init method. A real device driver should register its methods to the global function pointers during its `init_driver` method execution. Choosing this approach we would reach some form of modularity which would allow us to exchange to device drivers without necessity to change the platform code.

If an interaction with a device driver is blocking, then the calling agent will be put to status blocking unless there is an answer from the device driver.

8.5 Communication Layer

The agents should be able to communicate with other agents on the same node or on the same board. Therefore the agent language provides means to request the sending or receiving of a message.

The sending function is blocking the further execution of the agent until the message is sent respectively received. When an agent wants to send a message this message is proceeded to the communications service which takes care of the actual transmission. While the sending procedure is ongoing the further execution of the sending agent is blocked. Its status in the agent structure is set to “blocked”. As soon as the communication service signalizes a successful message transmission or a failure the result of the sending function is written to the accumulator and the agent will be made available for further execution.

When an agent sends a message to another agent, the receiving platform stores the id of the message and its content to the receiver agent structure. The receiving agent is able to retrieve the last message from its buffer. However only one message can be stored within the receiving agent structure and the next message will overwrite the content and possible the id of the last message.

The communication service provides no guarantees that sending of a message will succeed; it works on a best effort approach. Therefore the agent developer has to make sure by reading the return value of a sending operation whether the message was successfully sent or not and should initialize a retransmission in case of failure.

Every message sent should be identified by an id, in order to allow the transmission of messages with different semantics.

The receiver of a message should be identified via the node number (0..3) where the receiving agent is currently expected to be running and the receiver agent id. As the ids of agents are within the scope of the agent developer she has to make sure, that the correct receiving agent is addressed. Additionally a multicast message could be supported by allowing omitting the node address which should result in sending the message to all agents identified by the provided id.

8.6 Scheduler

The main task of the scheduler which is part of the virtualization platform is to identify the next agent to be executed and to utilize the execution layer to perform the execution of the according agent. The decision which agent to be chosen should be made on a static priority based scheduling policy.

Every agent is assigned a priority (0..3) by the agent developer which is stored within the agent structure. The highest priority is 3 and the lowest priority is 0. Based on the priorities of the currently running agents the scheduler creates a static list by which the order of the agent execution is defined. The scheduler instructs the execution layer to execute exactly priority + 1 opcodes for a given agent. Eventually the control returns to the scheduler and the next agent from the list is picked. The list is iterated cyclically.

If an agent which is to be scheduled next is currently blocked by a sending operation, then its execution should be omitted.

As an agent can reproduce it self to another node or board or clone itself within the same platform the scheduling list requires adaptation as soon as new agent is deployed on a platform. Whenever a given platform is the destination end point of a reproduction respectively cloning operation the scheduler needs to update its scheduling list before proceeding with further executions.

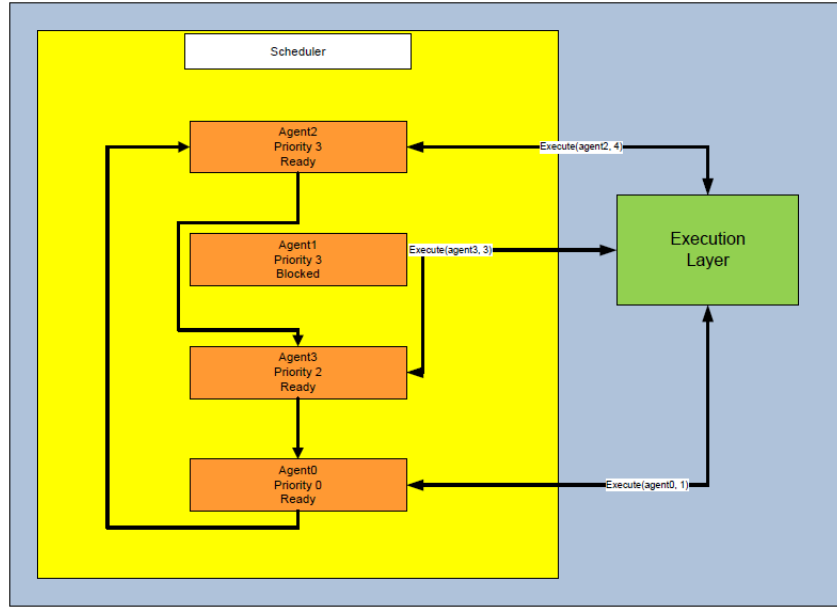


Figure 8.6: Scheduler

8.7 Agent language

8.7.1 Agent language (Assembler level)

To develop a mobile agent *Agent language* will be used. Language is tied to agent internal structure and support necessary operation for code mobility and message exchange. While writing the program for agent user should not be aware of hardware services presented on a given platform, but have common knowledge about all available services and what operations are allowed to do with the services (have list of services and available operations). It is the responsibility of the platform to provide required service to the agent (perform measurement, IO operation) or to manifest an error if the service is not available on the given platform. All current variables are allowed to store only in registers of agent structure (Fig. 8.2).

The language supports the following groups of operations: arithmetic, control flow, code mobility, message exchange , access to hardware services.

Addressing registers of agent structure			
General purpose registers		Character registers	
Register	rrrr	Char Register	rrrr
reg_0	0000	reg_str_0	1101
reg_1	0001	reg_str_1	1110
reg_2	0010	reg_str_2	1111
reg_3	0011		
reg_4	0100		
reg_5	0101		
reg_6	0110		
reg_7	0111		
reg_8	1000		
reg_9	1001		
reg_10	1010		
reg_11	1011		
reg_12	1100		

Arithmetic operations of agent assembly language

Addition

Add the content of `reg_d` and `reg_r` (or value) and put the result into `reg_0`.

`add reg_d, reg_r`

Syntax	Operands	Program counter	Flags
<code>add reg_d, reg_r</code>	$\text{reg}_0 \leq \text{reg}_d \leq \text{reg}_{12},$ $\text{reg}_0 \leq \text{reg}_r \leq \text{reg}_{12}$	$\text{PC} = \text{PC} + 1$	C

Operation

$\text{reg}_0 \leftarrow \text{reg}_d + \text{reg}_r$

16-bit opcode:

		reg_d	reg_r
0000	0011	dddd	rrrr

`add reg_r, value`

Syntax	Operands	Program counter	Flags
<code>add reg_r, value</code>	$\text{reg}_0 \leq \text{reg}_r \leq \text{reg}_{12},$ $0x00 \leq \text{value} \leq 0xFF$	$\text{PC} = \text{PC} + 1$	C

Operation

$\text{reg}_0 \leftarrow \text{reg}_r + \text{value}$

16-bit opcode:

	reg_r	value	
0011	rrrr	vvvv	vvvv

Subtraction

Subtract `reg_s` (or value) from `reg_m` and put the result into `reg_0`.

`sub reg_m, reg_s`

Syntax	Operands	Program counter	Flags
<code>sub reg_m, reg_s</code>	$\text{reg}_0 \leq \text{reg}_m \leq \text{reg}_{12},$ $\text{reg}_0 \leq \text{reg}_s \leq \text{reg}_{12},$	$\text{PC} = \text{PC} + 1$	C

Operation

$\text{reg}_0 \leftarrow \text{reg}_m - \text{reg}_s$

16-bit opcode:

		reg_m	reg_s
0000	0110	mmmm	ssss

`sub reg_m, value`

Syntax	Operands	Program counter	Flags
<code>sub reg_m, value</code>	$\text{reg}_0 \leq \text{reg}_m \leq \text{reg}_{12},$ $0x00 \leq \text{value} \leq 0xFF$	$\text{PC} = \text{PC} + 1$	C

Operation

$\text{reg}_0 \leftarrow \text{reg}_m - \text{value}$

16-bit opcode:

	reg_m	value	
0110	mmmm	vvvv	vvvv

Division

Divide `reg1` by `reg2` (or value) and put the result into `reg_0`.

`div reg_d, reg_r`

Syntax	Operands	Program counter	Flags
<code>div reg_d, reg_r</code>	$\text{reg_0} \leq \text{reg_d} \leq \text{reg_12},$ $\text{reg_0} \leq \text{reg_r} \leq \text{reg_12},$	$\text{PC} = \text{PC} + 1$	C

Operation

$\text{reg_0} \leftarrow \text{reg_d} / \text{reg_r}$

16-bit opcode:

		reg_d	reg_r
0000	1001	dddd	rrrr

`div reg_d, value`

Syntax	Operands	Program counter	Flags
<code>div reg_d, value</code>	$\text{reg_0} \leq \text{reg_d} \leq \text{reg_12},$ $0\text{x}00 \leq \text{value} \leq 0\text{x}\text{FF}$	$\text{PC} = \text{PC} + 1$	C

Operation

$\text{reg_0} \leftarrow \text{reg_d} / \text{value}$

16-bit opcode:

	reg_d	value	
1001	dddd	vvvv	vvvv

Multiplication

Multiply `reg1` and `reg2` (or value) and put the result into `reg_0`.

`mul reg_d, reg_r`

Syntax	Operands	Program counter	Flags
<code>mul reg_d, reg_r</code>	$\text{reg_0} \leq \text{reg_d} \leq \text{reg_12},$ $\text{reg_0} \leq \text{reg_r} \leq \text{reg_12}$	$\text{PC} = \text{PC} + 1$	C

Operation

$\text{reg_0} \leftarrow \text{reg_d} * \text{reg_r}$

16-bit opcode:

		reg_d	reg_r
0000	1100	dddd	rrrr

`mul reg1, value`

Syntax	Operands	Program counter	Flags
<code>mul reg_d, value</code>	$\text{reg_0} \leq \text{reg_d} \leq \text{reg_12},$ $0\text{x}00 \leq \text{value} \leq 0\text{x}\text{FF}$	$\text{PC} = \text{PC} + 1$	C

Operation

$\text{reg_0} \leftarrow \text{reg_d} * \text{value}$

16-bit opcode:

	reg_d	value	
1100	dddd	vvvv	vvvv

Control flow operations and comparison in agent assembly language

Jump if greater

Jump to **offset** in code segment of agent structure if the value of **reg_0**. is 1.

jmpgr offset

Syntax

jmpgr offset

Operands

$-128 \leq \text{offset} \leq +127$

Program counter

$\text{PC} = \text{PC} + \text{offset} + 1$ if **reg_0** = 1,

$\text{PC} = \text{PC} + 1$ otherwise

16-bit opcode:

		offset	
1111	0011	vvvv	vvvv

Jump if equal

Jump to **offset** in code segment of agent structure if the value of **reg_0** is 0.

jmpeq offset

Syntax

jmpeq offset

Operands

$-128 \leq \text{offset} \leq +127$

Program counter

$\text{PC} = \text{PC} + \text{offset} + 1$ if **reg_0** = 0,

$\text{PC} = \text{PC} + 1$ otherwise

16-bit opcode:

		offset	
1111	0110	vvvv	vvvv

Jump if less

Jump to **offset** in code segment of agent structure if the value of **reg_0** is -1.

jmppls offset

Syntax

jmppls offset

Operands

$-128 \leq \text{offset} \leq +127$

Program counter

$\text{PC} = \text{PC} + \text{offset} + 1$ if **reg_0** = -1,

$\text{PC} = \text{PC} + 1$ otherwise

16-bit opcode:

		offset	
1111	1100	vvvv	vvvv

Comparison

Compare **reg1** and **reg2** (or value).

compare reg_d, reg_r

Syntax

compare reg_d, reg_r

Operands

$\text{reg}_0 \leq \text{reg}_d \leq \text{reg}_{12},$

$\text{reg}_0 \leq \text{reg}_r \leq \text{reg}_{12}$

Program counter

$\text{PC} = \text{PC} + 1$

Operation

$\text{reg}_0 \leftarrow 1$ if $(\text{reg}_d - \text{reg}_r > 0)$

$\text{reg}_0 \leftarrow 0$ if $(\text{reg}_d - \text{reg}_r = 0)$

$\text{reg}_0 \leftarrow -1$ if $(\text{reg}_d - \text{reg}_r < 0)$

16-bit opcode:

		reg_d	reg_r
0000	1010	dddd	rrrr

compare reg_d, value

Syntax	Operands	Program counter
compare reg_d, value	$\text{reg_0} \leq \text{reg_d} \leq \text{reg_12},$ $0\text{x}00 \leq \text{value} \leq 0\text{x}\text{FF}$	$\text{PC} = \text{PC} + 1$
Operation		
$\text{reg_0} \leftarrow 1$ if $(\text{reg_d} - \text{value} > 0)$		
$\text{reg_0} \leftarrow 0$ if $(\text{reg_d} - \text{value} = 0)$		
$\text{reg_0} \leftarrow -1$ if $(\text{reg_d} - \text{value} < 0)$		
16-bit opcode:		
	reg_r	value
1010	rrrr	vvvv vvvv

Code mobility operations of agent assembly language

Move code

Move agent structure to platform that possess required service

`move service`

Syntax	Operands	Program counter
move service	$\text{service_0} \leq \text{service} \leq \text{service_255}$	$\text{PC} = \text{PC} + 1$
Operation		
Serialize and transmit agent structure to the platform that possess required service		

16-bit opcode:

	reg_r	service
1111	0001	ssss ssss

Clone code

Replicate agent structure on the given platform

`clone`

Syntax	Program counter
clone	$\text{PC} = \text{PC} + 1$

16-bit opcode:

1111	0010	0000	0000

Die

Destroy agent structure and free corresponding memory

`die`

16-bit opcode:

1111	0100	0000	0000

Message exchange

Send Message exchange between agents

`sendmsg reg, agent, platform`

Syntax	Operands	Program counter
sendmsg reg, agent, platform	platform_0 ≤ platform ≤ platform_3 agent_0 ≤ agent ≤ agent_3 reg_0 ≤ reg ≤ reg_12 reg_str_0 ≤ reg ≤ reg_str_2	PC = PC + 1

Operation

Send value of the register **reg** to the agent **aa** on the platform **pp**

16-bit opcode:

		register	agent	platform
1111	1000	rrrr	aa	pp

Receive

Pull message from platform to register.

pullmsg reg

Syntax	Operation	Program counter
pullmsg	reg_0 ≤ reg ≤ reg_12 reg_str_0 ≤ reg ≤ reg_str_2	PC = PC + 1

Operation

reg ← message

16-bit opcode:

		rrrr	
1111	1010	0000	0000

Store, move and wait operations

Store

Store value in h-part of **reg_d**

ldh reg_d, value

Syntax	Operands	Program counter
ldh reg_d, value	reg_0 ≤ reg_d ≤ reg_12, 0x00 ≤ value ≤ 0xFF	PC = PC + 1

Operation

reg_d_h ← value

16-bit opcode:

	reg_d	value	
1101	dddd	vvvv	vvvv

Store value in l-part of **reg_d**

ldl reg_d, value

Syntax	Operands	Program counter
ldl reg_d, value	reg_0 ≤ reg_d ≤ reg_12, 0x00 ≤ value ≤ 0xFF	PC = PC + 1

Operation

reg_d_l ← value

16-bit opcode:

	reg_d	value	
0100	dddd	vvvv	vvvv

Push char value in the str_reg

storecr reg_str, char

Syntax

storecr reg_str, char

Operands

reg_str_0 ≤ reg_str ≤ reg_str_2

Program counter

PC = PC + 1

Operation

reg_str ← value

16-bit opcode:

	reg_str	value	
1011	rrrr	vvvv	vvvv

Clear the str_reg

clr reg_str

Syntax

clr str_reg

Operands

reg_str_0 ≤ reg_str ≤ reg_str_2

Program counter

PC = PC + 1

Operation

clear str_reg

16-bit opcode:

			rrrr
0000	0010	0000	rrrr

Move

Move value from reg_r to reg_d

mv reg_d, reg_r

Syntax

mv reg_d, reg_r

Operands

reg_0 ≤ reg_d ≤ reg_12,

reg_0 ≤ reg_r ≤ reg_12

Program counter

PC = PC + 1

Operation

reg_d ← reg_r

16-bit opcode:

		reg_d	reg_r
0000	1101	dddd	rrrr

Wait

Wait for ms

wait delay_ms

Syntax

wait delay_ms

Operands

0 ≤ delay_ms ≤ 0xFF

Program counter

PC = PC + 1

16-bit opcode:

		delay	
0000	0101	dddd	dddd

Assign priority value

Assign priority of the agent to value in range 0..3

priority value

Syntax	Operands	Program counter
priority value	$0 \leq \text{value} \leq 3$	$\text{PC} = \text{PC} + 1$
Operation		
priority \leftarrow value		
16-bit opcode:		
	priority	
0000 1000	pppp pppp	

Access to hardware services

Set

Set service to reg or value

```
setservice service_id, reg
```

Syntax	Operands	Program counter
setservice service_id,	$\text{service}_0 \leq \text{service_id} \leq \text{service}_{255},$	$\text{PC} = \text{PC} + 1$
reg	$\text{reg}_0 \leq \text{reg} \leq \text{reg}_{12}$	
16-bit opcode:		
	reg service_id	
0111 rrrr	ssss ssss	

Get

Put corresponding value from the service to the reg_0.

```
getservice service_id
```

Syntax	Operand	Program counter
getservice service_id	$\text{service}_0 \leq \text{service_id} \leq \text{service}_{255}$	$\text{PC} = \text{PC} + 1$
16-bit opcode:		
	service_id	
0000 0111	ssss ssss	

8.8 Communication Architecture

The communication architecture is designed to support communication between nodes on the same development board as well as between boards.

8.8.1 Hardware

The communication on the board is carried out over two serial bus channels. One of them is to be used for a distributed control application running on nodes 0-3. Another bus is dedicated for code mobility between nodes 0-4.

Access to the bus is controlled by separate UART modules on each micro-controller. The bit rate is constrained by the maximum value of 2 Mbps according to the manual.

Node 4 functions as a gateway to another board. It is a bridge between the local and the wireless zigbee network.

8.8.2 Distributed Control

Time-triggered protocols are customary for distributed control applications. This approach is suitable for low data volumes and data subject to real time constraints and regular sending intervals. In a time triggered scheme each node has a separate slot for writing to the bus. Meanwhile, other nodes can read the bus in the same slot or process a computation task.

8.8.3 Code Mobility

Code mobility between nodes includes local mobility on the same board and remote mobility between different boards. Executable agents generally have larger volume than control data. Sending at regular time intervals is not assumed, thus communication is aperiodic. A simple protocol based on message acknowledgment can be used.

There are two use cases: a) local mobility: destination is one of the nodes 0-3. b) remote mobility: destination is the gateway node 4. The gateway is to contain a zigbee stack implementation to enable access to the personal area network.

8.8.4 Addressing Scheme

Simple local addressing requires unique identifiers for each node. The requirement is that this be compatible with the time-triggered protocol implementation. For remote communication, board addresses have to be compatible with the configuration of the zigbee network. Since, each node will have a static number of agent execution environments, the address has to contain its identifier as well.

8.8.5 Communication Interface

The interface for accessing the communication system is given below in Figures 8.7 through 8.10.

```
typedef struct comm_msg_S {
    int type;           /* Message type identifier */
    int node;           /* Destination or origin node
                        depending on the context */
    int board;          /* Destination or origin board
                        depending on the context */
    long int len;       /* Payload length */
    char *payload;      /* Payload data */
} comm_msg_T;
```

Figure 8.7: Message Structure

```
/**
    Function: comm_register
            Registers a communication endpoint
    Returns: Zero if successful
    Parameters: board
                Board id
                node
                Node id inside a board
 */
int comm_register(int board, int node);
```

Figure 8.8: Endpoint Registration


```

/**
    Function: comm_set_recvr
            Sets a callback to process incoming messages
    Parameters: recvr_callback
            Callback function that processes a received message
    Returns: Zero if successful
*/
int comm_set_recvr(void (*recvr_callback)(comm_msg_T *msg));

```

Figure 8.9: Message Receiving

```

/**
    Function: comm_send_msg
            Sends message over communication interface
    Parameters: msg
            Pointer to message structure;
            structure will be copied by function
            sent_cb
            Callback that is called when the message has been sent
    Returns: Zero if message is accepted for sending
            or nonzero if rejected
*/
int comm_send_msg(comm_msg_T *msg, void (*sent_cb)(int));

```

Figure 8.10: Message Sending

9 Implementation timetable

Figure 9.1 shows the implementation timetable.

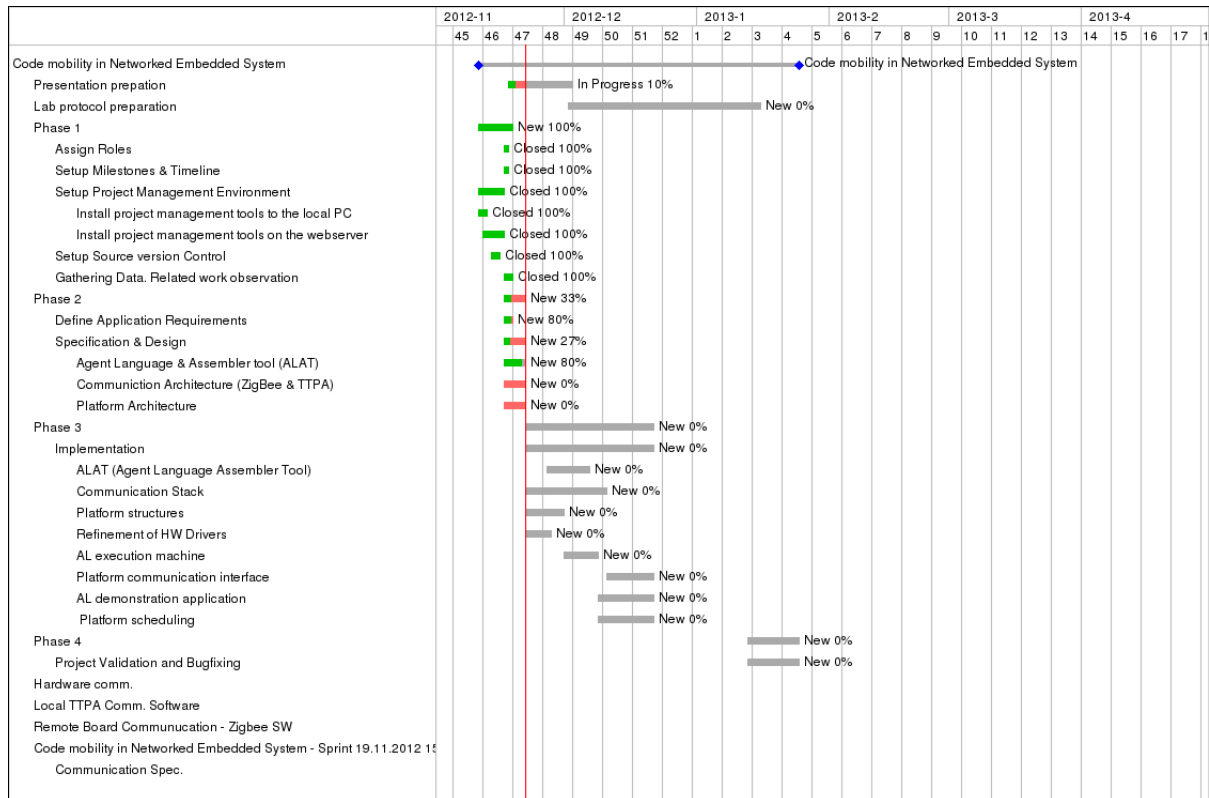


Figure 9.1: Gantt Diagram of the Project

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