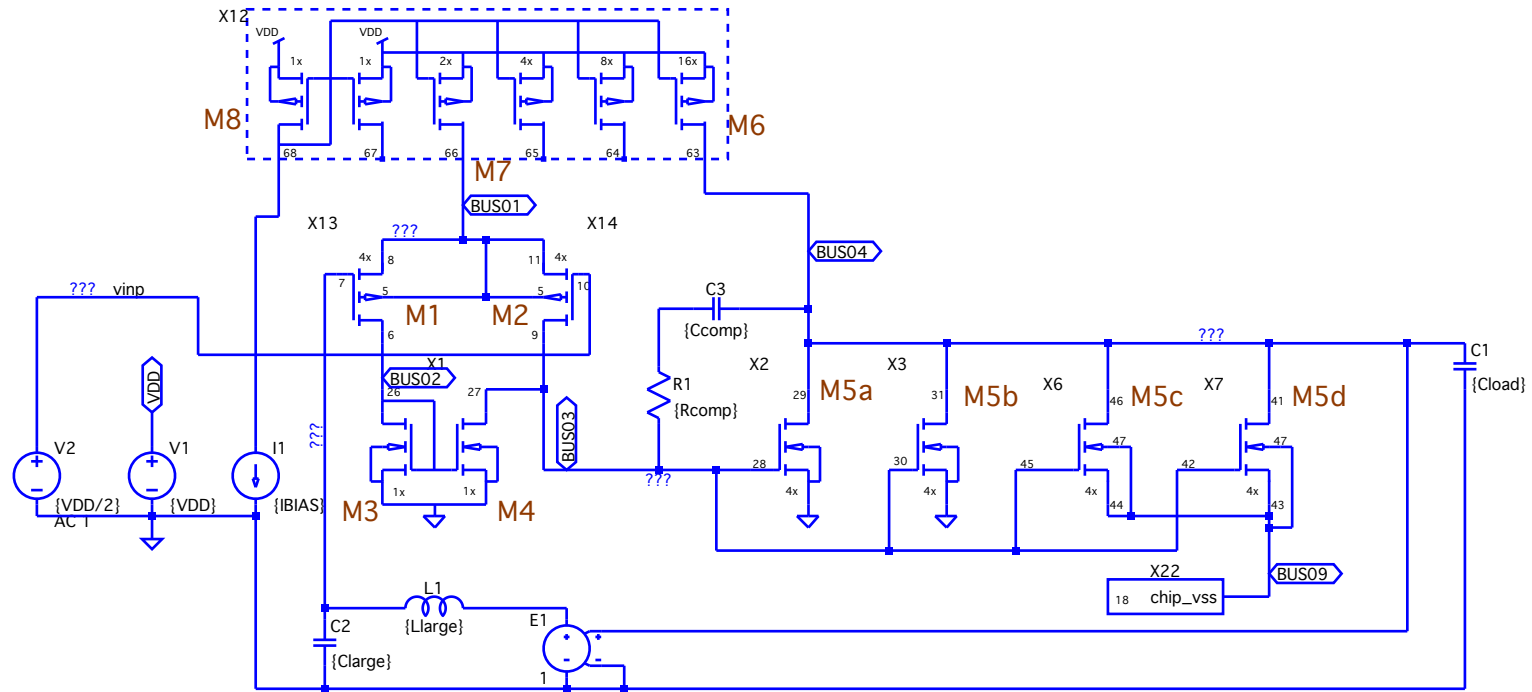


Two-Stage Miller-Compensated OTA with pMOS Input Stage



Note: Do not connect a BUS directly to VDD or VSS even with a jumper.
Use the Chip_VDD and Chip_VSS.

```
.global VDD
.param VDD = 2.5
.inc transistor_models_tsmc025_public.inc
```

```
.param IBIAS = 100u Cload = 200p Ccomp=120p Rcomp=200
.param Clarge = 10 Llarge = 10
```

```
*.step param Ccomp list 100f 100p 200p 400p
*.step param Rcomp list 0.001 200
*.ac dec 50 1 1e9
.op
```