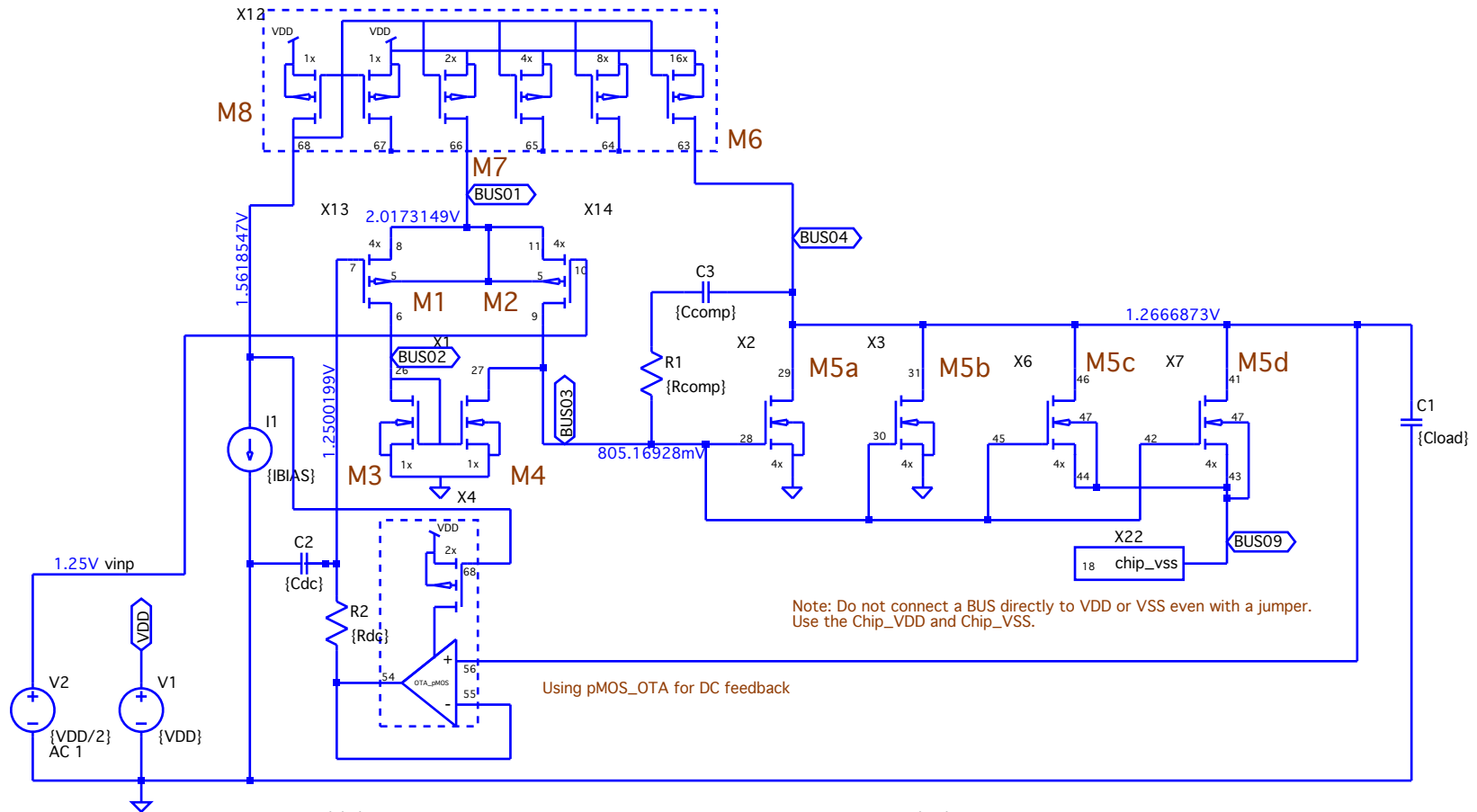


Two-Stage Miller-Compensated OTA with pMOS Input Stage

DC feedback using pMOS_OTA



```
.global VDD
.param VDD = 2.5
.inc transistor_models_tsmc025_public.inc
```

```
.param IBIAS = 100u Cload = 1n Ccomp=150p Rcomp=200
.param Cdc = 470u Rdc = 100K
```

```
*.step param Ccomp list 100f 100p 200p 400p
.step param Rcomp list 0.001 200
.ac dec 50 1 1e9
*.op
```