



GOLDS-UFSC Documentation

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SpaceLab, Universidade Federal de Santa Catarina, Florianópolis - Brazil

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Project Chief:
Eduardo Augusto Bezerra

Authors:
Gabriel Mariano Marcelino
André Martins Pio de Mattos
Eduardo Augusto Bezerra

Contributing Authors:

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Nomenclature

ACS	<i>Attitude Control System.</i>
EDC	<i>Environmental Data Collection.</i>
EPS	<i>Electrical Power System.</i>
GOLDS	<i>Global Open Collecting Data System</i>
INPE	<i>Instituto Nacional de Pesquisas Espaciais.</i>
LIT	<i>Laboratório de Integração e Testes.</i>
MPPT	<i>Maximum Power Point Tracking.</i>
OBDH	<i>On-Board Data Handling.</i>
PCB	<i>Printed Circuit Board.</i>
SBCD	<i>Sistema Brasileiro de Coleta de Dados.</i>
TTC	<i>Telemetry, Tracking and Command Module.</i>

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CHAPTER 1

Introduction

GOLDS stands for Global Open Collecting Data System...

INPE
LIT
PCB

1.1 Mission Description

1.2 Mission Objectives

1. To serve as a host platform for the EDC payload.
2. Validate the EDC payload in orbit.
3. Validate EDC functionality in orbit.
4. Validate core-satellite functions in orbit.
5. Evaluate the behavior of the core modules.
6. Perform experiments on radiation effects in electronic components in orbit.
7. Serve as relay for amateur radio communications.

1.3 Mission Patch

The mission patch of the GOLDS-UFSC can be seen in Figure 1.1, it is inspired by the FloripaSat-I patch [1].



Figure 1.1: GOLDS-UFSC mission patch.

CHAPTER 2

Mission Requirements

1. The power system shall be able to harvest solar energy.
2. The power system shall be able to store energy for use when GOLDS-UFSC is eclipsed.
3. The power system shall supply energy to all other modules.
4. The data handling system shall communicate with the other modules and store their data.
5. The communications system shall send a beacon signal periodically using VHF radio.
6. The communications system shall send the CubeSat telemetry using UHF radio.
7. The communications system shall be able to receive telecommands and respond to them accordingly.
8. The attitude system shall be able to perform a 1-axis stabilization of the CubeSat.
9. GOLDS-UFSC shall have the capability to receive and execute a shutdown telecommand, therefore ceasing all transmissions.
10. The downlink transmissions shall be done once at a time, either telemetry or beacon.
11. The ground station shall operate under the proper radio frequency communication licenses.
12. GOLDS-UFSC shall comply with international and Brazilian radio license agreements and restrictions.
13. The team shall build and operate a ground station for full communication with GOLDS-UFSC.

CHAPTER 3

Mission Schedule

Activity	Month (2021)											
	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dez
1												
2												
3												
4												
5												
6												
7												
8												
9												
10												
11												
12												
13												
14												

Table 3.1: Mission schedule.

Each activity of Table 3.1 is described below:

1. Acquisition and manufacturing of critical elements and components for the solo platform.
2. Acquisition and manufacture of elements and components critical to the payload.
3. Acquisition and manufacturing of critical elements and components for the solo segment.
4. Compatibility tests between platform and payload in SpaceLab UFSC.
5. Integration of the engineering model in SpaceLab UFSC.
6. Preparation and suitability of the ground segment.
7. Verification and validation of the engineering model at SpaceLab UFSC.
8. Verification and validation of the flight model at SpaceLab UFSC.

9. Data collection platforms installation.
10. Verification and validation tests of Engineering Model compatibility with EMMN in the INPE / CRN in Natal.
11. Environmental tests at the Integration and Testing Laboratory (LIT/INPE).
12. Flight model acceptance and ground segment review.
13. Ground segment delivery.
14. Flight model delivery.

CHAPTER 4

Overall Description

4.1 General Diagrams

4.2 General Behaviour

4.3 Orbit Parameters

4.4 Power Budget

4.5 Link Budget

4.5.1 VHF Link

- Direction: **Downlink**
- Frequency: **145,97 MHz**
- Modulation: **MSK**
- Datarate: **1200 bps**
- Output Power: **30 dBm (1 W)**
- Protocol: **NGHam**

4.5.2 UHF Links

Main UHF Link

- Direction: **Downlink and uplink**
- Frequency: **436,9 MHz**
- Modulation: **MSK**
- Datarate: **4800 bps**
- Output power: **30 dBm (1 W)**
- Protocol: **NCHam**

EDC UHF Link

- Direction: **Uplink**
- Frequency: **401.635 MHz**
- Modulation: **BPSK**
- Datarate: **400 bps**
- Protocol: **SBCD**

4.6 PC-104 Bus

4.7 Telecommunication

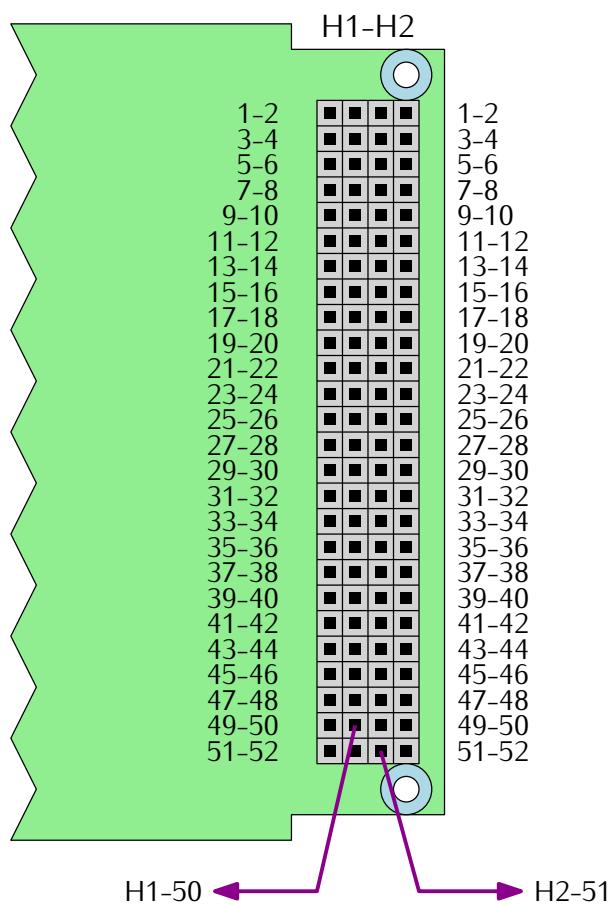


Figure 4.1: Reference diagram of the PC-104 bus.

Pin Row	H1 Odd	H1 Even	H2 Odd	H2 Even
1-2	-	-	-	-
3-4	-	-	EDC_1_EN	EDC_2_EN
5-6	-	-	BE_UART_RX	-
7-8	RA_GPIO_0	RA_GPIO_1	BE_UART_TX	GPIO_0
9-10	RA_GPIO_2	-	-	-
11-12	RA_RESET	RA_EN	BE_SPI_MOSI	BE_SPI_CLK
13-14	-	-	BE_SPI_CS	BE_SPI_MISO
15-16	-	-	-	-
17-18	EDC_UART_RX/TX	PLX_EN	-	GPIO_1
19-20	EDC_UART_TX/RX	GPIO_2	-	GPIO_3
21-22	-	-	-	GPIO_4
23-24	-	-	-	-
25-26	-	-	-	-
27-28	-	-	-	-
29-30	GND	GND	GND	GND
31-32	GND	GND	GND	GND
33-34	-	-	-	-
35-36	RD_SPI_CLK	-	ANT_VCC	ANT_VCC
37-38	RD_SPI_MISO	-	-	-
39-40	RD_SPI_MOSI	RD_SPI_CS	-	-
41-42	PL_I2C_SDA	-	-	GPIO_5
43-44	PL_I2C_SCL	-	-	-
45-46	OBDH_VCC	OBDH_VCC	BAT_VCC	BAT_VCC
47-48	EDC_VCC	EDC_VCC	-	-
49-50	RD_VCC	RD_VCC	EPS_I2C_SDA	-
51-52	BE_VCC	BE_VCC	EPS_I2C_SCL	-

Table 4.1: PC-104 bus pinout.

Signal	Pin(s)	Used By	Description
GND	H1-29, H1-30, H1-31, H1-32, H2-29, H2-30, H2-31, H2-32	All	Ground reference
BAT_VCC	H2-45, H2-46	EPS	Battery terminals (+)
ANT_VCC	H2-35, H2-36	EPS, ANT	Antenna power supply (3.3 V)
OBDH_VCC	H1-45, H1-46	EPS, OBDH	OBDH power supply (3.3 V)
EDC_VCC	H1-47, H1-48	EPS, EDC 1, EDC 2	EDC power supply (5 V)
RD_VCC	H1-49, H1-50	EPS, TTC	Main radio power supply (5 V)
BE_VCC	H1-51, H1-52	EPS, TTC	Beacon power supply (6 V)
RD_SPI_CLK	H1-35	OBDH, TTC	CLK signal of the main radio SPI bus
RD_SPI_MISO	H1-37	OBDH, TTC	MISO signal of the main radio SPI bus
RD_SPI_MOSI	H1-39	OBDH, TTC	MOS signal of the main radio SPI bus
RD_SPI_CS	H1-40	OBDH, TTC	CS signal of the main radio SPI bus
EPS_I2C_SDA	H2-49	OBDH, EPS	SDA signal of the EPS I2C bus
EPS_I2C_SCL	H2-51	OBDH, EPS	SCL signal of the EPS I2C bus
BE_UART_RX	H2-5	EPS, TTC	EPS TX, Beacon RX (UART bus)
BE_UART_TX	H2-7	EPS, TTC	EPS RX, Beacon TX (UART bus)
EDC_UART_TX/RX	H1-25	OBDH, EDC 1, EDC 2	OBDH TX, EDCs RX (UART bus)
EDC_UART_RX/TX	H1-27	OBDH, EDC 1, EDC 2	OBDH RX, EDCs TX (UART bus)
EDC_1_EN	H2-3	OBDH, EDC 1	EDC 1 enable signal
EDC_2_EN	H2-4	OBDH, EDC 2	EDC 2 enable signal
PLX_EN	H1-18	OBDH, Payload X	Payload X enable (GPIO)
PL_I2C_SDA	H1-41	OBDH, Payload X	SDA signal of the payload I2C bus
PL_I2C_SCL	H1-43	OBDH, Payload X	SCL signal of the payload I2C bus
GPIO_N	H2-8, H2-18, H1-20, H2-20, H2-22, H2-42	OBDH	GPIO pin (not used)

Table 4.2: PC-104 bus signal description.

<i>Link</i>	<i>Packet Name</i>	<i>Payload</i>			
		<i>ID</i>	<i>Source Callsign</i>	<i>Data (up to 220 bytes)</i>	<i>Size (bytes)</i>
Beacon	EPS Data	00h	"0" + "PY0EGU"	EPS + TTC data	58
	TTC Data	01h		TTC data	18
	EPS Data	02h		EPS + TTC data	39
	TTC Data	03h		TTC data	18
Downlink	Telemetry	10h	"0" + "PY0EGU"	Flags + OBDH/EPS data	220
	Ping Answer	11h		Requester callsign	15
	Data Request Answer	12h		Req. callsign + data	15 to 155
	Hibernation Feedback	13h		Req. callsign + hibernation in hours	17
	Message Broadcast	14h		Req. + dst. callsign + message	22 to 60
Uplink	Ping Request	20h	Any Callsign	None	8
	Data Request	21h		Data flags + count + origin + offset	16
	Hibernation Request	22h		Req. callsign + hibernation in hours + key	29
	Broadcast Message	23h		Dst. callsign + message	15 to 46

Table 4.3: Telecommunication packets and their content.

CHAPTER 5

Subsystems

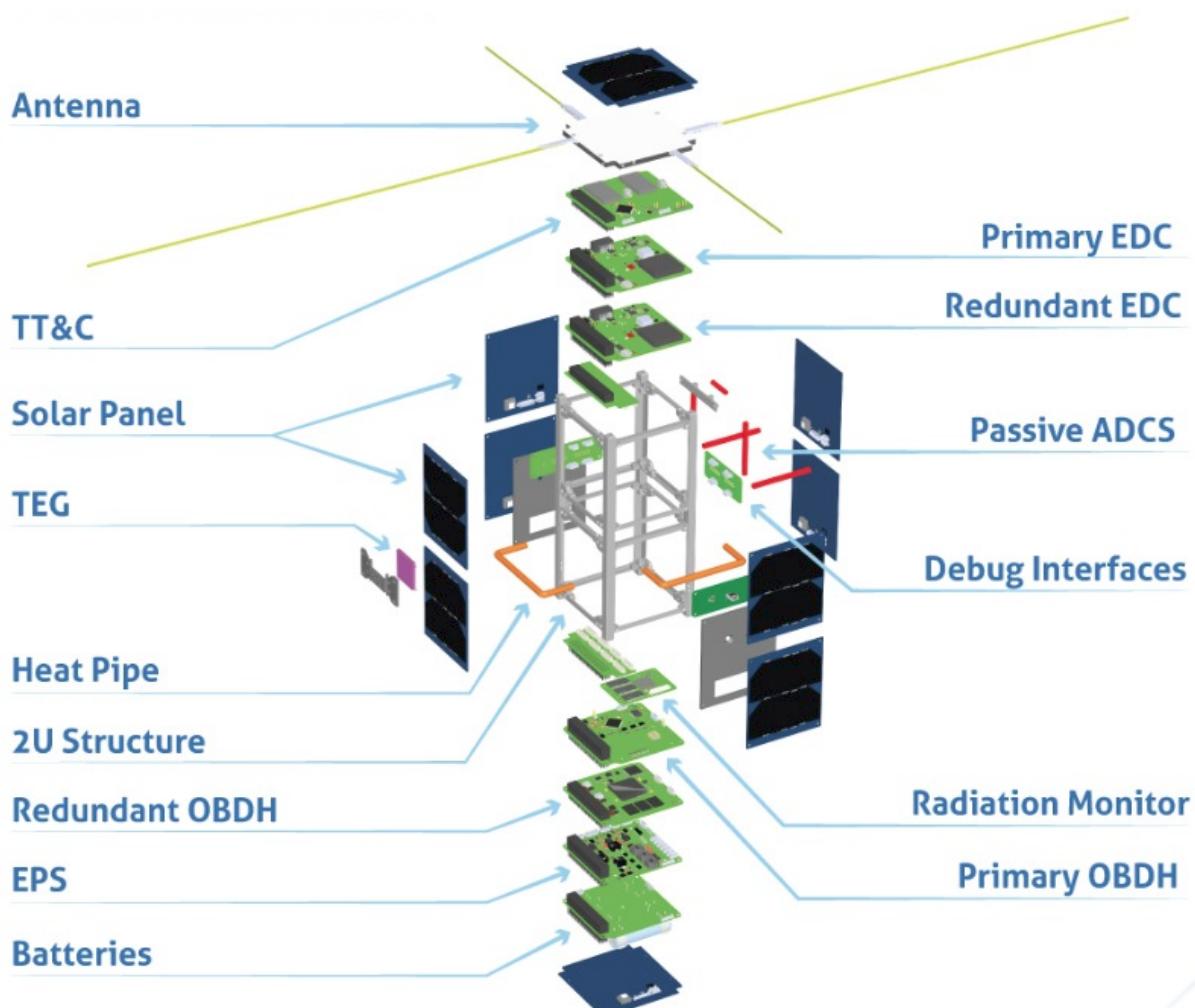


Figure 5.1: Exploded view of the GOLDS-UFSC satellite.

5.1 On-Board Data Handling

The OBDH 2.0 is an On-Board Computer (OBC) module designed for nanosatellites. The module is responsible for synchronizing actions and the data flow between other modules

(i.e., power module, communication module, payloads) and the Earth segment. It packs the generated data into data frames and transmit back to Earth through a communication module, or stores it on a non-volatile memory for later retrieval. Commands sent from Earth segment to the CubeSat are received by radio transceivers located in the communication module and redirected to the OBDH, which takes the appropriate action or forward the commands to the target module.

The module is a direct upgrade from the OBDH of FloripaSat-1 [1], which grants a flight heritage rating. The improvements focus on providing a cleaner and more generic implementation in comparison with the previous version, more reliability in software and hardware implementations, and adaptations for the new mission requirements. The board of the module can be seen in Figure 5.2.

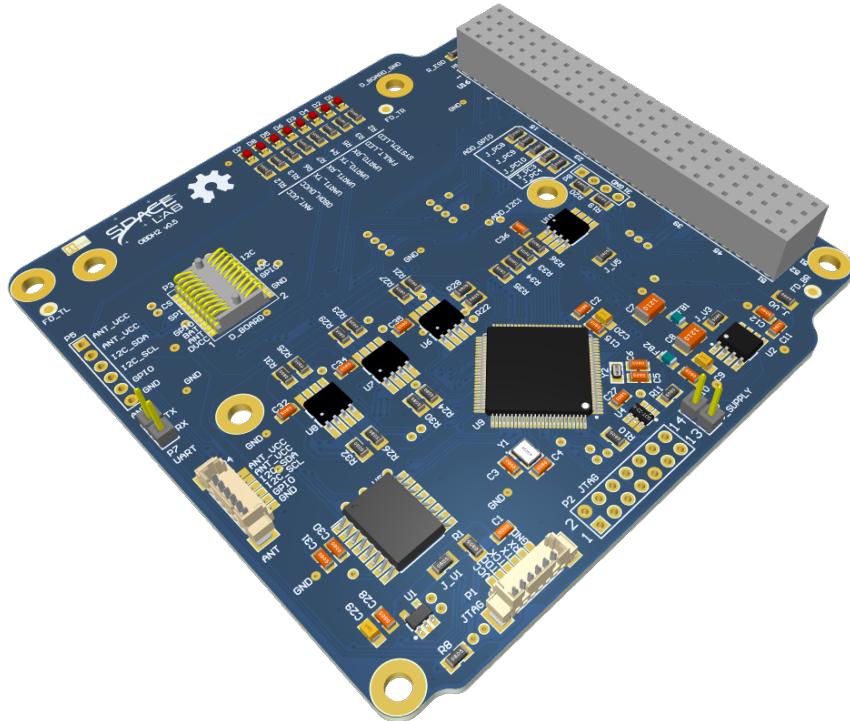


Figure 5.2: OBDH module.

More information about this module can be found in [2].

5.2 Telemetry, Tracking and Command Module

The TTC (or TT&C) is responsible to make the communication between the earth (a ground station) and the satellite, and is divided in two sub-modules: Beacon and downlink/uplink. The beacon is a independent sub-module who transmits a periodic signal containing an identification data (ID) of the satellite and some basic telemetry data. The downlink/uplink sub-module is the main communication device. It has a bidirectional data link to receive telecommands from the earth and transmit all available data back to Earth. The board of the module can be seen in Figure 5.3.

More information about this module can be found in [3].

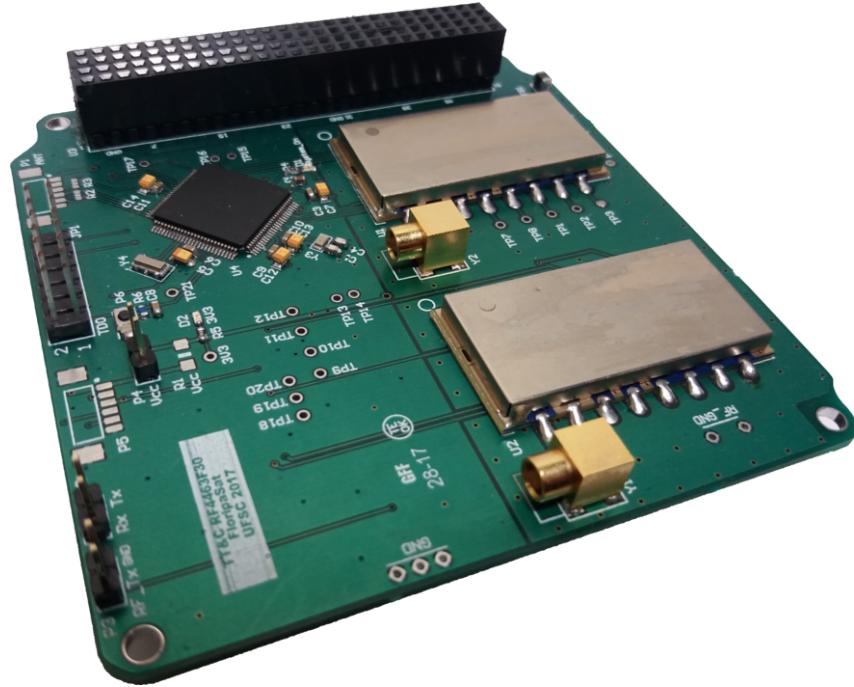


Figure 5.3: TTC module.

5.2.1 Antenna Module

The used antenna module is the CubeSat deployable VHF and UHF antenna from ISISpace [4]. It is a four monopole antenna built with tape strings (up to 55 cm) and compliant with the CubeSat standard (dipole or turnstile options are also available). The deployment method is the burning wire and it can be controlled digitally through a I²C interface. To allow redundancy, there are two independent deployment controllers that can be activated separately. Also, the construction of this module allows the installation of a solar panel at the top side. The RF gain is about 0 dBi.

A picture of the antenna module (with all antennas released) can be seen in Figure 5.4.

The chosen configuration for this mission can be seen below (using Figure 5.5 as reference):

- Configuration: 4 monopoles (1x VHF + 3x UHF)
 - Antenna 1: VHF - 145,97 MHz (beacon)
 - Antenna 2: UHF - 401,635 MHz (EDC)
 - Antenna 3: UHF - 436,9 MHz (downlink/uplink)
 - Antenna 4: UHF - 401,635 MHz (redundant EDC)
- Tuning structure size: 2U
- Mounting position: Top
- Supply voltage: 3,3 V
- I²C control type: Dual bus
 - Primary I²C address: 31h (7-bit address)

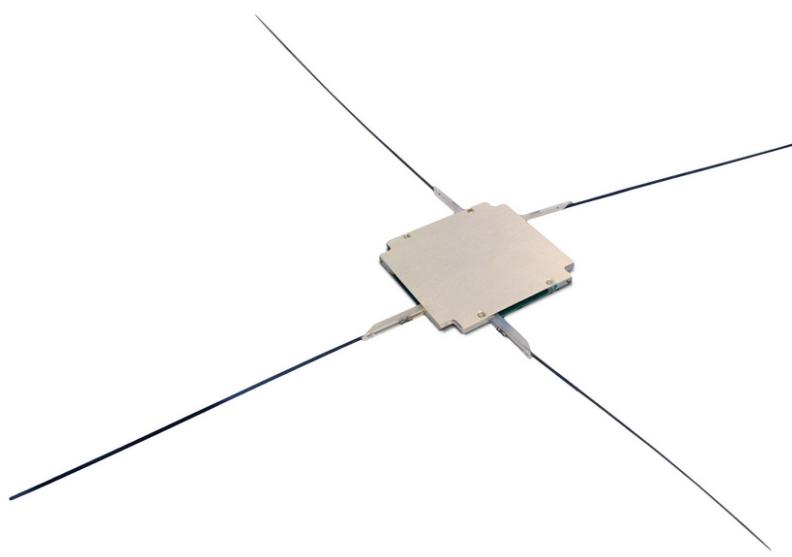


Figure 5.4: Antenna module from ISISpace.

- Redundant I²C address: 32h (7-bit address)
- I²C watchdog: Enabled with a time out of 60 seconds.

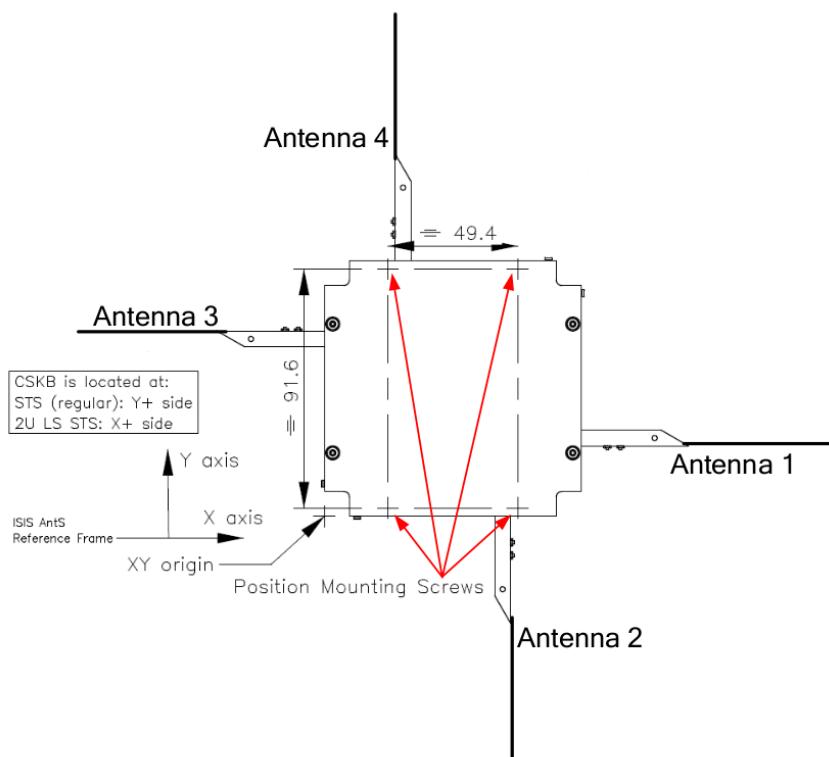


Figure 5.5: Configuration reference of the antenna module.

In the digital interface, a temperature sensor and the state of four deployment switches (1 per monopole) are also available. These switches indicate if a monopole is released or not, and can be used as feedback of the deployment process.

5.3 Electrical Power System

The EPS is the module designed to harvest, store and distribute energy for the satellite. The energy harvesting system is based on solar energy conversion through the solar panels attached to the CubeSat structure. The EPS is designed to operate the solar panels at their maximum power point (MPPT). The board also measures the solar panels current, voltage and the temperature of the batteries. The harvested solar energy is stored in a battery module connected to the EPS. The energy distribution is done by several integrated buck DC-DC converters. The full EPS system is composed of the solar panels, the EPS PCB and the battery module. A general view of the EPS board can be seen in Figure 5.6.

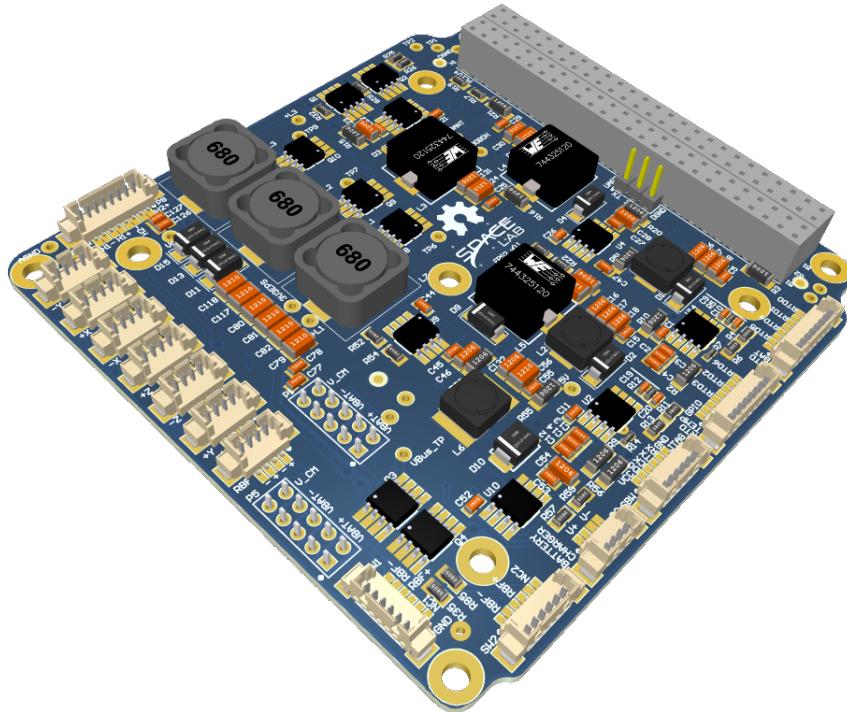


Figure 5.6: EPS module.

The module is a direct upgrade from the EPS of FloripaSat-1 [1], which grants a flight heritage rating. The improvements focus on providing a cleaner and more generic implementation in comparison with the previous version, more reliability in software, and adaptations for the new mission requirements.

More information about this module can be found in [5].

5.3.1 Battery Module

[6]

5.3.2 Solar Panels

5.3.3 Kill-Switches and RBF

5.4 Attitude Control System

The Attitude Control System (ACS) is a passive attitude control system, which depends on the Earth's magnetic field to rotate and stabilize the satellite [7, 8]. The system is composed of one permanent magnet to create a force to align the magnet with the Earth's magnetic field and four hysteresis bars to damp the cube oscillations and achieve stabilization.

When equilibrium is achieved, the permanent magnet aligns itself to the Earth's field lines. The hysteresis bars convert oscillation and rotation energy into heat, maintaining the alignment through magnetic moment. The components are placed in positions as to minimize the magnet's interaction with the hysteresis bars, which limits the magnetic moment of the magnet [9]. Figure 5.7 shows the mounting of the hysteresis bars (green) and the permanent magnet (red) on the mechanical structure. The whole passive ACS was implemented according to [9].

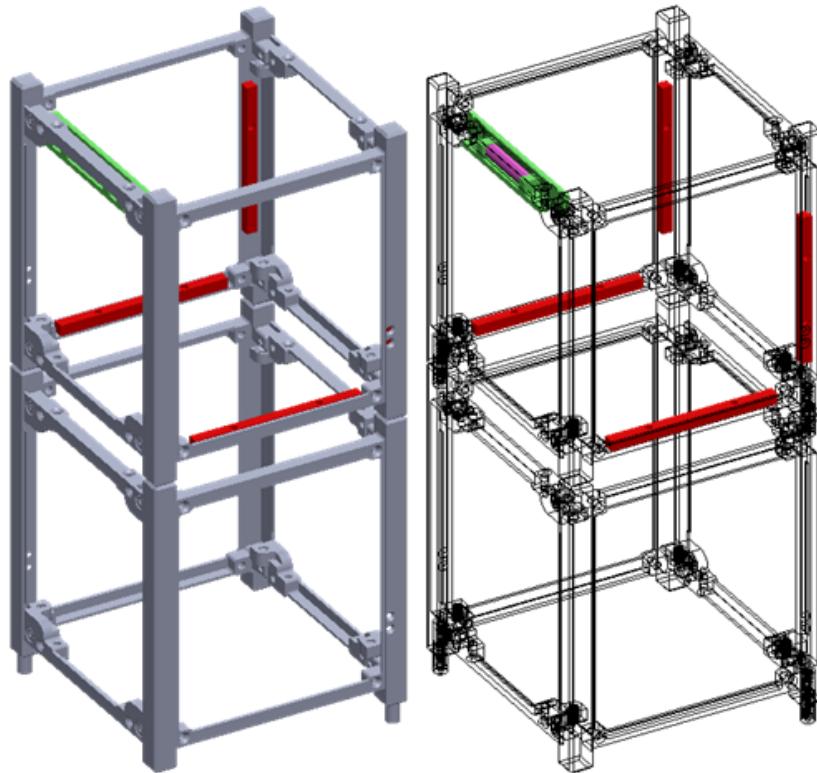


Figure 5.7: ACS subsystem. Rare earth magnet (pink) and hysteresis bars (red) installed in the structure.

As a passive magnetic attitude control system is used, it is possible to stabilize only one axis, and so, the CubeSat will still slowly (due to hysteresis bars) rotate around this

axis, even after stabilized. A N45 neodymium magnet and 4 hysteresis bars of Permanorm 5000 H2 are used (courtesy of Vacuumschmelze GmbH & Co. KG). The material of the hysteresis bar is shaped in order to maximize the stabilization, which is the most important part of the attitude control.

Many conditions impact on the detumbling time, which is the time required for the satellite to stabilize. Magnetic passive attitude stabilization systems such as the one developed for this mission achieve the equilibrium state within a few weeks of operation [7].

The GOLDS-UFSC satellite does not feature an orbit control subsystem.

5.5 Mechanical Structure

5.6 Interconnection Modules

5.6.1 PC-104 Interconnection Boards

The PC-104 interconnection boards are intended to be used as an interconnection of the two PC-104 bus segments of the 2U structure (top and bottom units). This interconnection is made with a set of PicoBlade cables between the top and bottom boards. The set of two boards can be seen in Figure 5.8.

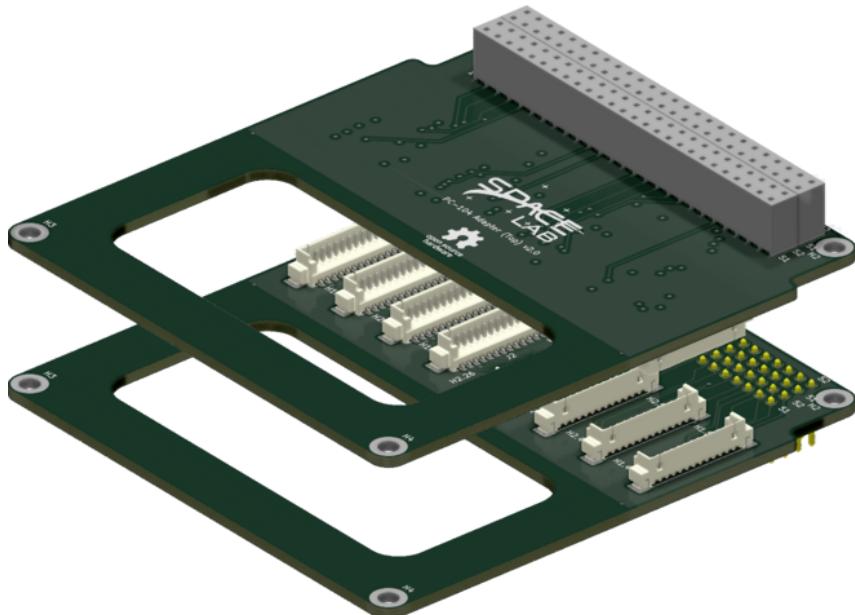


Figure 5.8: PC-104 adapter boards (top and bottom).

More information about these boards can be found in [10].

5.6.2 External Connection Boards

The Interstage Interface Panels (IIP) are three vertical internally mounted PCBs designed to give external access up to four modules inside of a 2U CubeSat during final assembly, integration and testing (AIT) before launch. The complete set of the boards allow the nanosatellite to be charged, programmed and debugged. The usage of this hardware platform is taking into account the use of a MSP-FET: MSP430 Flash Emulation Tool from Texas Instruments for JTAG programming and debugging, UART debugging through a mini USB type B port interfacing the FT4232H USB bridge IC from FTDI, a JST XH header for charging internal batteries and a Remove Before Flight (RBF) pin header. The boards can be seen in Figure 5.9.

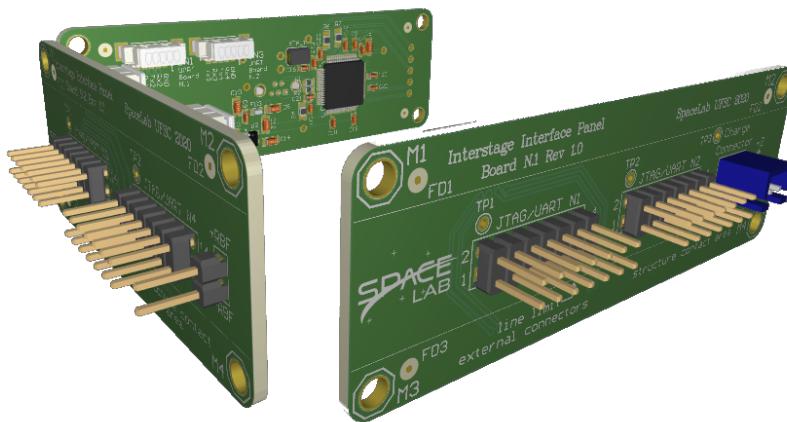


Figure 5.9: Set of external connection boards.

More information about these boards can be found in [11].

5.7 Payloads

The GOLDS-UFSC satellite is planned to carry three different payloads on-board: “*EDC*”, “*Payload-X*” and the “*Harsh Payload*”. Each one of these payloads are presented next.

5.7.1 Environmental Data Collection

The Environmental Data Collector (EDC) is a CubeSat-compatible payload that decodes signals from Platform Transmitter Terminals (PTTs) belonging to the Brazilian Environmental Data Collection System (SBCD) and the Argos-2 System. It is the main payload of the GOLDS-UFSC mission.

The main features of this payload are listed below, a 3D model of the EDC board can be seen in Figure 5.10.

- Reception/decoding of SBCD and Argos-2 signals on the $401.635 \text{ MHz} \pm 30 \text{ kHz}$ frequency range.
- Can decode up to 12 PTT signals simultaneously.

- Attaches a header to decoded messages with frequency, time, and signal strength information.
- Full speed I²C interface (400 kbit/s) for the OBC communication.
- Full-duplex RS-485 interface with fail-safe for the OBC communication.
- 5 V power supply.
- Memory capable of storing up to 64 decoded user messages.
- Generates housekeeping information including current supply, board temperature, digitized signal RMS level, front-end PLL synchronism state and overcurrent events.
- Can capture a 2048 samples sequence (16 ms window) from the received signal upon request.

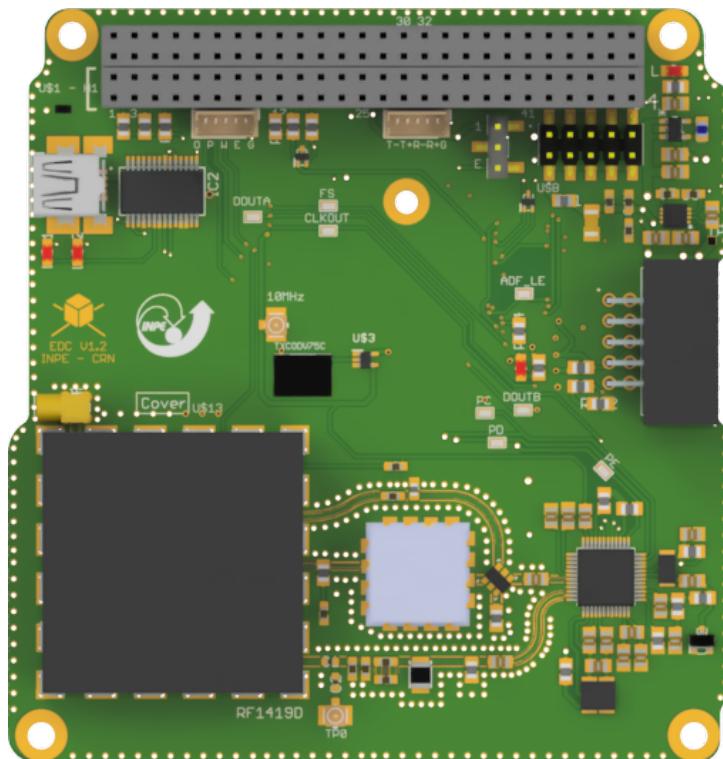


Figure 5.10: EDC board.

As can be seen in Figure 5.1, for this mission, two identical EDC boards will be used, in a cold redundancy configuration. More information about this payload can be found in [12].

5.7.2 Redundant OBDH (Payload-X)

The Payload-X is a radiation-hardened reconfigurable hardware platform designed for a radioactive environment, having as a main feature the possibility to change the hardware configuration of the FPGA through remote uplink of its bitstream.

More information about this payload can be found in [13].

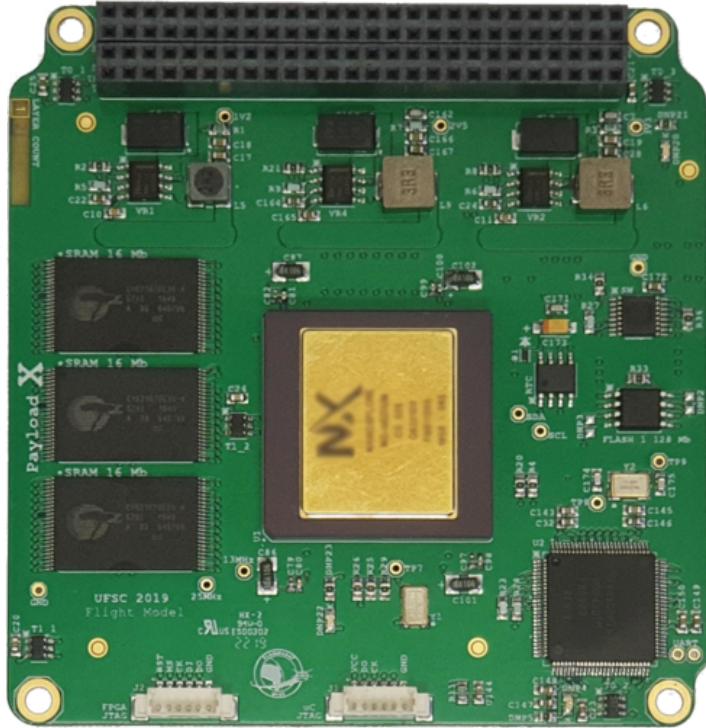


Figure 5.11: Payload-X board.

5.7.3 Radiation Monitor (Harsh Payload)

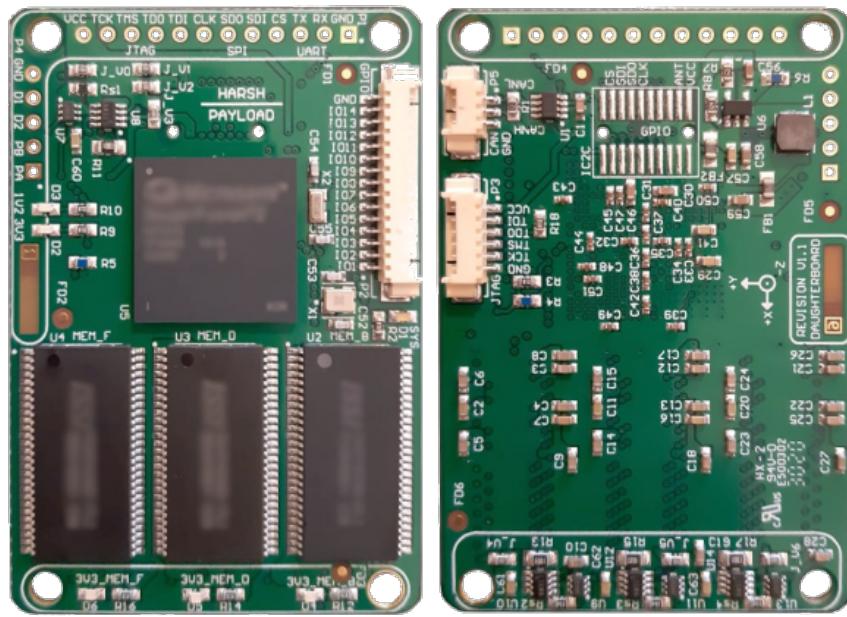


Figure 5.12: Radiation monitor board.

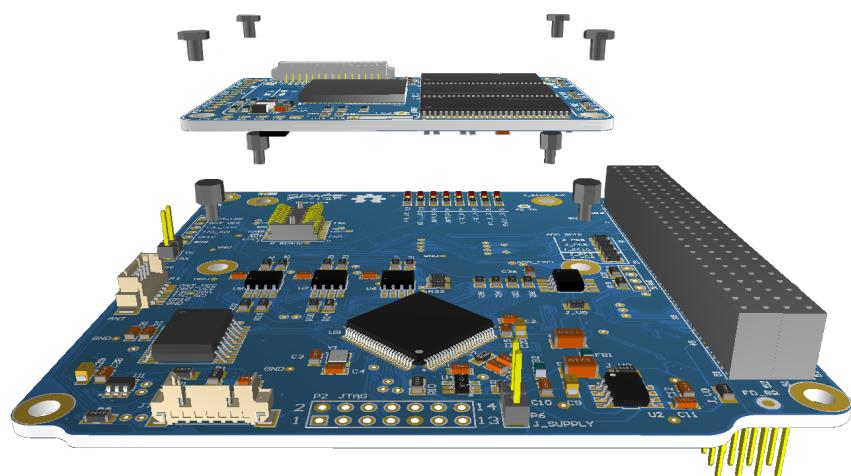


Figure 5.13: Integration of the radiation monitor payload in the OBDH.

CHAPTER 6

Test Plan and Results

6.1 Test Procedure

6.1.1 Hardware

.

6.1.2 Software

Unit Tests

- Hardware checks (might require mock circuitry).
- Driver operation checks: not extensive, might use loopback and fake sensor data schemes for hardware checks.
- Device operation checks: one test file for each device implemented, more extensive than driver checks, but should avoid development overhead.
- Standalone application checks: evaluate the application logic (masking or faking operating system calls, such as waiting for queue or a delay). It should be implemented without the operating system, in other words, evaluate inputs/outputs in dedicated main file.

Integration Tests

- Operating system initialization: assert memory allocation (RAM, stack, heap), hooks and etc;
- Boot sequence (as similar to the actual procedure as possible).
- Operating system task/queue/interrupts priority, constraints, size, depth and delay checks: use dummy task/queue/interrupts (same config as actual system).
- Short-term system check: after 1 hour, exit without error logs.
- Mid-term system check: after 1 day, exit without error logs.
- Long-term system check (used in flatsat): after 1 week, exit without flatsat/integration error logs.

Workflow

- Always it is a build->flash->test, change main and repeat.
- It must have a test folder containing subfolders (hardware, drivers, devices, app, integration) and a json file (with name, path and type).
- Inside the workflow is called a python script that read this json and setup variables to allow running multiple main file swaps for each test type.
- There are 5 different workflows, one for each test type: hardware, drivers, devices, app, integration;
- The workflow, tests and scripts must be reviewed before each release.
- Idea: for short/mid/long-term tests, the workflow should evaluate the log messages offline instead of real time, in which a job is scheduled to run just after this period and “a script” will read the log file and search for the test criteria, giving the actual CI result.
- Idea: Inside the code, using the log message approach, we might create our ultra lightweight framework that consists of only log types (colors) and log messages (specific strings). This way we do not modify our current workflow and we can add a simple scheme to access the flight code.
- Unit Tests = Tests performed per firmware unit.
- Integration Tests = Tests performed per firmware component (several units abstracted).

6.1.3 Flatsat

To test all modules during the development of the projet, a flatsat platform was developed. The FlatSat Platform is a testbed for CubeSat PCB modules. FlatSats enable easier, faster and a secure method for testing subsystems independently while been integrated in a flat design before going to integration on a CubeSat form factor. The PCB can support up to 7 modules, all PC-104 pins are interligated to flexibilize its use, only the particularity connection between modules need to be taken into account. One PC-104 has inverted pinout, the board also makes it possible to have two separate power supplies, a UART to USB converter for 4 modules, kill-switches activation through SPDTs, Remove Before Flight (RBF) pin header, connector for charging batteries and SMA connectors for antennas. A picture of the flatsat board can be seen in Figure 6.1.

More information about the Flatsat Platform can be found in [14].

6.2 Preliminary Results

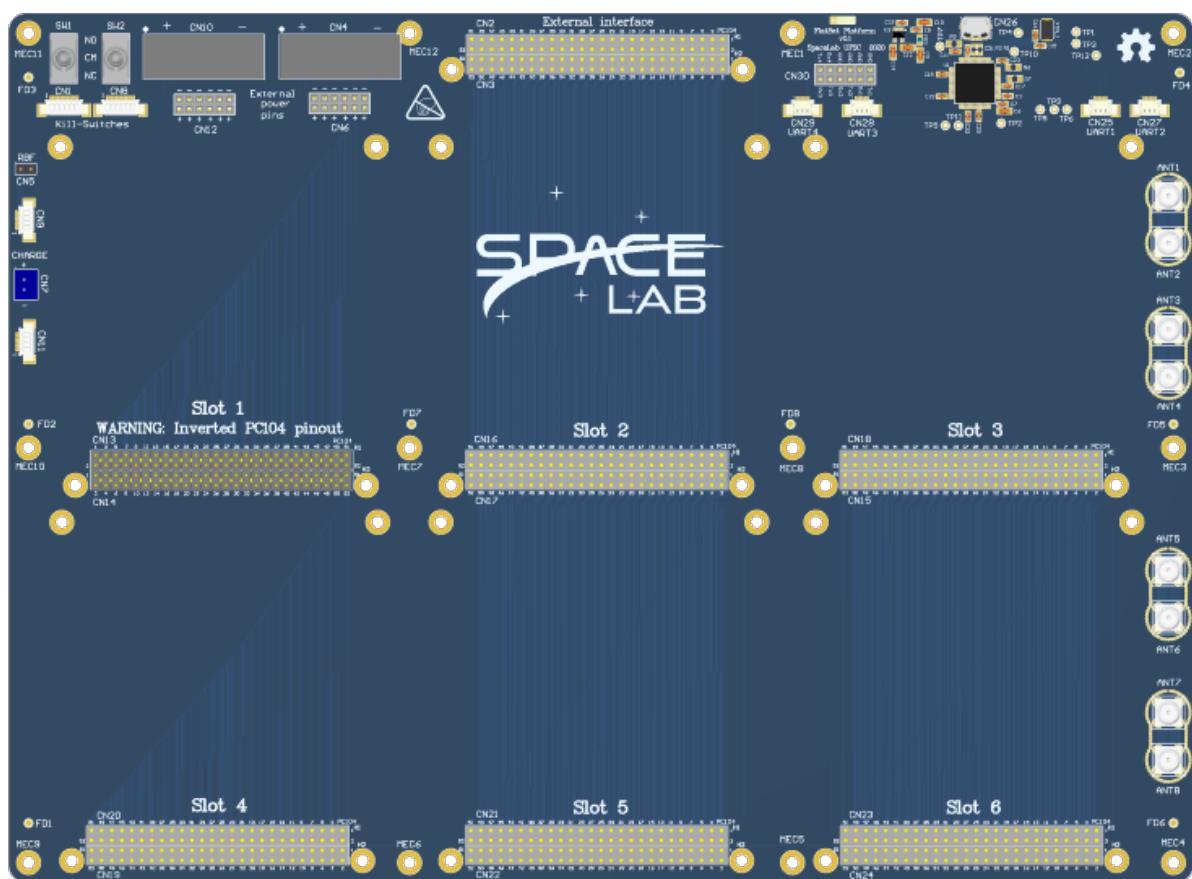


Figure 6.1: Top view of the flatsat board.

CHAPTER 7

Ground Segment

CHAPTER 8

Operation Planning

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