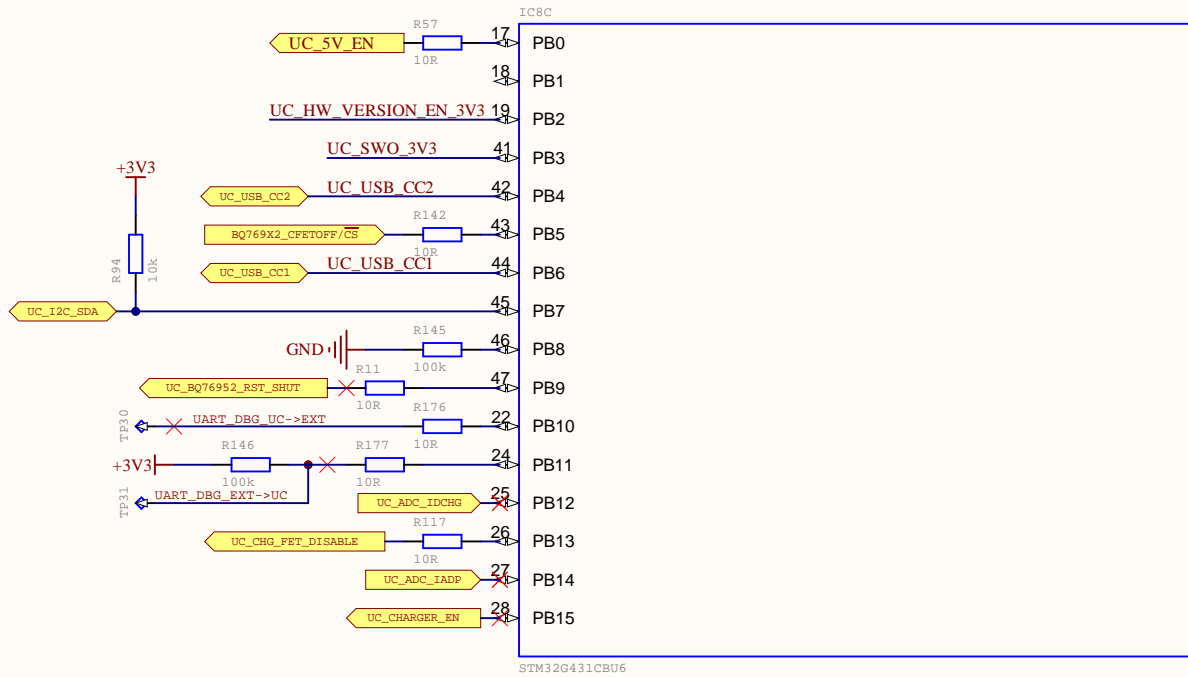
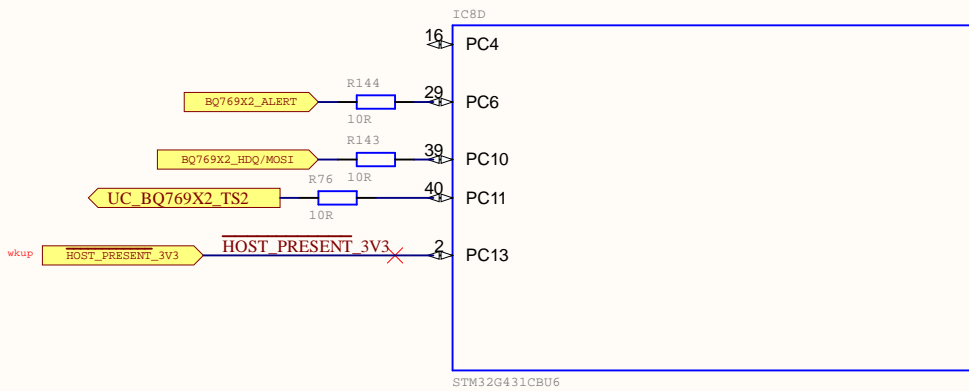


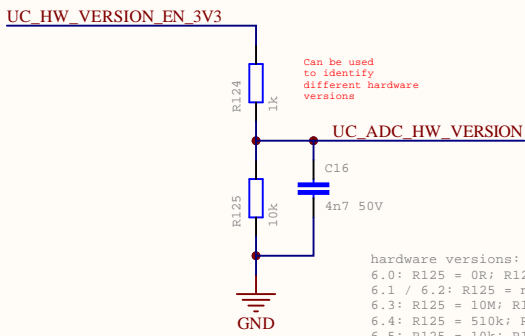
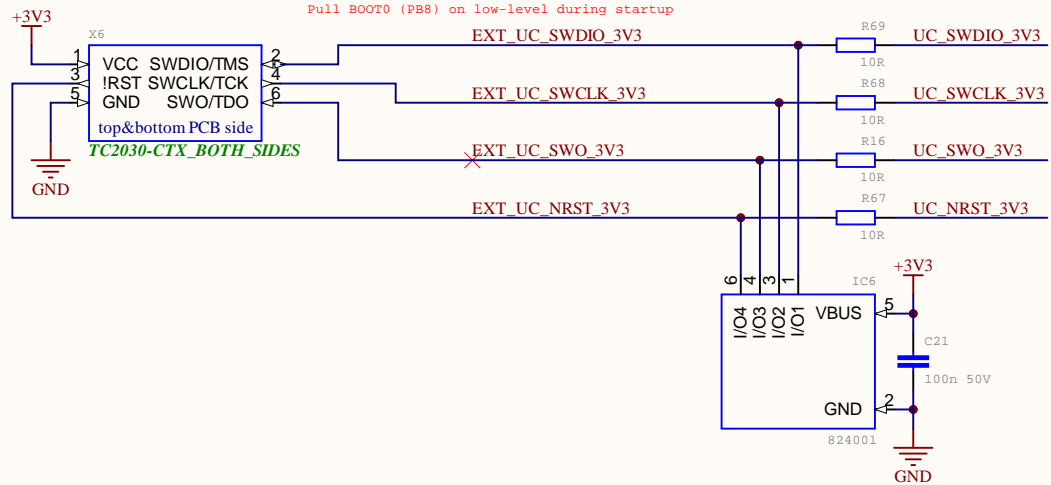
40 kR pull-up on PA13 & PA15 enabled after reset
40 kR pull-down on PA14 enabled after reset

UCPD1_DBCCx (PA9/PA10) must be bridged
to the corresponding UCPD1_CcX (PB6/PB4)
for supporting dead battery functionality

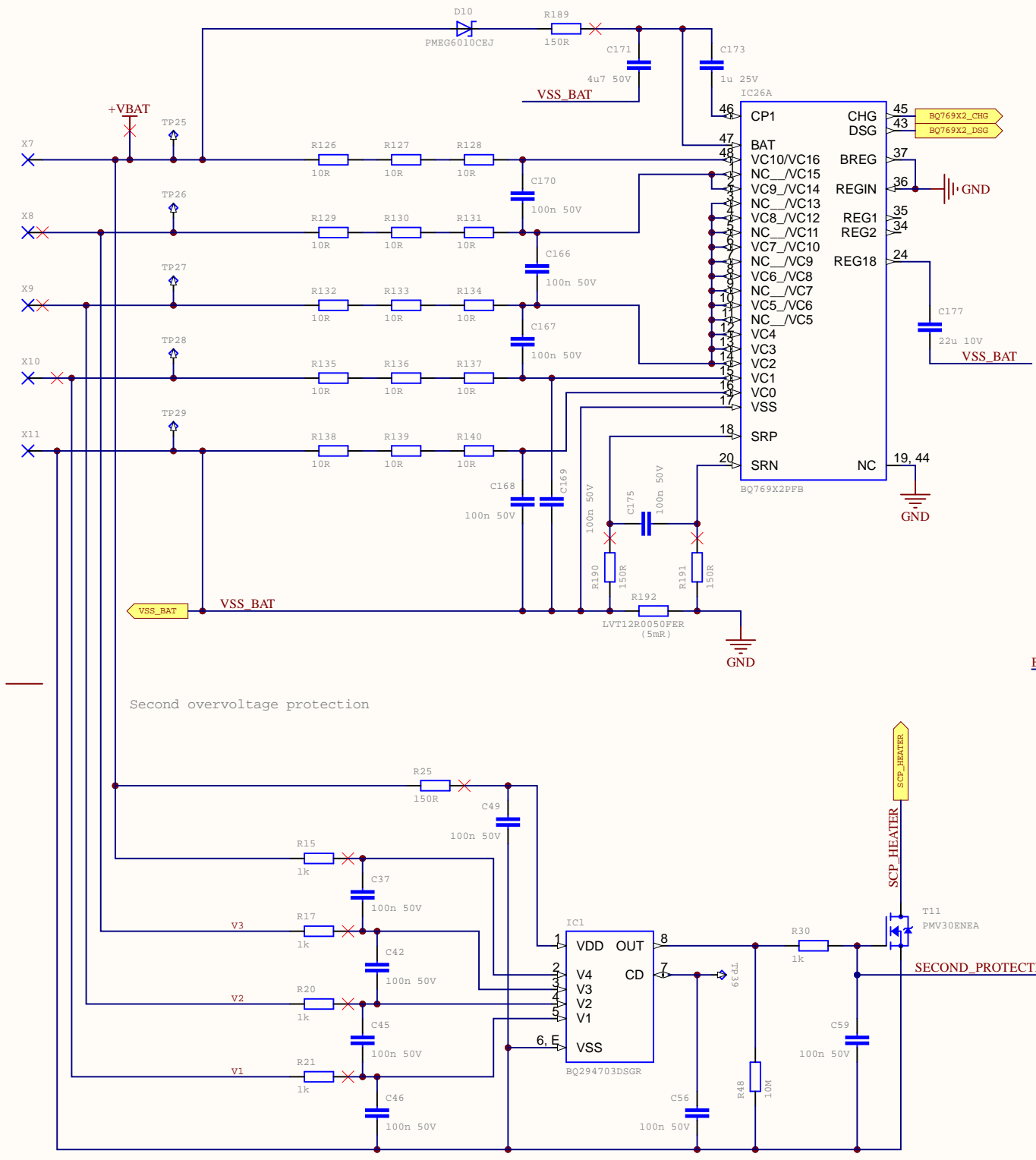


40 kR pull-up on PB4 enabled after reset

STM32 SWD programming interface



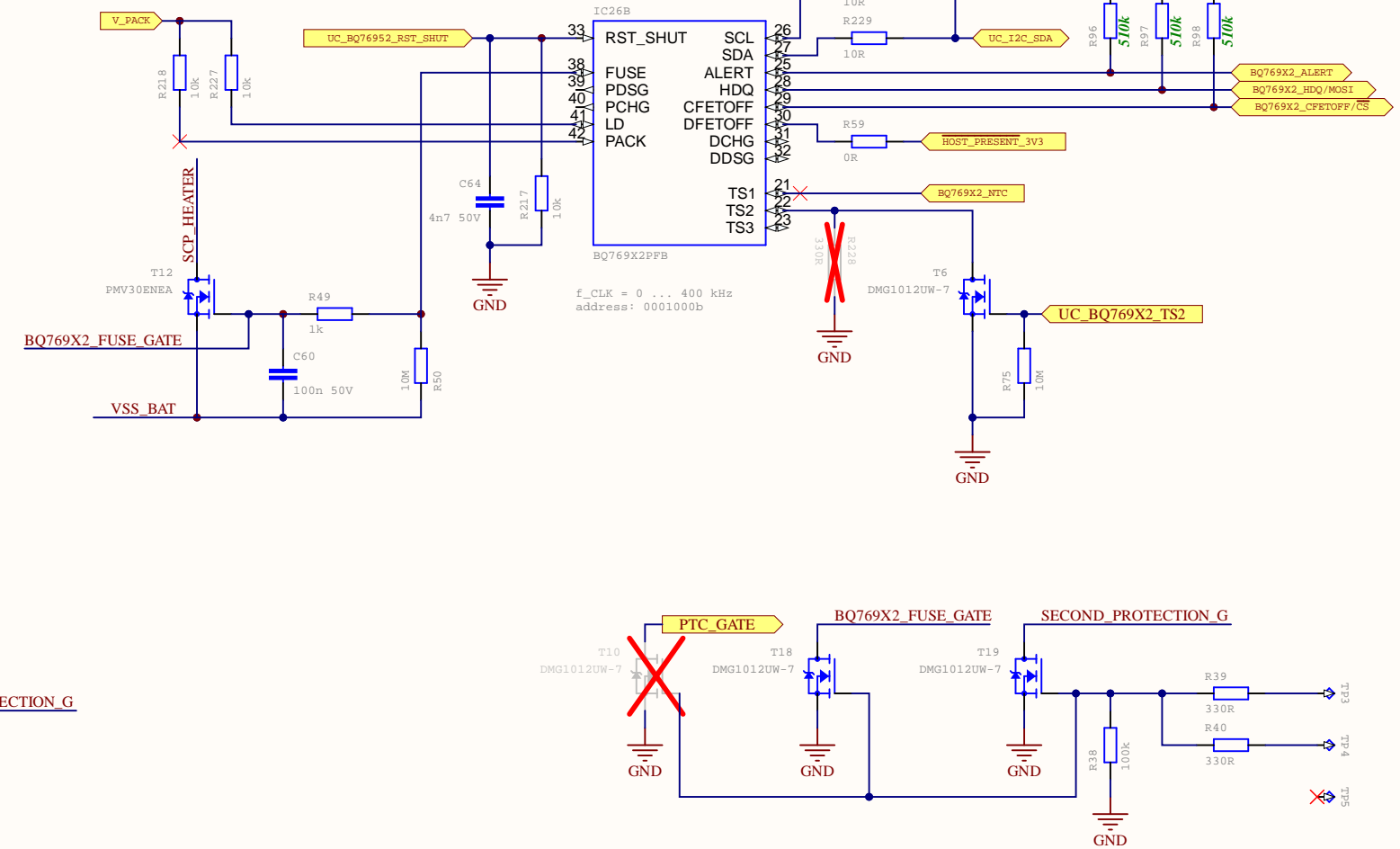
hardware versions:
6.0: R125 = 0R; R124 = n.m. -> +3V3 * 0 %
6.1 / 6.2: R125 = n.m.; R124 = 0R -> +3V3 * 100 %
6.3: R125 = 10M; R124 = 10M -> +3V3 * 50 %
6.4: R125 = 510k; R124 = 10M -> +3V3 * 4.8 %
6.5: R125 = 10k; R124 = 1k -> +3V3 * 90.9 %



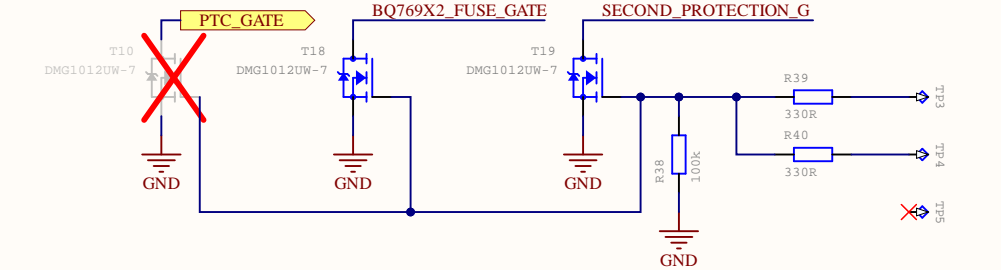
Second overvoltage protection

Delay time can be reduced
for device testing by pulling
CD to GND.

Battery Monitor 2/2



f_CLK = 0 ... 400 kHz
address: 0001000b



A

A

B

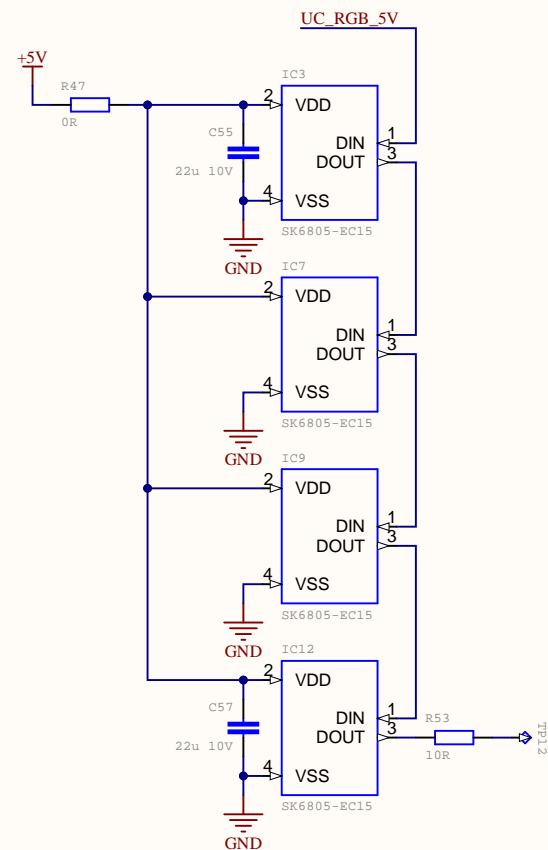
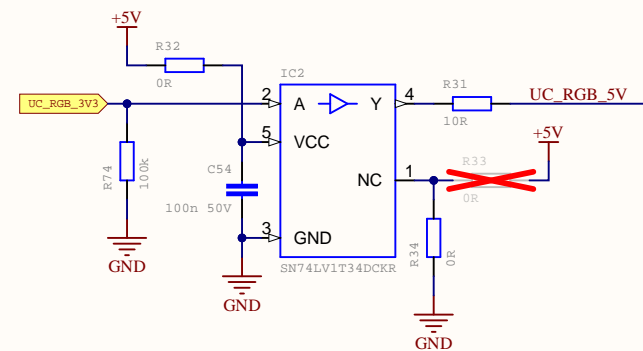
B

C

C

D

D



Temperature Sensors

