VIVEKANAND EDUCATION SOCIETY'S
INSTITUTE OF TECHNOLOGY
Department of Electronics
Academic Year: 2023-24

Digital Design with Reconfigurable Architecture
PROJECT REPORT

PROJECT TITLE

PC: Program Counter

SUBJECT TEACHER

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TEAM MEMBERS

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INTRODUCTION:

- Program Counter is very important part of any microprocessor or microcontroller. Its job is the store the address of next instruction to be executed. It is basically a combination of up counter and PIPO register. For general instruction, PC simply increase the count by 1 after every instruction gets executed. For Branch instructions the PC directly loads the address of next instruction to be executed. These are the 2 modes of operation of a PC. These 2 modes are selected using 2 control signals i.e increment & load.

- We have designed a 4-bit program counter capable of counting from 0000 to 1111 in binary, (0 to F) in hexadecimal, thereby enabling the execution of basic programs in digital systems.

METHODOLOGY:

- The design methodology involves a combination of digital logic design principles and sequential circuit techniques. We used hardware description language (HDLs) such as VHDL for the design, simulation, and synthesis of the program counter.

- The implementation is simulated using software tools like Xilinx Vivado, and then synthesized for deployment on programmable logic devices such as FPGAs.

- We have implemented our design on Spartan-7 FPGA - Boolean board.

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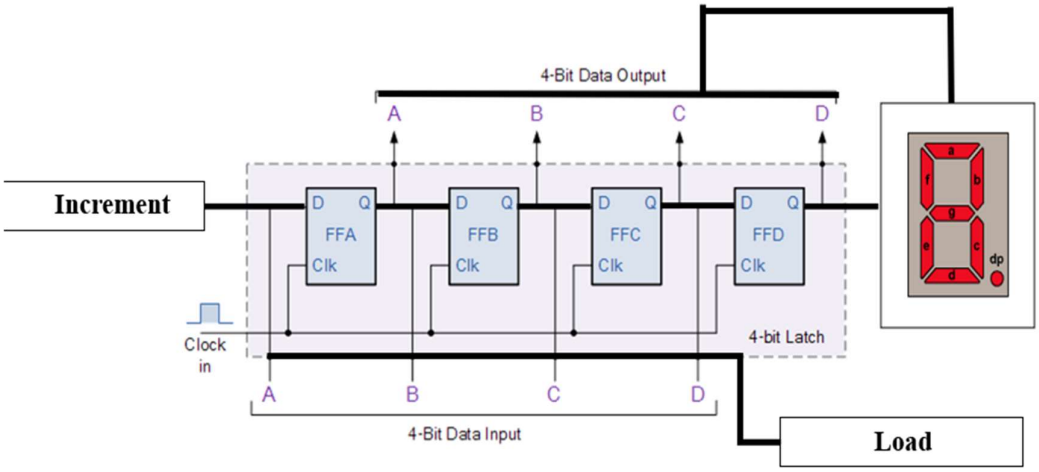
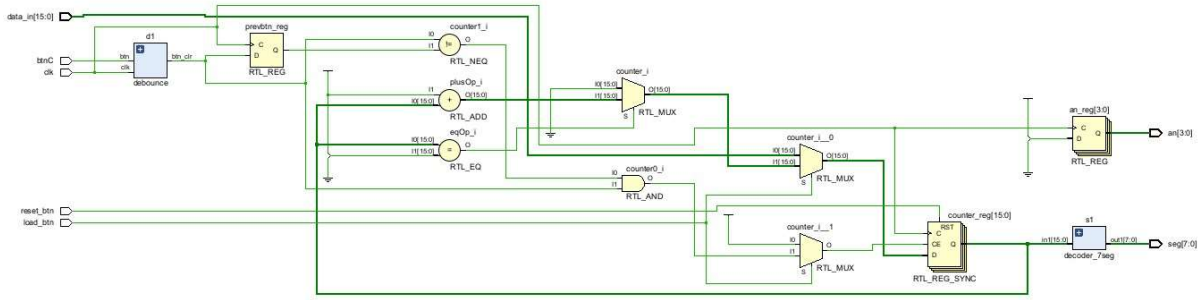
RESULTS:

The implemented 4-bit program counter successfully counts from 0 to F in Hexadecimal, incrementing on each clock cycle when increment button is pressed. When the load button is pressed it loads the input data value in the output signal.
Output signals accurately reflect the current count value. The design meets the specified objectives and performs reliably within the defined operational parameters.

- **Block diagram for Program Counter:**



- **Schematic for our VHDL code:**



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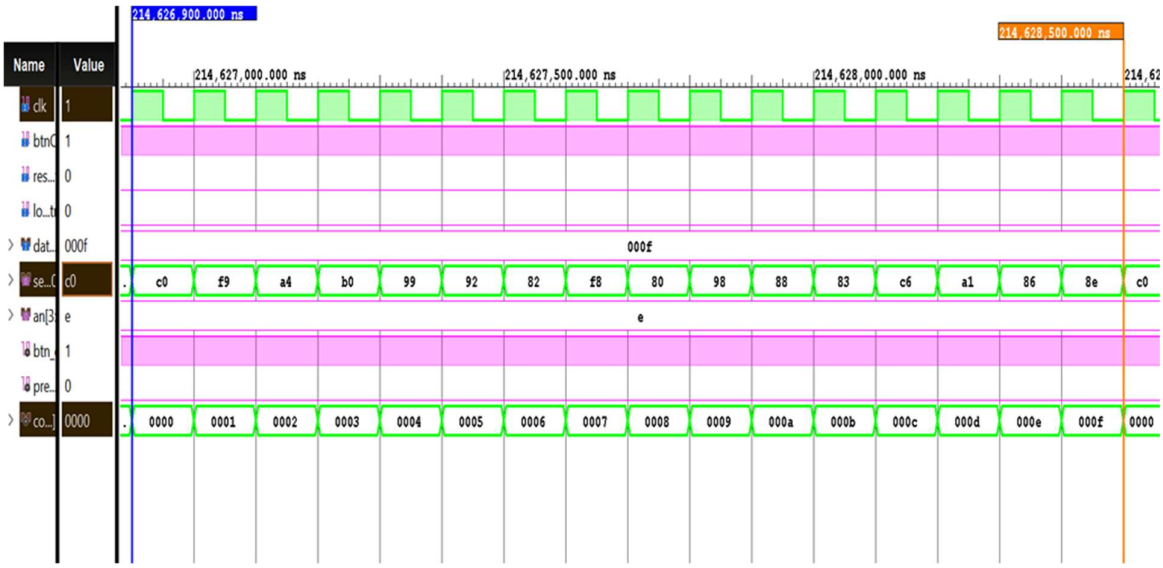
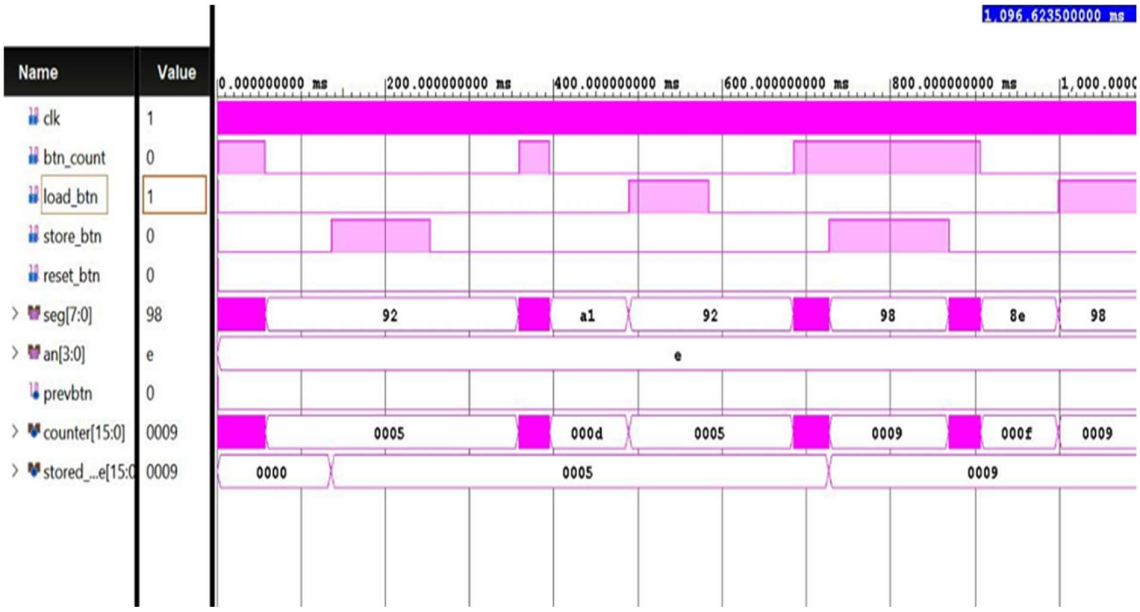
- **Simulation:**

1. **when increment button is pressed (PC is counting from 0 to F)**



2. **when load button is pressed it gives input data value in output.**



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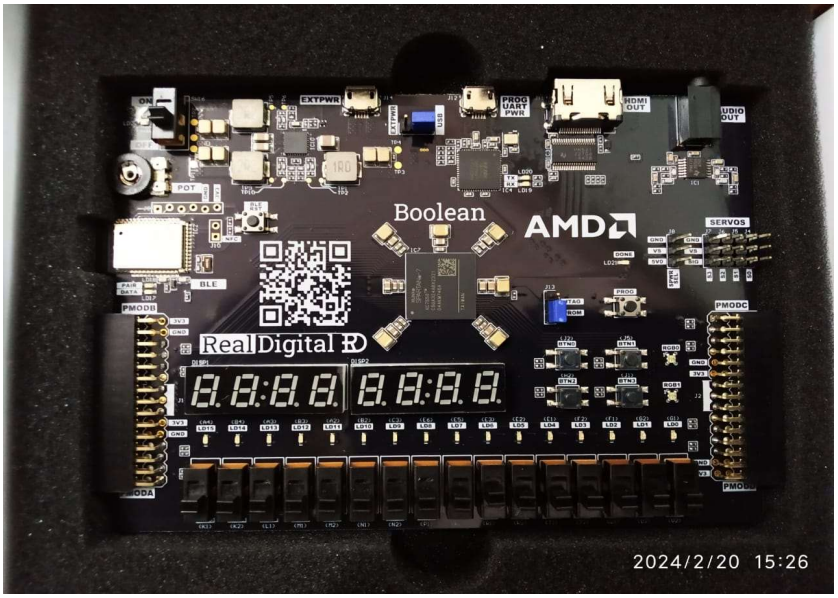
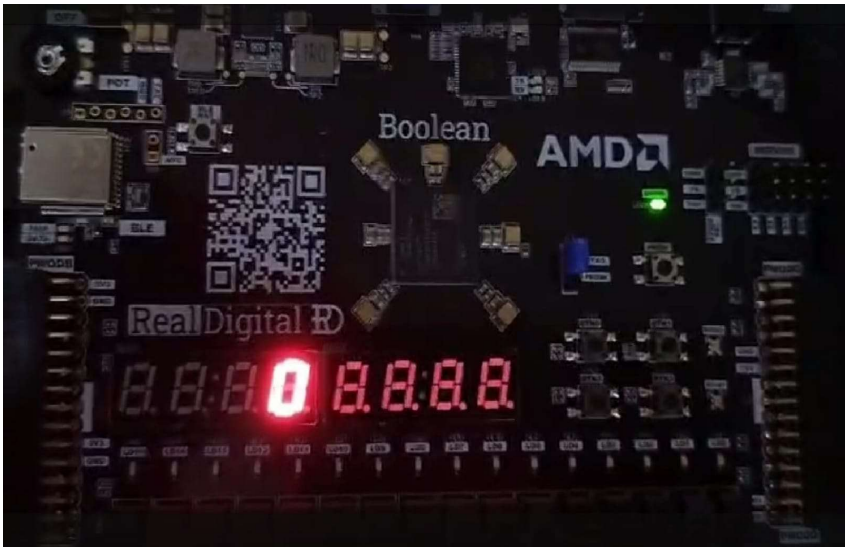
- **FPGA Board Used:** **Xilinx Spartan-7 XC7S50-CSGA324 FPGA**





Specifications of Boolean board:
USB port for power, programming and UART/COM port
Bluetooth Low Energy (BLE) radio
HDMI source (up to 1080p)
16MB QSPI
8-digit seven-segment display
16 slide switches
4 pushbuttons
16 discrete LEDs
2 RGB LEDs
Audio out (PWM)
Four servo motors
On-board ADC with thumbwheel potentiometer
2 Pmod+ connectors (4 Pmods)

Here is the output video of our design implemented on FPGA:

https://drive.google.com/file/d/1i78sPInkYVmyKubj1UIyee_uBzZF3wh7/view?usp=sharing

APPLICATIONS:

1. **Microcontrollers and Microprocessors:** In microcontrollers and microprocessors, a 4-bit program counter is often used to sequence through program instructions stored in memory. It helps fetch the next instruction address during each clock cycle.

2. **Digital Signal Processors (DSPs):** DSPs require program counters to execute instructions sequentially, so a 4-bit counter is suitable for simple DSP applications or as part of a larger address generation system.

3. **Embedded Systems:** In embedded systems with limited memory and simple instruction sets, a 4-bit program counter can efficiently manage program flow and execute basic operations.

4. **State Machines:** In finite state machines (FSMs), a 4-bit counter can serve as a state register, cycling through states based on external inputs or clock pulses.

5. **Instruction Decoding and Control:** The output of a 4-bit program counter is often used to select instructions from memory, enabling instruction decoding and control logic to execute the desired operations this logic can be used for control purpose.

6. **Address Generation:** In memory-mapped systems, a 4-bit program counter generates memory addresses for accessing data or instructions stored in memory.

CONCLUSION:

The design and implementation of the 4-bit program counter demonstrate its essential role in digital systems. The project achieves its objectives of providing a reliable execution. A 4-bit program counter may be suitable for simple digital systems and microcontrollers with limited memory and basic instruction sets, more complex applications typically require larger address spaces and more sophisticated addressing schemes. The choice of program counter width depends on the specific requirements of the application, including memory size, instruction set architecture, and performance goals.