

Scrum Results Sheet:

Team Number: 6

Date: Mar 4, 2021

Name #1	
Amr Almoallim	Since last scrum: <ul style="list-style-type: none"><li>• Trying different stuff to get rfs working, different flashers, setups, firmwares</li></ul>
	Working on today: <ul style="list-style-type: none"><li>• Ordered new ftdi cable and will look into how to convert map image into bitmap</li><li>• So that it is understood how to map in on the vga</li></ul>
	Roadblocks: <ul style="list-style-type: none"><li>• Need discussion with Tawsif regarding map specifications: what parts to send,</li><li>• Where will it be sent from, resolution issues, color format, programming interface</li></ul>
Name #2	
Cody Li	Since last scrum: <ul style="list-style-type: none"><li>• Finished layout and Login on android app</li></ul>
	Working on today: <ul style="list-style-type: none"><li>• Adding a shopping cart interface</li></ul>
	Roadblocks: <ul style="list-style-type: none"><li>• taking time cause still learning android practices</li></ul>
Name #3	
Jonathan Lew	Since last scrum: <ul style="list-style-type: none"><li>• Planning stubs</li></ul>

	Working on today: <ul style="list-style-type: none"> <li>• Stub functions for bluetooth</li> </ul>
	Roadblocks:
Name #4	
Jared Paul	Since last scrum: <ul style="list-style-type: none"> <li>• Tried compiling pathfinding on quartus and we ran out of space on the FPGA (out of LUTs)</li> </ul>
	Working on today: <ul style="list-style-type: none"> <li>• Figuring out how we can use less logic blocks for pathfinding</li> </ul>
	Roadblocks: <ul style="list-style-type: none"> <li>• FPGA space constraints</li> </ul>
Name #5	
Tawsif Hasan	<ul style="list-style-type: none"> <li>• Since last scrum: Used python OpenCV libraries to identify store sections from store map diagrams to generate test data.</li> </ul>
	Working on today: <ul style="list-style-type: none"> <li>• Design the persistent layer/ postgres database to store the store, item and user information.</li> </ul>
	Roadblocks: N/A