

**Architectural Design Phase Defects Checklist**

| Nr. | Check Point / Defect Statement                          | Check Mark (✓) the Appropriate Column |     |
|-----|---|---------------------------------------|-----|
|     |   | Yes                                   | N/A |
| C01 | Decision logic is erroneous or inadequate.              |                                       | ✓   |
| C02 | Branching is erroneous.                                 |                                       | ✓   |
| C03 | There are undefined loop terminations.                  |                                       | ✓   |
| C04 | I/O format errors exist.                                |                                       | ✓   |
| C05 | Subprogram invocations are violated.                    |                                       | ✓   |
| C06 | There are errors in preparing or processing input data. |                                       | ✓   |
| C07 | Output processing errors exist.                         |                                       | ✓   |
| C08 | Error message processing errors exist.                  | ✓                                     |     |
| C09 | There is confusion in the use of parameters.            | ✓                                     |     |
| C10 | There are errors in loop counters.                      |                                       | ✓   |
| C11 | Errors are made in writing out variable names.          | ✓                                     |     |
| C12 | Variable type and dimensions are incorrectly declared.  |                                       | ✓   |