

The not-so-quick list of compatible LTTV and LTTng versions :
(please scroll to the bottom to see the latest versions)

LTTV	LTTng	LTT Control	LTTng modules	LTTng userspace tracer / markers-userspace	Genevent	Trace Major.Minor	Kernels-architectures	Comments	Kernel Regression ok	Regression fails/not tested
0.6.9	0.4.4		0.3		0.2	0.4	2.6.12-rc4-mm2-i386 (tarball) 2.6.12-rc4-i386 (git) 2.6.9-i386 (contribution) 2.6.9-x86_64 (contribution)			
0.7.0	0.4.5		0.3		0.2	0.5	2.6.12-rc4-i386 (git)	Traces can now be written/read between 32 and 64 bits arch.		
0.8.0	0.5.0		0.3		0.3	0.6	2.6.12-rc4-i386 (git) 2.6.12-i386 (git) 2.6.13-i386 (git)			
0.8.0	0.5.0		0.4		0.3	0.6	2.6.14-i386 (git)			
0.8.0	0.5.0a		0.4		0.3	0.6	2.6.14-i386 (tarball)			
0.8.1 0.8.2 0.8.3 0.8.4	0.5.1		0.4		0.4	0.6	2.6.14-i386 (git) 2.6.14-i386 (tarball) 2.6.15-i386 (git) 2.6.15-i386 (tarball)	Per architecture syscall enumeration		
0.8.1 0.8.2 0.8.3 0.8.4	0.5.3 0.5.4 0.5.5		0.4		0.4 0.5	0.6	2.6.15-i386 (git) 2.6.15-i386 (tarball)	APIC NMI watchdog can now be logged		
0.8.5	0.5.6		0.4		0.6	0.6	2.6.15-i386 (git) 2.6.15-i386 (tarball)	Custom write function in genevent (for process stack dump) Use atomic_cmpxchg()		
0.8.6 0.8.7 0.8.8 0.8.9	0.5.7 0.5.8 0.5.9a 0.5.10 0.5.11 0.5.13 0.5.16		0.4		0.6 0.7 0.8	0.7	2.6.15-i386 (git) 2.6.15-i386 (tarball)	Support for architectures without TSC. LTTV 0.8.7/genevent 0.7 fixes a bug in enum XML parsing.		
0.8.10 0.8.11 0.8.12	0.5.17		0.4		0.8	0.7	2.6.15-i386 (git) 2.6.15-i386 (tarball)	Multithreaded ltttd. Stack dump fix. LTTV 0.8.12 fixes timing problem in tracecontrol module.		

0.8.13	0.5.18a		0.5		0.8	0.7	2.6.15-i386	Kernel State Dump : get		
0.8.14	0.5.19		0.6		0.9		(git)	vmaps, process names, fd at		
0.8.15			0.7				2.6.15-i386	the beginning of a trace.		
0.8.16							(tarball)			
0.8.17										
0.8.18	0.5.20		0.6		0.10	0.7	2.6.15-i386	Add kernel stack dump		
0.8.19	0.5.20a		0.7		0.11		(git)	instrumentation and facility.		
0.8.20	0.5.22		0.8		0.12		2.6.15-i386	LTTV 0.8.21+genevent 0.11 :		
0.8.21	0.5.23						(tarball)	add support for network byte		
0.8.22	0.5.24							order data with the " network		
0.8.23	0.5.25							tag in the XML file.		
0.8.24	0.5.27							LTTng 0.5.27+LTTV 0.8.24		
0.8.25	0.5.28							introduces the heartbeat		
								timer.		
								LTTng 0.5.28 introduces the		
								synthetic 64 bits TSC for		
								archs with 32 bits TSC.		
0.8.26	0.5.29		0.8	0.1	0.13	0.7	2.6.15-i386	Add user space tracing		
	0.5.29a						(git)	through system call.		
							2.6.15-i386			
							(tarball)			
0.8.27	0.5.29		0.8	0.2	0.14	0.7	2.6.15-i386	Add function instrumentation		
	0.5.29a		0.9	0.3			(git)	tracing (gcc -finstrument-		
	0.5.30			0.4			2.6.15-i386	functions)		
							(tarball)			
0.8.28	0.5.33		0.10	0.5	0.15	0.7	2.6.15-i386	Fast "Usertrace"		
	0.5.34				0.16		(git)	Bugfixes.		
							2.6.15-i386	Network ip interfaces state.		
							(tarball)			
0.8.29	0.5.33		0.10	0.6	0.17	0.7	2.6.15-i386	XML facilities description		
0.8.30	0.5.34						(git)	format changed. Now XML		
							2.6.15-i386	1.0 : breaks compatibility		
							(tarball)	with old XML files.		
								LTTV 0.8.30 changes the		
								statistics tree structure. It		
								adds user functions statistics		
								support.		
0.8.31	0.5.34	0.1	0.10	0.6	0.17	0.7	2.6.15-i386	Separate the viewer from the		
0.8.32	0.5.36		0.11				(git)	tracing controller (useful for		
0.8.33							2.6.15-i386	embedded tracing).		
0.8.34							(tarball)			
0.8.34	0.5.37	0.1	0.12	0.7	0.17	0.7	2.6.16-i386	Kernel 2.6.16 support.		
0.8.35	0.5.38	0.2		0.8	0.18		(git)	LTTng 0.5.38 : Fix deadlock		
	0.5.39						2.6.16-i386	scenario involving deliver		
	0.5.40						(tarball)	from an NMI handler.		
	0.5.41							LTTng 0.5.40/ltt-usertrace		
								0.8 : Fix off-by-one which		
								pushes the reader with small		
								subbuffers (only in buffer full		
								condition)		
								genevent 0.18 fixes missing		
								nested subtypes align/write		
								functions.		
0.8.34	0.5.42	0.3	0.13	0.7	0.17	0.7	2.6.16-i386	Improvements in ltt-		
0.8.35	0.5.43	0.4	0.14	0.8	0.18		(git)	statedump process state.		
							2.6.16-i386	LTTng 0.5.43 : Add ARM		
							(tarball)	IRQ statedump.		
0.8.34	0.5.44	0.5	0.13	0.7	0.17	0.7	2.6.16-i386	Add IRQ		

0.8.35 0.8.36	0.5.45		0.14 0.15	0.8	0.18		(git) 2.6.16-i386 (tarball)	enable/disable/save/restore instrumentation under locking.xml. Various architecture specific fixes. LTTV : Add process names to textDump.		
0.8.37 0.8.38 0.8.39 0.8.40	0.5.46	0.6	0.15	0.8	0.18	0.7	2.6.16-i386 (git) 2.6.16-i386 (tarball)	Add C2 Microsystems support in LTTV and ltt-control. Include fixes to instrumentation of MIPS and PPC from Yuri Frolov. Fix state dump states in LTTV. Fix interrupt and diskperformance views.		
0.8.37 0.8.38 0.8.39 0.8.40	0.5.47a	0.6	obsolete	0.8	0.18	0.7	2.6.16-i386 (git) 2.6.16-i386 (tarball)	Merge ltt-modules into the kernel tree.		
0.8.41 0.8.42 0.8.43 0.8.44	0.5.48 0.5.49 0.5.51 0.5.52 0.5.52a 0.5.52b 0.5.52c 0.5.55 0.5.56 0.5.57	0.6 0.7	obsolete	0.8 0.9 0.10	0.19 0.20 0.21	0.7	2.6.16-i386 (git) 2.6.16-i386 (tarball)	Fix LTTV for PowerPC. Integrate some fixes useful for other architectures. Basic PowerPC tracing (incomplete instrumentation). LTTng 0.5.51, 0.5.52, genevent 0.20 : code indentation fix. LTTV 0.4.42, LTTng 0.5.55, LTT control 0.7, genevent 0.21 : PowerPC 32 and 64 bits support. LTTng 0.5.56 : MIPS time frequency fix. LTTng 0.5.57 : add 32 bits compat execve tracing (for 64 bits architectures). LTTV 0.8.43 : fix trace start time, 0.8.44 : interrupt view fixes. ltt-usertrace : add a java tracing example.		
0.8.45 0.8.46 0.8.47	0.5.58 0.5.59 0.5.60 0.5.61 0.5.62 0.5.63 0.5.64 0.5.65 0.5.66 0.5.68a 0.5.69	0.8 0.9 0.10	obsolete	0.8 0.9 0.10 0.11 0.12	0.19 0.20 0.21 0.22	0.7	2.6.16-i386 (git) 2.6.16-i386 (tarball)	Add support for fs_data facility : get the first 32 bytes of data in read and write system calls. LTTng 0.5.59 : Fix usertrace facility registration : fork vs exec. LTTV 0.8.46 : fix end of process in state.c. LTTng 0.5.65 : Fix ltt-heartbeat. LTTng 0.5.69 : Fix ltt-usertrace blocking mode : causes OOPS on trace stop.		
0.8.45 0.8.46 0.8.47	0.5.70 0.5.71	0.11	obsolete	0.13	0.19 0.20 0.21	0.7	2.6.17 (git) 2.6.17 (tarball)	LTTng 0.5.71 : Fix a bug with ltt-usertrace blocking mode.		

					0.22					
0.8.48	0.5.72a 0.5.72b	0.12	obsolete	0.13	0.19 0.20 0.21 0.22	0.7	2.6.17 (git) 2.6.17 (tarball)	Add TGID. Merge Tim Bird fixes for compiling LTTV out of tree.		
0.8.49	0.5.73 0.5.74	0.13	obsolete	0.14 0.15	0.23	0.7	2.6.17 (git) 2.6.17 (tarball)	Add x86_64 support (Martin Bisson for Autodesk). Make LTTV control flow lines thicker (Tim Bird).		
0.8.49 0.8.50 0.8.51 0.8.52 0.8.53	0.5.75 0.5.75a 0.5.76 0.5.78	0.13	obsolete	0.14 0.15	0.24	0.7	2.6.17 (git) 2.6.17 (tarball)	Integrate transport abstraction from Richard Purdie. LTTV 0.8.52 : Add "stop" event handling to detailed event list (gui).		
0.8.49 0.8.50 0.8.51 0.8.52 0.8.53	0.5.79	0.14 0.15 0.16 0.17	obsolete	0.14 0.15	0.24	0.7	2.6.17 (git) 2.6.17 (tarball)	Separate the transport mechanism from the tracing (Richard Purdie).		
0.8.49 0.8.50 0.8.51 0.8.52 0.8.53 0.8.54 0.8.55 0.8.56 0.8.57 0.8.59	0.5.80 0.5.81 0.5.82 0.5.83 0.5.86 0.5.87 0.5.88 0.5.89 0.5.90 0.5.91 0.5.92 0.5.94 0.5.95 0.5.96 0.5.98 0.5.99 0.5.100 0.5.103	0.14 0.15 0.16 0.17 0.18	obsolete	0.15 0.16 0.17 0.18	0.24 0.25 0.26 0.27	0.7	2.6.17 (git) 2.6.17 (tarball)	Fix syscall enum on x86_64. Fix types in ltt-core.h. ltt-usertrace 0.16 makes headers completely kernel independant, which is sane. genevent 0.25, ltt-usertrace 0.18 and lttng 0.5.83 add support for g++. LTTV 0.8.55 fixes stats accounting for PID 0 at beginning of trace. LTTng 0.5.87 fixes x86 kernel and process stack dump and adds nice menu options for it. LTTV 0.8.57 fixes a bug recently introduced in event list scroll/page/move up. LTTng 0.5.88 fixes an important bug, present in all LTTng versions, which makes it discard silently every event nested on an already executing probe. genevent 0.27 and ltt- usertrace 0.18 fix a problem with alignment of the userspace printf event. LTTng 0.5.101 Adds sysenter/sysexit instrumentation. It also fixes i386 stack dump.		
0.8.49 0.8.50 0.8.51 0.8.52 0.8.53 0.8.54 0.8.55 0.8.56 0.8.57 0.8.59	0.5.104	0.19	obsolete	0.15 0.16 0.17 0.18	0.24 0.25 0.26 0.27	0.7	2.6.17 (git) 2.6.17 (tarball)	High, medium and low event rate channel size and number of subbuffers. Default : high 1MB, medium 256KB, low 64KB.		

0.8.49 0.8.50 0.8.51 0.8.52 0.8.53 0.8.54 0.8.55 0.8.56 0.8.57 0.8.59	0.5.105	0.20	obsolete	0.15 0.16 0.17 0.18	0.24 0.25 0.26 0.27	0.7	2.6.17 (git) 2.6.17 (tarball)	Add hybrid mode : High event rate channels are in flight recorder mode, low/medium rate channels in normal tracing mode.		
0.8.60	0.5.106 0.5.107	0.21	obsolete	0.19	0.28	0.7	2.6.17 (git) 2.6.17 (tarball)	Send statedump events to medium rate channels, thread branding is now "high_priority" : saved to the processes channel. LTTng 0.5.107 : fixes a potential oops when freeing the trace structure (use kref now).		
0.8.61 0.8.62	0.5.111 0.5.112 0.5.113	0.22 0.23 0.24	obsolete	0.19	0.28	0.7	2.6.17 (git) 2.6.17 (tarball)	Now use Relay+DebugFS. Add printk instrumentation.		
0.8.61 0.8.62	0.5.112	0.23	obsolete	0.19	0.28	0.7	2.6.17 (git) 2.6.17 (tarball)	Add printk instrumentation.		
0.8.61 0.8.62	0.5.113	0.24	obsolete	0.19	0.28	0.7	2.6.17 (git) 2.6.17 (tarball)	Add printk_locate instrumentation.		
0.8.61 0.8.62	0.6.0preX	0.24	obsolete	0.19	SVN head	0.7	2.6.17 (git) 2.6.17 (tarball)	Marker+Probe mechanism.		
0.8.61 0.8.62	0.6.0	0.25	obsolete	0.19	0.29	0.7	2.6.17 (git) 2.6.17 (tarball)	LTTng 0.6.0 with markers and probes.		
0.8.61 0.8.62	0.6.2	0.26	obsolete	0.20	0.29	0.7	2.6.18 (git) 2.6.18 (tarball)	2.6.18 kernel.		
0.8.61 0.8.62 0.8.63 0.8.64 0.8.65 0.8.66 0.8.67 0.8.68 0.8.69 0.8.70	0.6.3 0.6.4 0.6.5 0.6.6 0.6.7 0.6.8 0.6.9 0.6.10 0.6.11 0.6.13 0.6.14 0.6.15 0.6.16 0.6.17 0.6.18 0.6.19 0.6.20 0.6.21 0.6.22 0.6.23 0.6.24 0.6.25 0.6.26 0.6.27	0.27 0.28 0.29	obsolete	0.20 0.21	0.29 0.30	0.7	2.6.18 (git) 2.6.18 (tarball)	Locking, hardirq and softirq instrumentation. Coding style fixes. Round to count order for subbuffer size and number of subbuffers. Fix ltt-statedump with unnamed irq chips. LTTng 0.6.18 fixes an important bug in LTT statedump (semaphore on the stack). LTTng 0.6.23 implements optimise per-cpu atomic operations for non shared variables. It provides cheap NMI protection.		
0.8.61	0.6.28	0.30	obsolete	0.20	0.29	0.7	2.6.18 (git)	Syscall facilities update for		

0.8.62	0.6.29			0.21	0.30		2.6.18 (tarball)	2.6.18. 0.8.29 fixes a typo in asm-generic/atomic-up.h.		
0.8.63	0.6.30									
0.8.64	0.6.31									
0.8.65	0.6.32									
0.8.66										
0.8.67										
0.8.68										
0.8.69										
0.8.70										
0.8.71										
0.8.72										
0.8.61	0.6.35	0.30	obsolete	0.20	0.31	0.7	2.6.18 (git)	Add CPU hotplug support		
0.8.62	0.6.36			0.21			2.6.18 (tarball)	(hotplug events in Relay and inotify support in debugfs and ltt).		
0.8.63	0.6.36a									
0.8.64	0.6.37									
0.8.65	0.6.38									
0.8.66	0.6.39									
0.8.67	0.6.40									
0.8.68	0.6.41									
0.8.69										
0.8.70										
0.8.71										
0.8.72										
0.8.61	0.6.42	0.32	obsolete	0.22	0.31	0.7	2.6.19 (git)	2.6.19 support.		
0.8.62	0.6.42a						2.6.19 (tarball)	LTTV 0.8.73 has important statistics fixes.		
0.8.63	0.6.43							LTTV 0.8.76 adds TASK_DEAD support, new task state from 2.6.19.		
0.8.64	0.6.44									
0.8.65	0.6.45									
0.8.66	0.6.46									
0.8.67	0.6.47									
0.8.68	0.6.48									
0.8.69										
0.8.70										
0.8.71										
0.8.72										
0.8.73										
0.8.74										
0.8.75										
0.8.76										
0.8.77										
0.8.78										
0.8.79										
0.8.61	0.6.51	0.33	obsolete	0.22	0.31	0.7	2.6.20-rc1-git7 (git)	2.6.20-rc1-git7 support.		
0.8.62	0.6.52						2.6.20-rc1-git7 (tarball)	LTTng 0.6.52 fixes the ltt-heartbeat synthetic TSC with cpu hotplug.		
0.8.63								LTTng 0.6.52 marks ltt-heartbeat periodic events as EXPERIMENTAL, as it is problematic with cpu hotplug and trace stop/restart sequence.		
0.8.64										
0.8.65										
0.8.66										
0.8.67										
0.8.68										
0.8.69										
0.8.70										
0.8.71										
0.8.72										
0.8.73										
0.8.74										
0.8.75										
0.8.76										
0.8.77										
0.8.78										
0.8.79										
0.8.61	0.6.53	0.34	obsolete	0.23	0.31	0.7	2.6.20-rc4-git3 (git)	2.6.20-rc4-git3 support.		
0.8.62	0.6.54				0.32		2.6.20-rc4-	Change marker format to %p[type], %u[extended type],		
0.8.63	0.6.55									

0.8.64	0.6.55a						git3 (tarball)	i.e. : %p[struct task_struct], %u[__be]. LTTng 0.6.56 and genevent 0.32 changes the preempt_enable_no_resched() calls for a preempt_enable() call. LTTng 0.6.58 fixes markers for i386 : optimized version correctly does XMC following Pentium III erratum 49.		
0.8.65	0.6.56									
0.8.66	0.6.58									
0.8.67										
0.8.68										
0.8.69										
0.8.70										
0.8.71										
0.8.72										
0.8.73										
0.8.74										
0.8.75										
0.8.76										
0.8.77										
0.8.78										
0.8.79										
0.8.61	0.6.59	0.34	obsolete	0.23	0.31	0.7	2.6.20-rc5-git4 (git)	2.6.20-rc5-git4 support. Use cpuid instruction in pIII XMC.		
0.8.62	0.6.60				0.32		2.6.20-rc5-git4 (tarball)			
0.8.63	0.6.61									
0.8.64										
0.8.65										
0.8.66										
0.8.67										
0.8.68										
0.8.69										
0.8.70										
0.8.71										
0.8.72										
0.8.73										
0.8.74										
0.8.75										
0.8.76										
0.8.77										
0.8.78										
0.8.79										
0.8.61	0.6.62	0.34	obsolete	0.23	0.31	0.7	2.6.20-rc6	2.6.20-rc6 support.	ARM,	arm26, avr32,
0.8.62	0.6.63	0.35		0.24	0.32		ARM,	Use my own infrastructure to support intel pIII xmc instead of using kprobes.	mips,	cris, frv,
0.8.63	0.6.64			0.25			MIPS32/64,	ltt-usertrace 0.25 fixes signal reentrancy in ltt-usertrace-fast.	mipsel,	h8300, m32r,
0.8.64	0.6.68			0.26			powerpc32,	LTTng 0.6.64 adds 2.6.20 support.	powerpc64,	m68knommu,
0.8.65	0.6.69			0.27			powerpc64,	LTTng 0.6.68 fixes regression for MIPS, ARM, powerpc64.	i386,	parisc, sh,
0.8.66	0.6.70						ppc, i386,	LTTng 0.6.69 fixes menus, regression for i686 ok, fixes an upstream bug for sparc64.	sparc64,	sh64, um,
0.8.67	0.6.71						x86_64	LTTng 0.6.70 fixes regression for m68k.	m68k,	v850, xtensa.
0.8.68	0.6.72						2.6.20	LTTng 0.6.71 fixes regression for ppc 405 and sparc (fixing alignment of .markers.c section).	ia64, s390,	
0.8.69	0.6.73						ARM,	LTTng 0.6.71 fixes upstream error in ppc/powerpc.	sparc,	
0.8.70	0.6.74						MIPS32/64,	LTTng 0.6.72 fixes corrects .markers.c section declaration : fixes regression on sparc.	alpha.	
0.8.71	0.6.75						powerpc32,	LTTng 0.6.72 fixes regression on alpha.		
0.8.72	0.6.76						powerpc64,	ltt-usertrace 0.27 fixes a signal race.		
0.8.73	0.6.77						ppc, i386,			
0.8.74							x86_64			
0.8.75										
0.8.76										
0.8.77										
0.8.78										
0.8.79										

								LTTng 0.6.77 puts all process events in medium rate process channel. ltt-control 0.35 adds the Xen facility. Matches LTTng for xen-unstable up to changeset: 14390.		
0.8.80 0.8.81	0.6.78 0.6.80	0.36	obsolete	0.28	0.33	0.8	2.6.20 ARM, MIPS32/64, powerpc32, powerpc64, ppc, i386, x86_64	Add compact channel. Fix start of trace get full timestamp. Caused problems with time gap between trace create/start and 32 bits (or less) TSC in events. Ok for xen-unstable changeset starting at 14391. Adds TSC testing in kernel. Adds monotonic logical clock based on highest TSC count for x86 and x86_64 with async TSCs. Fix missing compat_fs_exec event : rename to fs_exec.		ARM, mips, mipsel, powerpc64, i386, sparc64, m68k, ia64, s390, sparc, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa.
0.8.82	0.9.0	0.37	obsolete	0.29	0.34	0.8	2.6.20 ARM, MIPS32/64, powerpc32, powerpc64, ppc, i386, x86_64	Introduces the LTT serialization library. Cuts the kernel patch size from 2M to 500k. Deprecates genevent for kernel instrumentation, but keeps backward compatibility with genevent generated code (still used for user space tracing). Slightly longer execution time at instrumentation site (270ns vs 200ns for 4 bytes write on P4 3GHz), but the global advantage of using less memory, and therefore less cache, should overweight this cost in cycles. Note : In this version, the ltt-probe-* kernel modules and the XML definitions must be kept in sync by hand, otherwise, LTTV will detect a size mismatch between the kernel trace and the expected event size..	arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.	alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa.
0.8.82	0.9.1 0.9.2 0.9.3 0.9.4 0.9.5 0.9.6	0.38 0.39 0.40 0.43	obsolete	0.30 0.31 0.32	0.34	0.8	2.6.21-rc6-mm1 2.6.21 2.6.21-mm2 ARM, MIPS32/64, powerpc32, powerpc64, ppc, i386, x86_64 2.6.22-rc2-mm1	Ported to Andrew Morton's tree. LTTng 0.9.3 and ltt-usertrace 0.32 fixes a syscall parameter size mismatch between 64 bits kernel and 32 bits user-space processes for user-space tracing. LTTng 0.9.5 adds support for kernel 2.6.21. LTTng 0.9.6 supports kernel 2.6.21-mm2.		alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.

0.8.83 0.8.84	0.9.7 0.9.8 0.9.9 0.9.10	0.41 0.42 0.43	obsolete	0.33	0.35	0.8	2.6.22-rc2-mm1 2.6.22-rc4-mm2	LTTng 0.9.7 supports kernel 2.6.22-rc2-mm1. automake updates in LTTV and ltt-control. format="" format strings support for XML descriptions. LTTng 0.9.9 adds support for kernel 2.6.22-rc4-mm2. LTTng 0.9.10 fixes a bug in i386 optimized immediate values. ltt-control 0.43 fixes a bug with LTTng lseek event.		alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.
0.8.83	0.9.10	0.41 0.42 0.43 0.44	obsolete	Not available (syscall IDs not updated)	obsolete	0.8	2.6.22.1-rt4	LTTng 0.9.10 port to 2.6.22.1-rt4. LTT control 0.44 fixes facilities makefile.	Tested on x86_32.	alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.
0.10.0-pre1	0.10.0-pre5 0.10.0-pre6 0.10.0-pre7 0.10.0-pre8 0.10.0-pre9 0.10.0-pre10 0.10.0-pre11 0.10.0-pre12 0.10.0-pre13 0.10.0-pre14 0.10.0-pre15 0.10.0-pre16	0.45	obsolete	Not available (syscall IDs not updated)	obsolete	1.0	2.6.23-mm1 2.6.23.1 2.6.24-rc1-git11 2.6.24-rc1-git13	Simplified trace_mark(). Remove XML. Per marker activation through /proc/ltt. Crash dump trace extraction. LTTV --edebg for raw binary even debugging, hexedit style. LTTng is now mostly arch-agnostic, using a non precise logical clock fallback. LTTng 0.10.0-pre6 is a compile fix. LTTng 0.10.0-pre7 adds the upstream 2.6.23.1 sata_mv fix. LTTng 0.10.0-pre7-port to 2.6.23.1. LTTng 0.10.0-pre8 fixes x86_64 segfault due to thread flags and fixes ltt-serialize x86_64 va_list argument passing array nonsense. LTTng 0.10.0-pre9 fixes SH and polishes the menus. LTTng 0.10.0-pre9 fixes s390 instrumentation mistake. LTTng 0.10.0-pre12 checkpatch coding style fixes. LTTng 0.10.0-pre16 Change LTT menu location : now in general setup.	Tested on x86_32.	alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.

								LTTng 0.10.0-pre17 LTTng 0.10.0-pre18 : supports 2.6.24-rc1-git13, add markers support for multiple probes. Immediate values updates.		
0.10.0-pre2	0.10.0-pre20 0.10.0-pre21 0.10.0-pre22 0.10.0-pre23 0.10.0-pre24 0.10.0-pre25 0.10.0-pre26 0.10.0-pre27 0.10.0-pre28	0.46	obsolete	Not available (syscall IDs not updated)	obsolete	1.0	2.6.24-rc2 2.6.24-rc2- git3 2.6.24-rc2- git5 2.6.24-rc3- git1	Fix multi-probes markers. LTTng 0.10.0-pre22 fixes multi-probes markers, fixes markers mutex usage at module load. LTTng 0.10.0-pre22 fixes a multi-probes pointer bug. LTTng 0.10.0-pre24, announcement to LKML.	Tested on x86_32.	alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.
0.10.0-pre3 0.10.0-pre4	0.10.0-pre30 0.10.0-pre31 0.10.0-pre32 0.10.0-pre33 0.10.0-pre34 0.10.0-pre35 0.10.0-pre36	0.46	obsolete	Not available (syscall IDs not updated)	obsolete	1.0	2.6.24-rc3- git1 2.6.24-rc3- git3	Fix process release in LTTV. Add missing APIC interrupts on x86 and x86_64. lttng pre31, pre32, pre33 fixes x86_64 instrumentation. lttng pre36 adds listing of syscalls, interrupts and softirqs. KALLSYMS is useful to get the symbol names. lttng pre36 fixes the missing ltt-test-tsc. in Kconfig options.	Tested on x86_32.	alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.
0.10.0-pre5 0.10.0-pre6 0.10.0-pre7 0.10.0-pre8 0.10.0-pre9 0.10.0-pre10 0.10.0-pre11	0.10.0-pre37 0.10.0-pre38 0.10.0-pre39 0.10.0-pre40 0.10.0-pre41 0.10.0-pre42	0.46	obsolete	Not available (syscall IDs not updated)	obsolete	1.0	2.6.24-rc4 2.6.24-rc4- git3 2.6.24-rc5- git7 2.6.24-rc8- git3	Fix process end of life (LTTng/LTTV). Fix endianness (LTTV). LTTV 0.10.0-pre6 shows system call and softirq names. (depends on KALLSYMS) LTTV 0.10.0-pre7 fixes large irq tables. LTTng 0.10 pre10 fixes a segfault in state.c	Tested on x86_32. Tested on x86_64.	alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.
0.10.0-pre5 0.10.0-pre6 0.10.0-pre7 0.10.0-pre8	0.10.0-pre43	0.47	obsolete	Not available (syscall IDs not updated)	obsolete	1.0	2.6.24	LTTng 0.10.0-pre43 supports kernel 2.6.24		x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa.

0.10.0-pre9 0.10.0-pre10 0.10.0-pre11									arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.
0.10.0-pre5 0.10.0-pre6 0.10.0-pre7 0.10.0-pre8 0.10.0-pre9 0.10.0-pre10 0.10.0-pre11 0.10.0-pre12 0.10.0-pre13	0.10.0-pre44 0.10.0-pre45 0.10.0-pre47 0.10.0-pre49 0.10.0-pre50 0.10.0-pre51 0.10.0-pre52 0.10.0-pre53 0.10.0-pre54 0.10.0-pre56	0.48	obsolete	Not available (syscall IDs not updated)	obsolete	1.0	2.6.25-rc3 2.6.25-rc6-git8 2.6.25-rc7-git6 2.6.25-rc9-git1 2.6.25 2.6.25.4 2.6.26-rc8 2.6.26-rc9	LTTng 0.10.0-pre44 supports kernel 2.6.25-rc3 and includes various fixes. LTTng 0.10.0-pre50 fixes x86 NMIs instrumentation, includes workaround for RCU preempt for markers (in mainline rc9). LTTng 0.10.0-pre51 fixes the buffer switch in active mode. LTTng 0.10.0-pre52 adds nop/jump optimization to immediate values and fix NMI in the Linux kernel. LTTng 0.10.0-pre52 supports 2.6.25. LTTng 0.10.0-pre55 supports 2.6.25.4, includes bugfixes. LTTTV 0.10-pre12 fixes control flow view interaction with newer GTK versions. LTTTV 0.10-pre13 fixes ressource view interaction with newer GTK versions. LTTng 0.10-pre56 supports kernel 2.6.26-rc8. LTTng 0.10-pre57 starts using Tracepoints and re-introduces userspace tracing through a userspace marker infrastructure, only for x86 for now. LTTng 0.10-pre58 fixes tracepoint probe build in some configurations and x86 arch-specific irq instrumentation. LTTng 0.11 has an immediate values bugfix which could case a kernel OOPS when enabling tracepoints or markers.	x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.
0.10.0-pre5 0.10.0-pre6 0.10.0-pre7 0.10.0-pre8 0.10.0-pre9 0.10.0-pre10 0.10.0-pre11 0.10.0-	0.10 0.11 0.12 0.13 0.14 0.15	0.49 0.50	obsolete	0.5	obsolete	1.0	2.6.26 2.6.26.1	LTTng 0.10 support kernel 2.6.26 and adds markers support for userspace on x86 32 and 64 bits. ltt-control 0.49 contains sample scripts to enable all userspace markers visible in /proc at script execution. ltt-control 0.50 fixes lttid mutex usage in when multithreaded (-N x). lttv 0.10.0-pre14 fixes support of traces where CPU are hotplugged. LTTng 0.14 includes a fix to	x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc,

pre12 0.10.0- pre13 0.10.0- pre14								the LTTng buffering scheme, found by running a Spin model through Promela. LTTng 0.15 has whitespace cleanups.		sparc64.
0.10.0- pre15	0.16 0.17 0.18 0.19 0.20 0.21 0.22 0.23 0.24 0.25 0.26	0.51 0.52	obsolete	0.6	obsolete	1.0	2.6.27-rc2 2.6.27-rc3 2.6.27-rc6 2.6.27-rc7	LTTng 0.16 supports 2.6.27-rc2, including ftrace and "taps". LTTV 0.10.0-pre15 includes some fixes in state.c (uninitialized variables). lttctl 0.51 adds support for taps. markers-userspace 0.6 supports kernel 2.6.27-rc2. LTTng 0.17 uses per-cpu variables to keep ltt_nesting. LTTng 0.18 reverts the formal-verif fix patch, which was broken since LTTng 0.14. LTTng 0.19 really just fixes the "author" in the lttng git tree. LTTng 0.23 contains a build fix for psrlock on x86_32. LTTng 0.24 fixes the git tree. LTTng 0.25 fixes x86_64 compilation. LTTng 0.26 supports kernel 2.6.27-rc7. lttctl 0.52 fixes the ltt-armall script.		x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.
0.10.0- pre15	0.27 0.28 0.29 0.30 0.31 0.32 0.33 0.34 0.35 0.36	0.53	obsolete	0.6	obsolete	1.0	2.6.27-rc7 2.6.27-rc8 2.6.27-rc9	LTTng 0.27 and ltt (ltt-control) 0.53 implement a vmap-less buffering scheme using splice(). LTTng 0.28 includes a small build fix. LTTng 0.29 supports 2.6.27-rc8. LTTng 0.32 supports 2.6.27-rc9 and adds a spinlock/irqoff buffering scheme (can be used with lttctl -T relay-locked). LTTng 0.33, 0.34 : checkpatch.pl coding style fixes. 0.35, 0.36 : size_t cast.		x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.
0.11.0	0.38	0.53 0.54	obsolete	0.6	obsolete	2.0	2.6.27	LTTng 0.38 and LTTV 0.11.0 I just cleaned up the LTTng trace format heavily, got rid of the heartbeat timer (by checking for overflow at the tracing site), got rid of the "special" compact channel; it removed about 5 FIXMEs in LTTng (cpu hotplug and compact channel related). Everything is compact now : This is much more compact than the previous format, and		x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.

								<p>permits a "tracer debug mode" in menuconfig which enables the "event size" field, which helps cross-checking the size expected by the userspace tool and the size written by the kernel.</p> <p>Note that this "event size" field can eventually be enabled on a per-event or per-channel basis. This will probably be useful to encode "binary blobs"...</p> <p>I also cleanup up the subbuffer header to make is much smaller than the previous one by removing unneeded information.</p> <p>I renamed the "facilities" channel to a better name : "metadata". It contains marker descriptions.</p> <p>The trace major number is bumped to "2" and is completely incompatible with old LTTV.</p>		
0.11.1 0.11.2 0.11.3	0.39 0.40 0.41 0.42 0.43 0.44 0.45 0.46	0.53 0.54 0.55 0.56	obsolete	0.6	obsolete	2.1	2.6.27 2.6.27.2	<p>LTTng 0.39 and LTTV 0.11.1 fixes 32-64 bits subbuffer header portability. LTTng 0.41 reintroduces the formal verification fix, which solves the case where the reader thinks the uncommitted subbuffer is fully committed. Unlikely to happen, but could, with small buffers especially.</p>		<p>x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.</p>
0.11.4 0.11.5 0.11.6	0.47 0.48 0.49 0.50 0.51 0.52 0.53 0.54 0.55 0.56 0.57	0.53 0.54 0.55 0.56	obsolete	0.6	obsolete	2.2	2.6.27.4 2.6.27.6	<p>LTTng 0.47 and LTTV 0.11.4 now saves the event lost count in the subbuffer header. It's printed as a lttv warning when encountered. LTTng 0.47 also implemented specialized trap probes. LTTng 0.49 is a compile fix for userspace markers. LTTng 0.50 fixes immediate values by reverting the "jump patching" version of immediate values. They were always on, and therefore broken. It also reimplements tracepoint, marker and imv mutexes. LTTng 0.51 implements</p>		<p>x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.</p>

								<p>timestamping updates.</p> <p>LTTng 0.53 implements a timer-based generic tracing clock, also includes various fixes.</p> <p>LTTng 0.54 fixes the MIPS get_cycles_rate() and also change the freq_scale variable from a multiplier to a divisor.</p> <p>LTTng 0.57 fixes the modification done to offset and commit count counters following formal verification so the overflow is dealt with correctly. It was causing problems on 32-bits machines after 4GB worth of data going through a buffer.</p>		
0.11.4 0.11.5 0.11.6	0.58 0.59 0.60 0.61 0.62 0.63 0.64	0.58 0.59	obsolete	0.6	obsolete	2.2	2.6.27.7 2.6.27.8	<p>LTTng 0.58 implements debugfs-based trace control which will gradually replace the netlink-based control. ltt-control 0.58 expects the netlink negative error values.</p> <p>LTTng 0.59 implements an exclusive wait poll to fix thundering herd problem and help scalability.</p>		<p>x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.</p>
0.12.0 0.12.1 0.12.2 0.12.3 0.12.4 0.12.5 0.12.6 0.12.7 0.73 0.74 0.75 0.76 0.77 0.78 0.79 0.80 0.81 0.82 0.83 0.84 0.85 0.86	0.65 0.66 0.67 0.68 0.69 0.70 0.71 0.72 0.73 0.74 0.75 0.76 0.77 0.78 0.79 0.80 0.81 0.82 0.83 0.84 0.85 0.86	0.60 0.61 0.62 0.63 0.64	obsolete	0.6	obsolete	2.3	2.6.27.8 2.6.27.9 2.6.27.10 2.6.28 2.6.28.1	<p>Channels are now dynamically allocated at runtime by LTTng.</p> <p>Marker IDs are now per channel.</p> <p>ltt-control 0.61 changes the command line arguments. See updated QUICKSTART.</p> <p>LTTng 0.68 implements /mnt/debugfs/ltt/write_event for basic userspace tracing.</p> <p>LTTV 0.12.3 updates the filter for channel_name.event_name, adds channel.name= support and event.subname= support.</p> <p>LTTV 0.12.4 fixes marker support so it handles disabled channels.</p> <p>ltt-control 0.62 fixes ash newline support in ltt-armall/ltt-disarmall. This is especially useful for busybox.</p> <p>LTTV 0.12.5 fixes the syscall and softirq names in print.c.</p> <p>LTTng 0.73 supports the 2.6.28 kernel.</p> <p>LTTng 0.74 fixes network probe build and adds</p>		<p>x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.</p>

								ext4/jbd2 tracepoints. LTTng 0.75 adds ext4 tracing support. LTTng 0.76 adds ext4 and jbd2 filtering on inode/device. See /mnt/debugfs/ltt/filter/ltt-control 0.64 automatically mounts debugfs and loads ltt-trace-control.ko. LTTng 0.80 fixes regression in entry_64.S where a tbench slowdown occurred when SYSCALL AUDIT or secure computing was enabled. LTTng 0.81 fixes userspace marker ABI x86_32 build error. LTTng 0.82 fixes x86_32 entry_32.S syscall tracing missing event due to thread flag being moved to bit 9 in 2.6.28. The testb has to be changed for a testw. LTTng 0.83 fixes kerneldoc. LTTng 0.84 adds block layer tracing. LTTng 0.85 fixes function tracer support. LTTV 0.12.7 updates LTTV to the new ltctl and ltt-armall commands. LTTng 0.86 fixes entry_64.S irq off tracer support.		
0.12.8 0.12.9 0.12.10 0.12.11	0.87 0.88 0.89 0.90 0.91	0.60 0.61 0.62 0.63 0.64	obsolete	0.6	obsolete	2.3	2.6.28.2 2.6.28.3 2.6.28.4 2.6.29-rc3	LTTng 0.87 combines 4 page fault events into 2. This is a very high-speed tracing path. LTTV 0.12.8 supports LTTng 0.87. LTTng 0.88 and LTTV 0.12.9 now supports dynamic kernel instrumentation with kprobes through a simple /mnt/debugfs/ltt/kprobes/ interface. LTTng 0.89 supports kernel 2.6.29-rc3, adds tree RCU tracing. LTTng 0.91 optimizes the write to page function (data copy to the buffers). LTTV 0.12.11 fixes a nasty event id/timestamp bug for event IDs 29, 30, 31.		x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.
0.12.8 0.12.9 0.12.10 0.12.11 0.12.12 0.12.13 0.12.14 0.12.15 0.12.16 0.12.17 0.12.18	0.92 0.93 0.94 0.95 0.96 0.97 0.98 0.99 0.100 0.101 0.102	0.65 0.66 0.67 0.68 0.69 0.70 0.71 0.72 0.73 0.74 0.75	obsolete	0.6	obsolete	2.3	2.6.29-rc4 2.6.29-rc6 2.6.29-rc7 2.6.29-rc8 2.6.29 2.6.29.1 2.6.30-rc1 2.6.30-rc2 2.6.30-rc5 2.6.30 2.6.30.9	LTTng 0.92 reorders the LTTng patchset and drops the /proc/ltt interface. lttctl 0.65 updates ltt-armall to the new debugfs interface. lttv 0.12.10 fixes 32-bits host support for kprobes hash table. LTTng 0.94 adds ARM OMAP3 uniprocessor support, no power		x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel,

0.12.19	0.103					2.6.31.3	management support yet.	x86_64,
0.12.20	0.104					2.6.31.5	LTTng 0.99 fixes ARM	powerpc 405,
	0.105					2.6.31.6	syscall exit tracing.	powerpc64,
	0.106						LTTng 0.101 supports Linux	s390, sparc,
	0.108						kernel 2.6.29-rc6.	sparc64.
	0.109						LTTng 0.103 integrates a	
	0.110						patch to cache the	
	0.111						page_address lookup.	
	0.112						LTTng 0.104 includes the	
	0.113						basics for future text output	
	0.114						support (ltt-ascii is still work	
	0.115						in progress) and a lot of	
	0.116						patch folding has been done	
	0.117						in preparation for LKML	
	0.118						post. It supports kernel	
	0.119						2.6.29-rc7.	
	0.120						LTTng 0.108 boosts	
	0.121						performances. Fixes a false-	
	0.122						sharing problem, divides the	
	0.123						buffer management code into	
	0.124						slow and fast paths for better	
	0.125						locality.	
	0.126						LTTng 0.110 moves to a	
	0.127						pointer array instead of a	
	0.128						linked list to manage the	
	0.129						buffer pages.	
	0.130						LTTng 0.111 adds commit	
	0.131						count prefetch, removes rcu	
	0.132						list prefetch (slows down	
	0.133						small lists) and fixes	
	0.134						powerpc64 build.	
	0.135						LTTng 0.114 fixes LTTng	
	0.136						vmcore (crash dump) support.	
	0.137						LTTng 0.115 support kernel	
	0.138						2.6.29.	
	0.139						LTTng 0.116 adds	
	0.140						irq_next_handler event to tell	
	0.141						which irq handler is being	
	0.142						called.	
	0.143						LTTng 0.117 fixes napi dev	
	0.144						instrumentation.	
	0.145						LTTng 0.120 fixes irq	
	0.146						instrumentation bug	
	0.147						introduced in 0.118.	
	0.148						LTTng 0.121 adds	
	0.149						performance enhancements	
	0.150						(lighter atomic primitives)	
	0.151						and adds NFS support for	
	0.152						splice write (can save traces	
	0.153						to NFS mounts again).	
	0.154						LTTng 0.123 supports kernel	
	0.155						2.6.29.1.	
	0.156						LTTng 0.124 fixes a mutex	
	0.157						circular dependency in ltt-	
	0.158						trace-control.	
	0.159						LTTng 0.125 supports kernel	
	0.160						2.6.30-rc1.	
	0.161						LTTng 0.126 supports kernel	
	0.162						2.6.30-rc2.	
	0.163						LTTng 0.129 finally gets	
	0.164						merge with event tracer right.	
	0.165						LTTng 0.130 fixes issues	
	0.166						when building LTTng as	
	0.167						modules (in ltt kprobes	
	0.168						support, Kconfig for ltt-relay-	

0.169
0.170

*.c and exporting
vmlalloc_sync_all symbol
appropriately).
LTTng 0.133 fixes a irq
handler instrumentation
regression introduced in the
2.6.30-rc LTTng versions.
LTTng 0.134 includes ARM
OMAP3 trace clock fixes,
idle support, PM tracepoints
and LTTng async timer
running as deferrable timer to
save power.
LTTng 0.135 support kernel
2.6.30-rc5.
LTTV 0.12.13 fixes a bug in
resourceview trap exit
handling when occurs before
any trap entry at the
beginning of the trace.
LTTV 0.12.14 fixes irq and
softirq handling of
resourceview for exit event at
the beginning of the trace.
LTTng 0.137 moves
trace_clock generic variable
to trace_clock_var. Conflict
with ftrace.
LTTng 0.139 modifies the
memory barrier IPIs (at sub-
subber switch) to give them a
good deal of paranoia : don't
trust the Linux IPI
mechanism to ensure that
memory is presented in
program order when the
remote ipi handler runs. Use
our own smp_mb()s to ensure
memory ordering. It's a slow
path anyway, we don't care.
LTTng 0.140 supports kernel
2.6.30.
LTTV 0.12.15 contains some
fixes for the resource view.
ltt-control 0.68 fixes a
memory leak in liblttctl.
lttv 0.12.16 fixes file/quit
menu.
lttv 0.12.17 changes
g_warning for missing
events/fields for a g_info.
Activate with -v on the
command line.
LTTng 0.143 fixes LTTng
build with kernels having
module config options
disabled.
LTTng 0.144 includes a fix
for network event byte
ordering.
LTTng 0.145 fixes the flight
recorder lockless fast path.
Need to push reader there
too. Also fixes the maximum
splice() size: fixed to 1

subbuffer. Also simplified the flight recorder wrap around handling.

LTTng 0.146 adds extra read-side sub-buffer for flight recorder.

LTTng 0.147 fixes a race in the commit path for flight recorder extra sub-buffer exchange.

LTTng 0.148 correctly rounds the subbuffer size to at least one page.

LTTng 0.149 has working experimental ascii output.

LTTng 0.150 fixes UP build for cpufreq driver.

ltt-control 0.69 fixes lttid append mode.

LTTng 0.151 fixes softirq instrumentation, caused by switch to mainline TRACE_EVENT with different name assignment.

LTTng 0.152 few SH build fixes.

LTTng 0.153 updates trace clock (minor fix).

LTTng 0.156 adds a required compiler barrier to the trace-clock-32-to-64 update-side.

LTTng 0.157 fixes the trace-clock-32-to-64 mutex to spinlock transition patch.

ltt-control 0.71 fixes uClibc support.

LTTng 0.158 fixes a memory size allocation problem, triggered with large number of subbuffers in lockless mode. Also adds a per-subbuffer event counter in lockless buffers shown in dmesg when buffers are destroyed.

LTTng 0.159 fixes trace-clock for mips and x86: use spinlock in cpu hotplug notifier instead of mutex.

LTTng 0.160 supports kernel 2.6.30.9. Using CONFIG_TREE_RCU with kernel 2.6.30.x is recommended due to problems encountered with mainline PREEMPT and CLASSIC RCU.

LTTng 0.161 fixes immediate values interaction with !CONFIG_KPROBES.

LTTV 0.12.18 fixes a name table resizing problem (memory leak).

LTTV 0.12.19 fixes packaging issue with headers.

LTTng 0.162 supports Linux 2.6.31.3.
LTTng 0.163 for Linux 2.6.31.5 adds support for network synchronization markers.
ltt-control 0.72 adds support for network synchronization markers.
LTTng 0.164 relicenses most tracer C files and headers, as well as immediate values C files to dual LGPL v2.1 / GPL v2 license, and most immediate values headers to dual BSD / GPL v2 license (phase 1).
LTTng 0.165 does a large refactoring/cleanup. It relicenses all the missing LTTng files that can be shared with the UST tracer. All code imported from relay.c is now in ltt-relay-splice.c, which is kept under the GPL license (and only useful within the kernel anyway).
LTTng 0.166 fixes trace teardown error for 32-bit machines introduced in 0.165.
LTTng 0.167 supports kernel 2.6.31.6.
ltt-control 0.73 adds per-channel parameter "switch_timer" (periodic buffer flush timer interval).
LTTV 0.12.20 fixes a dependency on non-standard __SIZEOF_LONG__. Uses __WORDSIZE instead.
Should fix some 32-bit builds.
ltt-control 0.74 fixes a missing return value from liblttctl.
LTTng 0.168 makes periodic flush energy-efficient, adds cpu idle notifiers for x86_32, x86_64 to deal with periodic flush vs idle interaction (other architectures are todo). Also fix a ltt-relay-vfs file close bug, increase default vm_state channel subbuffer size, fix ltt-ascii so it compiles after cleanup (ltt-ascii is still experimental, will need to be adapted to new periodic flush scheme).
LTTng 0.169 fixes trace teardown.
LTTng 0.170 fixes kref issues (since refactoring) and

								cpu hotplug vs trace teardown issues. Simplification of trace creation/remove locking scheme. Passes test of concurrent CPU hotplug, markers arm/disarm and trace create/delete in loops for minutes.		
0.12.21	0.171 0.172	0.65 0.66 0.67 0.68 0.69 0.70 0.71 0.72 0.73 0.74 0.75	obsolete	0.6	obsolete	2.4	2.6.31.6	LTTng 0.170 and LTTV 0.12.21 fixes gcc structure trace format compatibility. LTTng 0.172 adds credits to Steven Rostedt for the extra subbuffer for reader in flight recorder mode.		x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.
0.12.22 0.12.23 0.12.24 0.12.25 0.12.26 0.12.27 0.12.28 0.12.29	0.173 0.174 0.175 0.176 0.177 0.178 0.179 0.180 0.181 0.182 0.182o 0.183 0.183o 0.184 0.185 0.185o 0.186 0.186o 0.187 0.188 0.189 0.189o 0.190	0.76 0.77 0.78 0.79	obsolete	0.6	obsolete	2.5	2.6.31.6 2.6.32-rc8 2.6.32 2.6.32.2 2.6.32.4	LTTng 0.173, ltt-control 0.76, lttv 0.12.22, trace format 2.5 add support for variable-sized subbuffer transfers. Useful for network trace streaming with a periodic timer to flush the buffers periodically: only the actual payload (aligned to the next page) will be sent. LTTng 0.174 adds missing del_timer to switch_timer, and fixes buggy mips32 trace clock. LTTng 0.175 fixes event ID compaction. Caused problems when doing a second (and more) trace session. ltt-control 0.77 integrates new more flexible ltt- armall/ltt-disarmall scripts (which can now take options). LTTng 0.176 fixes a reverse locking issue in marker disable from trace control module. lttv 0.12.23 fixes polling in the trace control plugin. LTTng 0.177 fixes mips32 and mips64 trace clock. LTTng 0.178 reverts MIPS trace clock to assume synchronized TSCs. LTTng 0.179 supports kernel 2.6.32-rc8. LTTV 0.12.24 deals with try_wake_up by appropriately setting the target thread to "wake for cpu" state.		x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686, ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.

								<p>LTTng 0.180 fixes SH trace clock definition.</p> <p>LTTng 0.181 supports kernel 2.6.32.</p> <p>ltt-control update ltt-armall script to stop using deprecated head -1.</p> <p>LTTng 0.182 fixes powerpc 440 support and adds trace clock support for PM and DVFS on ARM OMAP3.</p> <p>LTTng 0.182o is based on the omap/pm tree 2.6.32-rc8 kernel. It has the DVFS/PM trace clock support found in lttng 0.182.</p> <p>LTTV 0.12.25 changes the size of default irq table to work around a bug in state.c (experienced on a ARM trace).</p> <p>LTTng 0.185 coexists peacefully with trace event.</p> <p>LTTng 0.186 and 0.186o fixes !CPUFREQ config for arm omap3 tracing.</p> <p>LTTng 0.187 and 0.188, for kernel 2.6.32.4, fixes x86-32 apm cpu idle instrumentation.</p> <p>LTTV 0.12.26 fixes a backward position seek wrap-around (time underflow) issue with the detailed event list.</p> <p>LTTV 0.12.27, 0.12.28 temporarily disables the new network sync module, which causes a build failure on 64-bit.</p> <p>ltt-control 0.79 ensures /bin/sh (busybox) script compatibility.</p> <p>LTTV 0.12.29 re-enables the network time alignment (sync/). It's fixed now.</p> <p>LTTng 0.189: tree reorganisation in subdirectories, phase 1. Add ARM cpufreq trace clock fix.</p> <p>LTTng 0.189o: lttng-omap tree, add arm cpufreq trace clock fix.</p> <p>LTTng 0.190 fixes net-extended instrumentation alignment on 32-bit architectures.</p>		
0.12.30	0.191 0.192 0.193 0.194 0.195 0.196 0.197 0.198 0.199	0.76 0.77 0.78 0.79	obsolete	0.6	obsolete	2.6	2.6.32.4 2.6.32.9 2.6.33 2.6.33.1	LTTng 0.191, LTTV 0.12.30 coming with trace format 2.6 revert to the pre-2.4 behavior for alignment of 64-bit fields on 32-bit architectures. It aligns them on 32-bit (maximum alignment is the architecture size). This follows gcc behavior.	x86, alpha, arm26, avr32, cris, frv, h8300, m32r, m68knommu, parisc, sh, sh64, um, v850, xtensa. arm, i686,	

	0.200 0.201 0.202							<p>LTTng 0.192 fixes a crash with immediate values on x86 for kernel configured without modules. It was caused by a mainline commit which restricted the liveness of the "nop" table to init time in this configuration.</p> <p>LTTng 0.193 modifies timers to support RT kernels again, and modifies omap3 trace clock spinlock taken with irqs off, turning it into a raw spinlock (needed for RT kernel).</p> <p>LTTng 0.194 uses mod_timer_pinned with add_timer_on to support NO_HZ SMP configs. Fixes incorrect API use introduced in 0.193.</p> <p>LTTng 0.195 supports kernel 2.6.32.9.</p> <p>LTTng 0.196 fixes a declaration omission in ARM omap3 trace clock, introduced in 0.194.</p> <p>LTTng 0.197 adds compiler barriers within nesting count. Eliminates possible false-negative caused by heavy compiler optimization. This only affects self-detection of internal LTTng problems (e.g. detecting trap recursion within the tracer), so it is not crucially important to update.</p> <p>LTTng 0.198 fixes a periodic flush timer bug introduced in LTTng 0.194.</p> <p>LTTng 0.199 supports Linux kernel 2.6.33.</p> <p>LTTng 0.200 supports Linux kernel 2.6.33.1.</p> <p>LTTng 0.201 fixes omap3 trace clock build: header moved from mach/ to plat/.</p> <p>LTTng 0.202 fixes the ARM omap3 build.</p>		ia64, m68k, mips, mipsel, x86_64, powerpc 405, powerpc64, s390, sparc, sparc64.
LTTV	LTTng	LTT Control	LTTng modules	LTTng userspace tracer	Genevent	Trace Major.Minor	Kernels-architectures	Comments	Kernel Regression ok	Regression fails/not tested