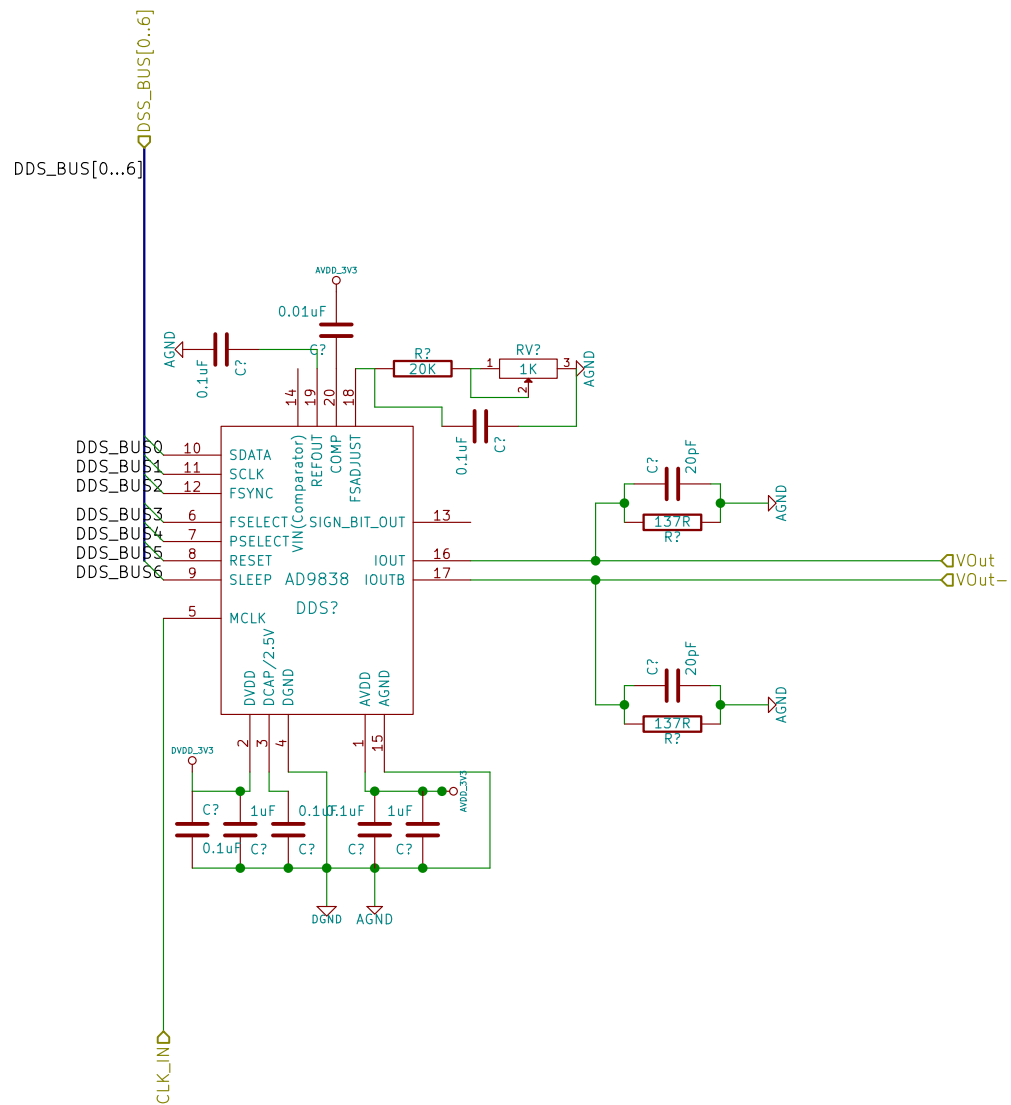
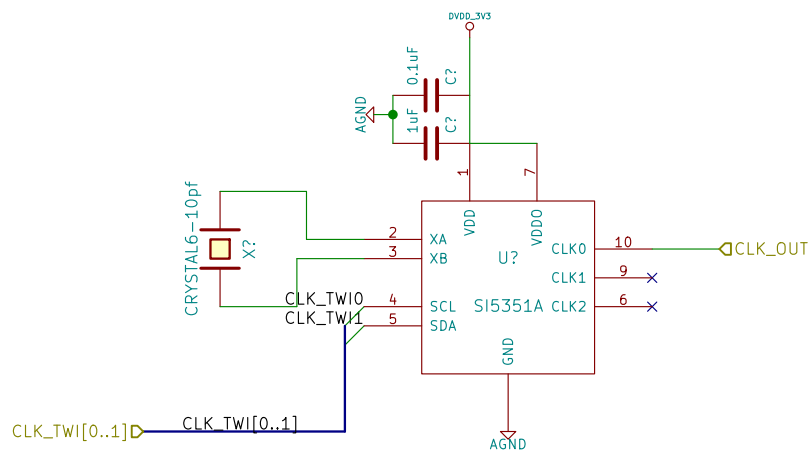


Does not need 2wire pullups,  
pulldowns needed for default  
on sleep/pselect/fselect (bit 6,7,9)  
DDS\_BUS[0..6]



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File:			
Sheet: DDS.sch			
Title: DDS Stage			
Size: A4	Date: 01 Feb 2014		Rev: A
KiCad E.D.A.	eeschema (2013-mar-13)-testing		Id: 4/7



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File:		
Sheet: clk.sch		
Title: Clock		
Size: A4	Date: 01 Feb 2014	Rev: A
KiCad E.D.A.	eeschema (2013-mar-13)-testing	Id: 5/7

