
Static Call Graph for image .\C7_SOS.axf

#<CALLGRAPH># ARM Linker, 5.03 [Build 76]: Last Updated: Tue Feb 25 12:05:16 2014

Maximum Stack Usage = 8 bytes + Unknown(Functions without stacksize, Cycles, Untraceable Function Pointers)

Call chain for Maximum Stack Depth:

__rt_entry_sh ⇒ __user_setup_stackheap

Functions with no stack information

- [Reset_Handler](#)
- [DisableInterrupts](#)
- [EnableInterrupts](#)
- [StartCritical](#)
- [EndCritical](#)
- [WaitForInterrupt](#)
- [__user_initial_stackheap](#)

Mutually Recursive functions

- [NMI_Handler](#) ⇒ [NMI_Handler](#)
- [HardFault_Handler](#) ⇒ [HardFault_Handler](#)
- [MemManage_Handler](#) ⇒ [MemManage_Handler](#)
- [BusFault_Handler](#) ⇒ [BusFault_Handler](#)
- [UsageFault_Handler](#) ⇒ [UsageFault_Handler](#)
- [SVC_Handler](#) ⇒ [SVC_Handler](#)
- [DebugMon_Handler](#) ⇒ [DebugMon_Handler](#)
- [PendSV_Handler](#) ⇒ [PendSV_Handler](#)
- [SysTick_Handler](#) ⇒ [SysTick_Handler](#)
- [ADC0Seq0_Handler](#) ⇒ [ADC0Seq0_Handler](#)

Function Pointers

- [ADC0Seq0_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [ADC0Seq1_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [ADC0Seq2_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [ADC0Seq3_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)

- [ADC1Seq0_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [ADC1Seq1_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [ADC1Seq2_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
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- [GPIOPortL_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [GPIOPortM_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [GPIOPortN_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
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- [GPIOPortP7_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [GPIOPortP_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [GPIOPortQ1_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [GPIOPortQ2_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [GPIOPortQ3_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
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- [GPIOPortQ7_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)

- [GPIOPortQ_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [GPIOPortR_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [GPIOPortS_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [HardFault_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Hibernate_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
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- [I2C3_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [I2C4_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [I2C5_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [I2S0_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [LPC0_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [MemManage_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [NMI_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
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- [PWM1Generator1_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [PWM1Generator2_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [PWM1Generator3_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
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- [Quadrature0_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Quadrature1_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Quadrature2_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Reset_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [SSI0_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [SSI1_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
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- [SSI3_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [SVC_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [SysCtl_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [SysTick_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Timer0A_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Timer0B_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Timer1A_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Timer1B_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Timer2A_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Timer2B_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Timer3A_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)

- [Timer3B_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Timer4A_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Timer4B_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Timer5A_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [Timer5B_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [UART0_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [UART1_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [UART2_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [UART3_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [UART4_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
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- [UART6_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [UART7_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [USB0_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [UsageFault_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WDT_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WideTimer0A_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WideTimer0B_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WideTimer1A_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WideTimer1B_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WideTimer2A_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WideTimer2B_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WideTimer3A_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WideTimer3B_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WideTimer4A_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WideTimer4B_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WideTimer5A_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [WideTimer5B_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)
- [uDMA_Error](#) from startup.o(RESET) referenced from startup.o(RESET)
- [uDMA_Handler](#) from startup.o(RESET) referenced from startup.o(RESET)

Global Symbols

Reset_Handler (Thumb, 0 bytes, Stack size unknown bytes, startup.o(RESET))

[Calls]

- [>>](#) __main

NMI_Handler (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))

[Calls]

- [>>](#) NMI_Handler

[Called By]

- [>>](#) NMI_Handler

[Address Reference Count : 1]

- startup.o(RESET)

HardFault_Handler (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))

[Calls]

- [>>](#) HardFault_Handler

[Called By]

- [>>](#) HardFault_Handler

[Address Reference Count : 1]

- startup.o(RESET)

MemManage_Handler (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))

[Calls]

- [>>](#) MemManage_Handler

[Called By]

- [>>](#) MemManage_Handler

[Address Reference Count : 1]

- startup.o(RESET)

BusFault_Handler (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))

[Calls]

- [>>](#) BusFault_Handler

[Called By]

- [>>](#) BusFault_Handler

[Address Reference Count : 1]

- startup.o(RESET)

UsageFault_Handler (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))

[Calls]

- [>>](#) UsageFault_Handler

[Called By]

- [>>](#) UsageFault_Handler

[Address Reference Count : 1]

- startup.o(RESET)

SVC_Handler (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))

[Calls]

- [>>](#) SVC_Handler

[Called By]

- [>>](#) SVC_Handler

[Address Reference Count : 1]

- startup.o(RESET)

DebugMon_Handler (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))

[Calls]

- [>>](#) DebugMon_Handler

[Called By]

- [>>](#) DebugMon_Handler

[Address Reference Count : 1]

- startup.o(RESET)

PendSV_Handler (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))

[Calls]

- [>>](#) PendSV_Handler

[Called By]

- [>>](#) PendSV_Handler

[Address Reference Count : 1]

- startup.o(RESET)

SysTick_Handler (Thumb, 2 bytes, Stack size 0 bytes, startup.o(RESET))

[Calls]

- [>>](#) SysTick_Handler

[Called By]

- [>>](#) SysTick_Handler

[Address Reference Count : 1]

- startup.o(RESET)

ADC0Seq0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Calls]

- [>>](#) ADC0Seq0_Handler

[Called By]

- [>>](#) ADC0Seq0_Handler

[Address Reference Count : 1]

- startup.o(RESET)

ADC0Seq1_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

ADC0Seq2_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

ADC0Seq3_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

ADC1Seq0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

ADC1Seq1_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

ADC1Seq2_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

ADC1Seq3_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

CAN0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

CAN1_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

CAN2_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Comp0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Comp1_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Comp2_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Ethernet_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

ExtBus_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

FPU_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Fan0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

FlashCtl_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortA_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortB_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortC_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortD_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortE_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortF_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortG_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortH_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortJ_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortK_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortL_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortM_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortN_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortP1_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortP2_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortP3_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortP4_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortP5_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortP6_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortP7_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortP_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortQ1_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortQ2_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortQ3_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortQ4_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortQ5_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortQ6_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortQ7_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortQ_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortR_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

GPIOPortS_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Hibernate_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

I2C0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

I2C1_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

I2C2_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

I2C3_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

I2C4_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

I2C5_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

I2S0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

LPC0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

PECI0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

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PWM0Fault_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

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PWM0Generator0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

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PWM0Generator1_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

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- startup.o(RESET)

PWM0Generator2_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

PWM0Generator3_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

PWM1Fault_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

PWM1Generator0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

PWM1Generator1_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

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- startup.o(RESET)

PWM1Generator3_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

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Quadrature0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
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Quadrature1_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
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- startup.o(RESET)

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[Address Reference Count : 1]

- startup.o(RESET)

SSI0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
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- startup.o(RESET)

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SSI2_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

SSI3_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

SysCtl_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Timer0A_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Timer0B_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Timer1A_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
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- startup.o(RESET)

Timer1B_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
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- startup.o(RESET)

Timer2A_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Timer2B_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Timer3A_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Timer3B_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Timer4A_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Timer4B_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Timer5A_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

Timer5B_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

UART0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

UART1_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

UART2_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

UART3_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

UART4_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

UART5_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

UART6_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

UART7_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

USB0_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

WDT_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

WideTimer0A_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

WideTimer0B_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

WideTimer1A_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

WideTimer1B_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

WideTimer2A_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

WideTimer2B_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

WideTimer3A_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

WideTimer3B_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

WideTimer4A_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

WideTimer4B_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))
[Address Reference Count : 1]

- startup.o(RESET)

WideTimer5A_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

WideTimer5B_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

uDMA_Error (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

uDMA_Handler (Thumb, 0 bytes, Stack size 0 bytes, startup.o(RESET))

[Address Reference Count : 1]

- startup.o(RESET)

__main (Thumb, 8 bytes, Stack size 0 bytes, __main.o(!!!main))

[Calls]

- [>>](#) __scatterload
- [>>](#) __rt_entry

[Called By]

- [>>](#) Reset_Handler

__scatterload (Thumb, 0 bytes, Stack size unknown bytes, __scatter.o(!!!scatter))

[Called By]

- [>>](#) __main

__scatterload_rt2 (Thumb, 44 bytes, Stack size unknown bytes, __scatter.o(!!!scatter), UNUSED)

[Calls]

- [>>](#) __rt_entry

__scatterload_rt2_thumb_only (Thumb, 0 bytes, Stack size unknown bytes, __scatter.o(!!!scatter), UNUSED)

__scatterload_null (Thumb, 0 bytes, Stack size unknown bytes, __scatter.o(!!!scatter), UNUSED)

__scatterload_copy (Thumb, 26 bytes, Stack size unknown bytes, __scatter_copy.o(!!handler_copy), UNUSED)

[Calls]

- [>>](#) __scatterload_copy

[Called By]

- [>>](#) __scatterload_copy

__scatterload_zeroinit (Thumb, 28 bytes, Stack size unknown bytes, __scatter_zi.o(!!handler_zi), UNUSED)

__rt_lib_init (Thumb, 0 bytes, Stack size unknown bytes, libinit.o(.ARM.Collect\$\$libinit\$\$00000000))

[Called By]

- [>>](#) __rt_entry_li

__rt_lib_init_alloca_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$0000002C))

__rt_lib_init_argv_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$0000002A))

__rt_lib_init_atexit_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$00000019))

__rt_lib_init_clock_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$0000001F))

__rt_lib_init_cpp_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$00000030))

__rt_lib_init_exceptions_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$0000002E))

__rt_lib_init_fp_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$00000002))

__rt_lib_init_fp_trap_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$0000001D))

__rt_lib_init_getenv_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$00000021))

__rt_lib_init_heap_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$00000008))

__rt_lib_init_lc_collate_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$0000000F))

__rt_lib_init_lc_ctype_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$00000011))

__rt_lib_init_lc_monetary_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$00000013))

__rt_lib_init_lc_numeric_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$00000015))

__rt_lib_init_lc_time_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$00000017))

__rt_lib_init_rand_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$0000000C))

__rt_lib_init_return (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$00000031))

__rt_lib_init_signal_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$0000001B))

__rt_lib_init_stdio_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$00000023))

__rt_lib_init_user_alloc_1 (Thumb, 0 bytes, Stack size unknown bytes, libinit2.o(.ARM.Collect\$\$libinit\$\$0000000A))

__rt_lib_shutdown (Thumb, 0 bytes, Stack size unknown bytes, libshutdown.o(.ARM.Collect\$\$libshutdown\$\$00000000))

[Called By]

- [>>](#) **__rt_exit_ls**

__rt_lib_shutdown_fp_trap_1 (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect\$\$libshutdown\$\$00000006))

__rt_lib_shutdown_heap_1 (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect\$\$libshutdown\$\$0000000E))

__rt_lib_shutdown_return (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect\$\$libshutdown\$\$0000000F))

__rt_lib_shutdown_signal_1 (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect\$\$libshutdown\$\$00000009))

__rt_lib_shutdown_stdio_1 (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect\$\$libshutdown\$\$00000003))

__rt_lib_shutdown_user_alloc_1 (Thumb, 0 bytes, Stack size unknown bytes, libshutdown2.o(.ARM.Collect\$\$libshutdown\$\$0000000B))

__rt_entry (Thumb, 0 bytes, Stack size unknown bytes, rtenry.o(.ARM.Collect\$\$rtentry\$\$00000000))

[Called By]

- [>>](#) __scatterload_rt2
- [>>](#) __main

__rt_entry_presh_1 (Thumb, 0 bytes, Stack size unknown bytes, rtenry2.o(.ARM.Collect\$\$rtentry\$\$00000002))

__rt_entry_sh (Thumb, 0 bytes, Stack size unknown bytes, rtenry4.o(.ARM.Collect\$\$rtentry\$\$00000004))

[Stack]

- Max Depth = 8 + Unknown Stack Size
- Call Chain = __rt_entry_sh ⇒ __user_setup_stackheap

[Calls]

- [>>](#) __user_setup_stackheap

__rt_entry_li (Thumb, 0 bytes, Stack size unknown bytes, rtenry2.o(.ARM.Collect\$\$rtentry\$\$0000000A))

[Calls]

- [>>](#) __rt_lib_init

__rt_entry_postsh_1 (Thumb, 0 bytes, Stack size unknown bytes, rtenry2.o(.ARM.Collect\$\$rtentry\$\$00000009))

__rt_entry_main (Thumb, 0 bytes, Stack size unknown bytes, rtenry2.o(.ARM.Collect\$\$rtentry\$\$0000000D))

[Stack]

- Max Depth = 8 + Unknown Stack Size
- Call Chain = __rt_entry_main ⇒ main ⇒ PortF_Init

[Calls]

- [>>](#) exit
- [>>](#) main

__rt_entry_postli_1 (Thumb, 0 bytes, Stack size unknown bytes, rtenry2.o(.ARM.Collect\$\$rtenry\$\$0000000C))

__rt_exit (Thumb, 0 bytes, Stack size unknown bytes, rtexit.o(.ARM.Collect\$\$rtexit\$\$00000000))

[Called By]

- [>>](#) exit

__rt_exit_ls (Thumb, 0 bytes, Stack size unknown bytes, rtexit2.o(.ARM.Collect\$\$rtexit\$\$00000003))

[Calls]

- [>>](#) __rt_lib_shutdown

__rt_exit_prels_1 (Thumb, 0 bytes, Stack size unknown bytes, rtexit2.o(.ARM.Collect\$\$rtexit\$\$00000002))

__rt_exit_exit (Thumb, 0 bytes, Stack size unknown bytes, rtexit2.o(.ARM.Collect\$\$rtexit\$\$00000004))

[Calls]

- [>>](#) _sys_exit

DisableInterrupts (Thumb, 0 bytes, Stack size unknown bytes, startup.o(.text), UNUSED)

EnableInterrupts (Thumb, 0 bytes, Stack size unknown bytes, startup.o(.text), UNUSED)

StartCritical (Thumb, 0 bytes, Stack size unknown bytes, startup.o(.text), UNUSED)

EndCritical (Thumb, 0 bytes, Stack size unknown bytes, startup.o(.text), UNUSED)

WaitForInterrupt (Thumb, 0 bytes, Stack size unknown bytes, startup.o(.text), UNUSED)

__user_initial_stackheap (Thumb, 0 bytes, Stack size unknown bytes, startup.o(.text))

[Called By]

- [>>](#) __user_setup_stackheap

__use_two_region_memory (Thumb, 2 bytes, Stack size 0 bytes, heapauxi.o(.text), UNUSED)

__rt_heap_escrow\$2region (Thumb, 2 bytes, Stack size 0 bytes, heapauxi.o(.text), UNUSED)

__rt_heap_expand\$2region (Thumb, 2 bytes, Stack size 0 bytes, heapauxi.o(.text), UNUSED)

__user_setup_stackheap (Thumb, 74 bytes, Stack size 8 bytes, sys_stackheap_outer.o(.text))

[Stack]

- Max Depth = 8 + Unknown Stack Size
- Call Chain = __user_setup_stackheap

[Calls]

- [>>](#) __user_perproc_libspace
- [>>](#) __user_initial_stackheap

[Called By]

- [>>](#) __rt_entry_sh

exit (Thumb, 12 bytes, Stack size 0 bytes, exit.o(.text))

[Calls]

- [>>](#) __rt_exit

[Called By]

- [>>](#) __rt_entry_main

__user_libspace (Thumb, 8 bytes, Stack size 0 bytes, libspace.o(.text), UNUSED)

__user_perproc_libspace (Thumb, 0 bytes, Stack size 0 bytes, libspace.o(.text))

[Called By]

- [>>](#) __user_setup_stackheap

__user_perthread_libspace (Thumb, 0 bytes, Stack size 0 bytes, libspace.o(.text), UNUSED)

_sys_exit (Thumb, 8 bytes, Stack size 0 bytes, sys_exit.o(.text))

[Called By]

- [>>](#) __rt_exit_exit

__I\$use\$semihosting (Thumb, 0 bytes, Stack size 0 bytes, use_no_semi.o(.text), UNUSED)

__use_no_semihosting_swi (Thumb, 2 bytes, Stack size 0 bytes, use_no_semi.o(.text), UNUSED)

__semihosting_library_function (Thumb, 0 bytes, Stack size unknown bytes, indicate_semi.o(.text),

UNUSED)

FlashSOS (Thumb, 406 bytes, Stack size 4 bytes, main.o(i.FlashSOS))

[Stack]

- Max Depth = 4
- Call Chain = FlashSOS

[Calls]

- [>>](#) delay

[Called By]

- [>>](#) main

PortF_Init (Thumb, 136 bytes, Stack size 8 bytes, main.o(i.PortF_Init))

[Stack]

- Max Depth = 8
- Call Chain = PortF_Init

[Called By]

- [>>](#) main

delay (Thumb, 20 bytes, Stack size 0 bytes, main.o(i.delay))

[Called By]

- [>>](#) FlashSOS

main (Thumb, 98 bytes, Stack size 0 bytes, main.o(i.main))

[Stack]

- Max Depth = 8
- Call Chain = main \Rightarrow PortF_Init

[Calls]

- [>>](#) PortF_Init
- [>>](#) FlashSOS

[Called By]

- [>>](#) __rt_entry_main

Local Symbols

Undefined Global Symbols
