PG 12864-N

OUTLINE DIMENSION & BLOCK DIAGRAM
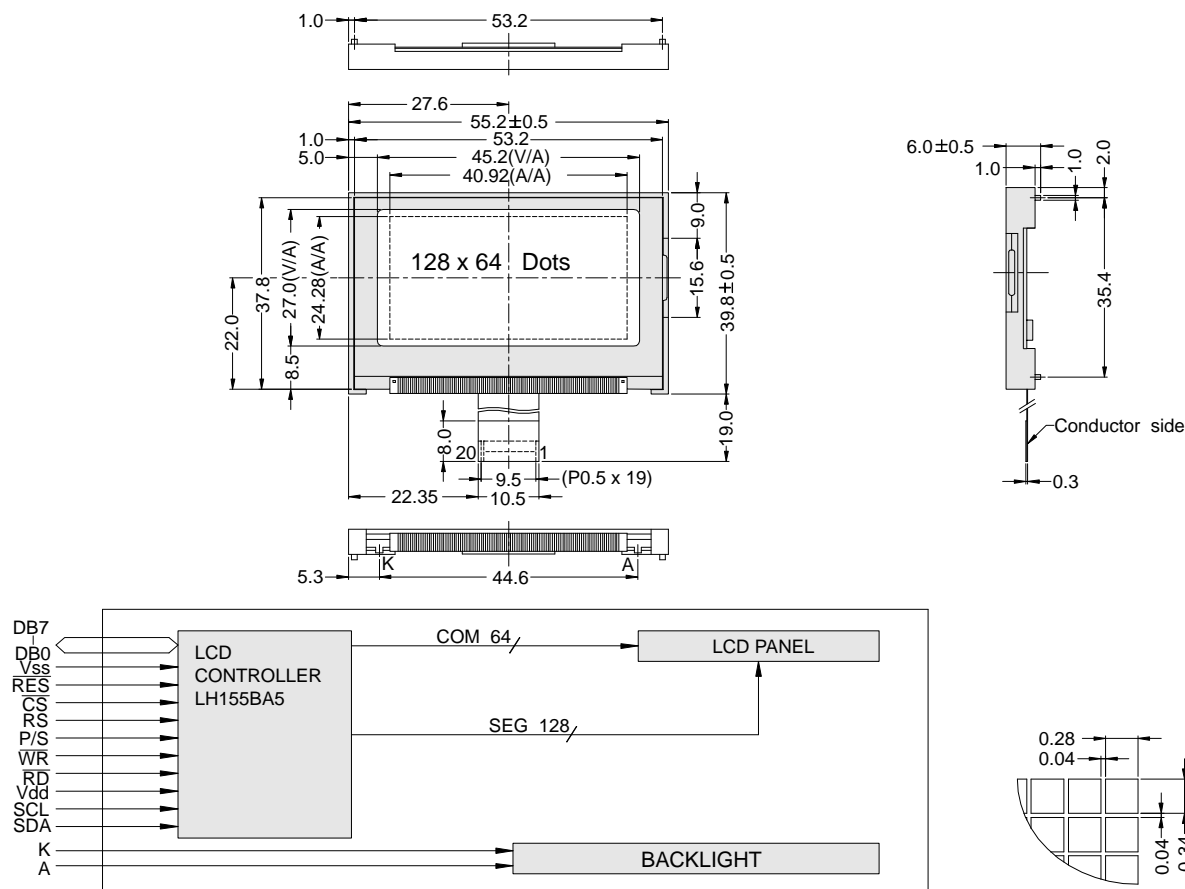
The tolerance unless classified ± 0.3mm

MECHANICAL SPECIFICATION

Overall Size	55.2 x 39.8	Module	H2 / H1
View Area	45.2 x 27.0	W /O B/L	5.0 / 6.0
Dot Size	0.28 x 0.34	EL B/L	– / –
Dot Pitch	0.23 x 0.38	LED B/L	5.0 / 6.0

PIN ASSIGNMENT

Pin no.	Symbol	Function
1	Vss	Power supply(GND)
2	$\overline{RES}$	Reset
3	$\overline{CS}$	Chip select signal
4	RS	Identify data sent by MPU at D0 to D7
5	P/S	Switch between parallel & serial interface
6	$\overline{WR}$	Write data signal
7	$\overline{RD}$	Read data signal
8-15	DB0-DB7	Data bus line
16	Vdd	Power supply(+)
17	SCL	Serial data input pin
18	SDA	Serial data transfer clock pin
19	A	Power supply for LED B/L(+)
20	K	Power supply for LED B/L(−)

ABSOLUTE MAXIMUM RATING

Item	Symbol	Condition	Min.	Max.	Units
Supply for logic voltage	Vdd-Vss	25°C	-0.3	6.0	V
LCD driving supply voltage	Vdd-Vee	25°C	-0.3	15.0	V
Input voltage	Vin	25°C	-0.3	Vdd-0.3	V

ELECTRICAL CHARACTERISTICS

Item	Symbol	Condition	Min.		Typical		Max.		Units
Power supply voltage	Vdd-Vss	25°C	1.8		–		5.5		V
		Top	N	W	N	W	N	W	V
		-20°C	–	8.6	–	9.0	–	9.5	V
LCD operation voltage	Vop	0°C	8.6	–	8.9	–	9.2	–	V
		25°C	8.1	8.6	8.4	9.0	8.7	9.5	V
		50°C	7.7	–	8.0	–	8.3	–	V
		70°C	–	8.0	–	8.4	–	8.8	V
LCM current consumption (No B/L)	Idd	Vdd=5V	–		1		3		mA
Backlight current consumption	LED/edge	VB/L=2.1V	–		40		–		mA
	LED/array	VB/L=4.2V	–		–		–		mA