Project Pinout

(Preliminary Plan)

Left Label	Pin			Pin	Right Label
GND	1			40	3.3out
VIN	2			39	5.0out
	3			38	
	4			37	
SPI1 MOSI	5			36	ETH1 RD-
SPI1 MISO	6			35	ETH1 RD+
SPI1 SCK	7			34	ETH1 TD-
	8			33	ETH1 TD+
UART3 Tx	I2C1 SDA			32	USB2 D-
UART3 Rx	I2C1 SCI			31	USB2 D+
11	SPI2 MOSI			30	CAN1 RD
12	SPI2 MISO			29	CAN1 TD
UART1 TX	SPI2 SCK			I2C2 SDA	UART2 TX
UART1 RX	14			I2C2 SCI	UART2 RX
A0	15			26	D5
A1	16			25	D4
A2	17			24	D3
A3	18			23	D2
A4	19			22	D1
A5	20			21	D0