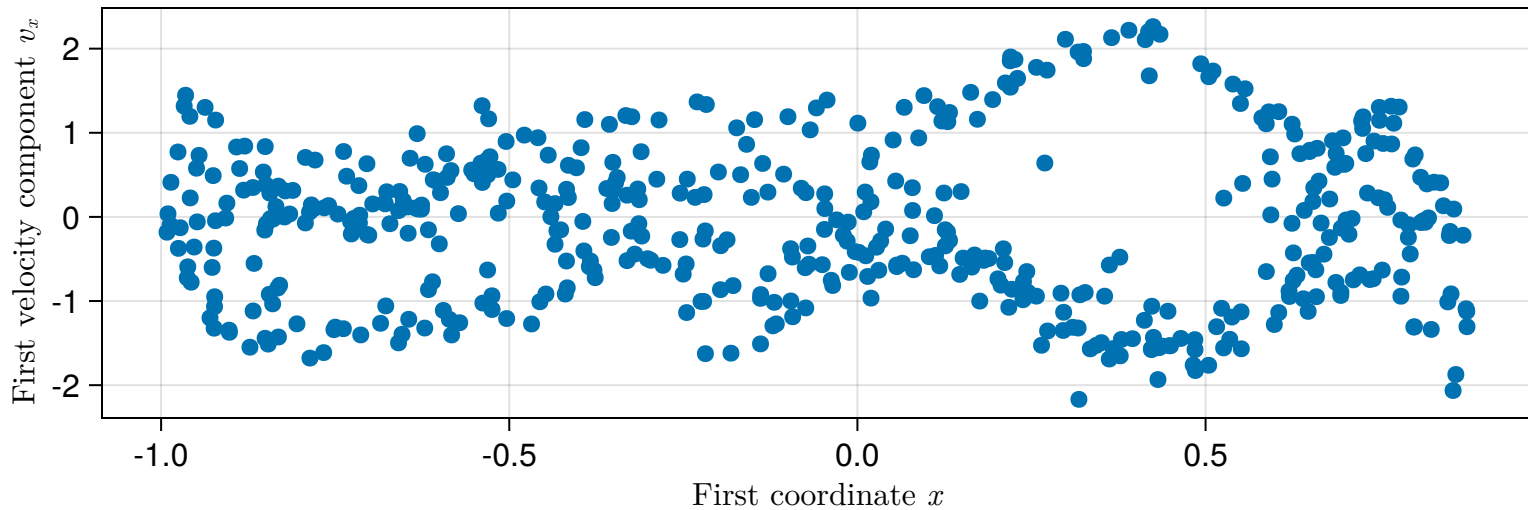


Simulation Output Phase Space ( $d = 1$ )Simulation Output Phase Space ( $d = 1$ )