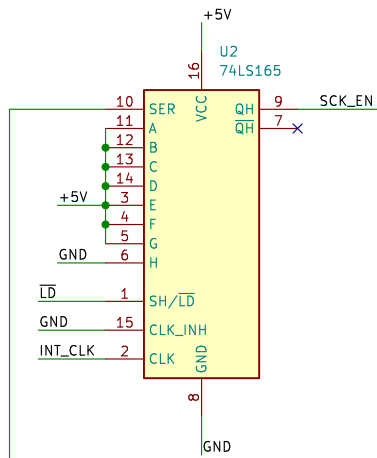
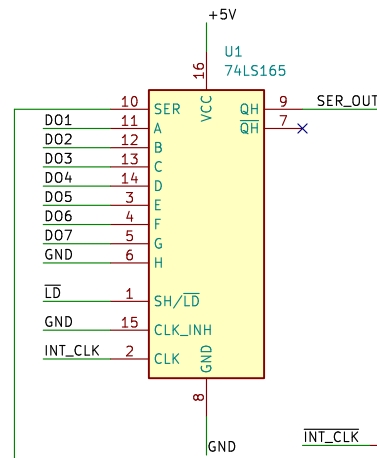


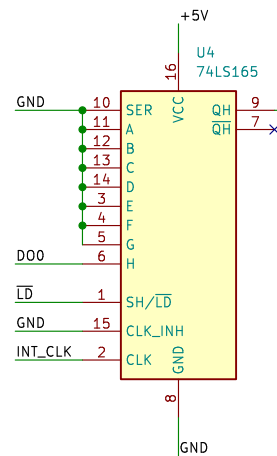
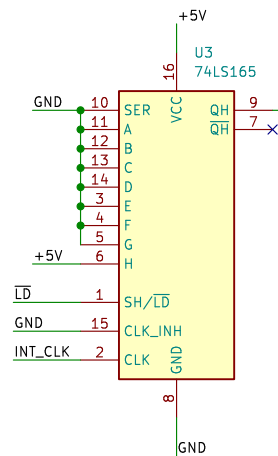
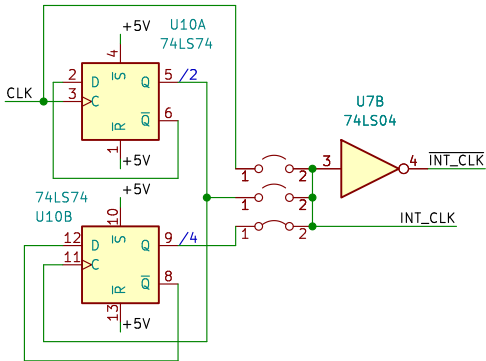
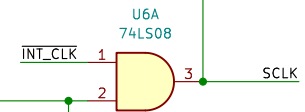
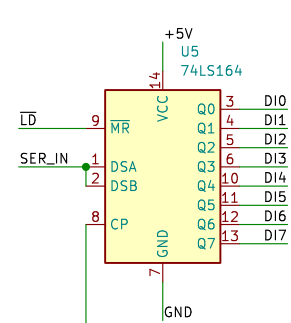
Counter



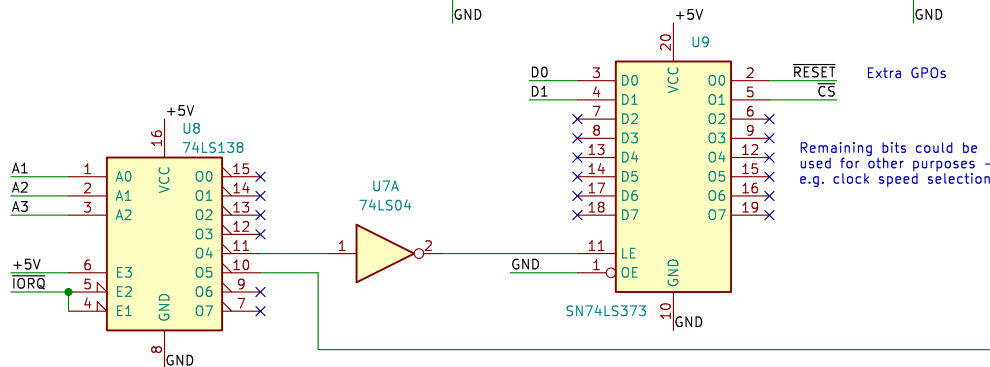
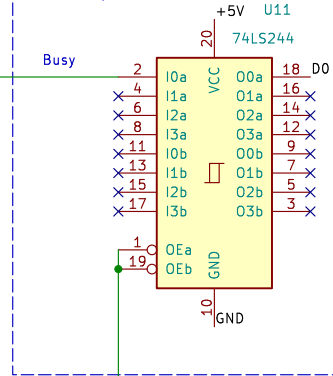
Serial Out



Serial In



Proposed "status register"  
Did not implement



Extra GPOs  
Remaining bits could be used for other purposes - e.g. clock speed selection