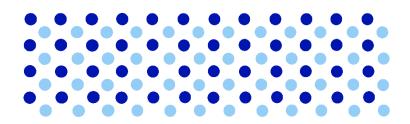


# System Block Diagrams Project FishWorks / ECET 292 / 10.13.2024

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Table 1 FishWorks PCB instruction Manual Revision History

Date Revision History	Revision				
October 13, 2024	0.1				

System Block Diagrams

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## **PROJECT DESCRIPTION**

We are looking to build an aquarium monitoring solution, allowing consumers to build a system comprised of off the shelf components that can centrally manage all aspects of their fish ecosystem. At the heart of our control system is a modular design allowing for a consumer to purchase only the components they require, while being expandable to allow for growth and new aquariums. The system is centrally controlled by a base station with onboard storage and a display for status, warnings and user controllable options. The base station will connect to other nodes which will control and monitor peripheral devices such as temperature and utilize this information to implement fail-safes such as power control. All data and control will be accessible through a web app allowing for real time monitoring, historical data, direct system control as well as setting user defined limits and alarm values.

Table 1 shows the power budget for single base station, multiple node system. 24V DC will be provided to the base station and used to power itself and all connected nodes. This budget assumes a worst-case scenario where all nodes are drawing their maximum allowed current simultaneously. The per node maximum current is estimated before final firmware has been developed and tested, and therefore is using worst case power draw numbers for the processor. Once final firmware can be tested, a lower per node power budget could likely be used, and therefore more nodes could be allowed and/or a smaller power supply could be used.



Table 2 - Full System Power Budget

Input Power:	er: 24V @ 5A(120W) max – via external AC-DC switching power supply.						
Device		Quantity(max)	Voltage	Current - Per Device	Current - Total		
Base Station	)	1	24V	185mA	185mA		
Node Controller & Attached Peripherals		20	24V	185mA	3.7A		
Actachica i empirorano				Total:	3.885A		
Notes:	Absolute maximum current values are used for all devices, actual current will likely be much lower under most circumstances.  A target max 80% utilization is used for all current ratings unless otherwise specified						

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## **NODE CONTROLLER**

The purpose of the node controller is to interface various peripheral devices (sensors, power control, etc.) with the CAN Bus network, allowing data to be sent to/received from the web app via the base station. Each node receives 24V power from the CAN Bus network. The power budget (Table 3) for the node includes both the node controller itself and any power required by the attached peripheral devices.

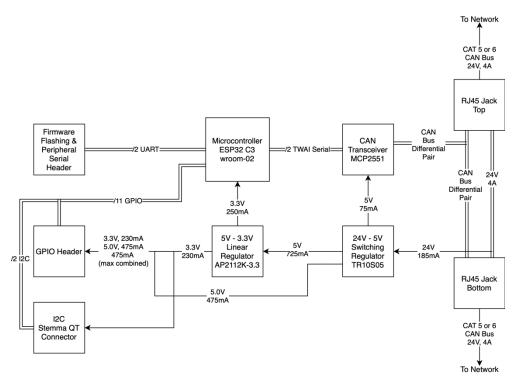


Figure 1 - Node Controller Block Diagram



Table 3 - Node Controller Pins

Block	ESP32 C3 GPIO	Protocol	Config	Description			
Stemma QT	SDA=10, SCL=5	I2C	Dedicated	Compatible various I2C			
Connector			Peripheral	connector standards			
GPIO Header	10,9,8,5,4,18 <sup>1</sup> ,19 <sup>1</sup> ,3,2,1,0,		GPIO	GPIO for connected			
	TXD <sup>2</sup> , RXD <sup>2</sup>			device. UART			
				programming pins broken			
				our if needed.			
CAN	TX=6, RX=7	TWAI	Dedicated	Connection to CAN Bus			
Transceiver			Peripheral				
UART	TXD, RXD	UART	Dedicated	For firmware flashing			
Programming			Peripheral	_			
Header							
Notes:	1 – GPIO 18 and 19 are configured for USB/JTAG by default, they must be reconfigured to						
	be used as GPIO.						
	2 – TXD, RXD must be available to be used for firmware flashing.						

Table 4- Node Controller Power Budget

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Input Power:	r: 24V @ 185mA max - from base station via CAT 5 or 6					
Device	Device Voltage Current			Source		
CAN Transceive	er	5.0V	75mA 24V – 5V switching regulat		′ – 5V switching regulator <sup>2</sup>	
Microcontrolle		3.3V	250mA		5V - 3.3V linear regulator	
GPIO - 5.0V O	ıtput	5.0V	475mA <sup>1</sup>		24V – 5V switching regulator <sup>2</sup>	
GPIO - 3.3V O	ıtput	3.3V	230mA <sup>1</sup>		5V - 3.3V linear regulator	
			Total	: 800mA @ 5.0V <sup>2</sup> 185mA @ 24V <sup>2</sup>		
A Id	1. GPIO 5.0V and GPIO 3.3V outputs must not exceed a combined 475mA. 2. Assuming a 90% average switching regulator efficiency.  Absolute maximum current values are used for all devices, actual current will likely be much lower under most circumstances.  A target max 80% utilization is used for all current ratings unless otherwise specified.					

## **BASE STATION**

The purpose of the base station is to send/receive CAN Bus message from attached nodes and send/receive these messages to the MQTT broker to allow the web app to interface with the system. The base station also provides 24V power to all connected nodes. As well the base station provides a basic user interface for the system, notifying the user of alerts from the system via a buzzer and notification LEDs. The user will be able to view details and silence these alerts using the user buttons and TFT screen.

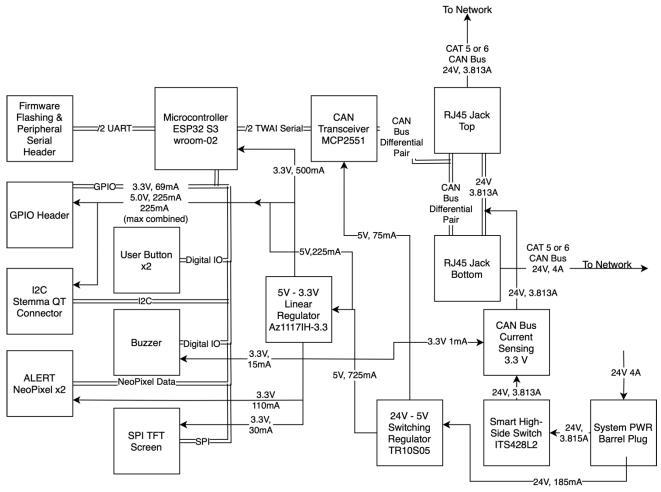


Figure 2 Base station block diagram



Table 5 - Base Station Pins

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Block	ESP32 C3 GPIO	Protocol	Config	Description		
Stemma QT	SDA=14, SCL=13	12C	Stemma	Compatible various		
Connector			QT	I2C connector		
			Connector	standards		
GPIO Header	1,2,3,8,18,19,20,21,40,41,42,		Header	GPIO for connected		
	43(TXD) <sup>2</sup> ,44(RXD) <sup>2</sup> ,45,47,48		Pins, GPIO	devices. UART		
				programming pins		
				broken out if		
				needed.		
SD Card	4(CD),5(DAT1),6(DAT0),7(CLK),		PCB	Dedicated GPIO for		
	15(CMD),16(DAT3),17(DAT2)		Routed	SD Card		
			GPIO			
CAN Power	IN=11, STS=12		PCB	Dedicated GPIO for		
Switch			Routed	CAN Bus power		
			GPIO	switching		
CAN Current	SDA=9, SCL=10	I2C	PCB	Dedicated GPIO for		
Sense			Routed I2C	CAN Bus current		
				switching		
CAN	TX=39, RX=38	TWAI	PCB	Connection to CAN		
Transceiver			Routed,	Bus		
			Dedicated			
			Peripheral			
UART	TXD(43),RXD(44)	UART	PCB	For firmware flashing		
Programming			Routed,			
Header			Dedicated			
			Peripheral			
Notes:	1 – GPIO 18 and 19 are configured for USB/JTAG by default, they must be reconfigured					
	to be used as GPIO.					
	2 – TXD, RXD must be available to be used for firmware flashing.					
	2 - 17D, NAD must be available to be used for immigate mashing.					



Table 6 - Base Station Power Budget

Input Powe	er: 24V @	24V @ 5A max – from external AC-DC power supply						
Device		Voltage	Current		Sou	Source		
NeoPixel x2	2	5.0V	110mA	2		- 5V switching regulator <sup>2</sup>		
LCD Screen	า	3.3V	30mA		5V -	- 3.3V linear regulator		
ESP32 S3		3.3V	500mA		5V -	- 3.3V linear regulator		
Buzzer		3.3V	15mA		5V -	- 3.3V linear regulator		
CAN Bus Po	ower Switch	24V	1.5mA		CAN	Bus 24V, external AC-DC power		
CAN Bus C	urrent Sense	3.3V	1mA			- 3.3V linear regulator		
CAN Transo	ceiver	5.0V	75mA		24V – 5V switching regulator <sup>2</sup>			
Microcontr	oller	3.3V	500mA		5V – 3.3V linear regulator			
Peripherals	s (including	5.0V	225mA¹		24V – 5V switching regulator <sup>2</sup>			
	s (including	3.3V	69mA¹		5V - 3.3V linear regulator			
		1	- 1	Total:	al: 800mA @ 5.0V <sup>2</sup> 185mA @ 24V <sup>2</sup>			
Notes:	2. Assuming Absolute mach low	<ol> <li>Peripherals 5.0V and 3.3V outputs must not exceed a combined 225mA.</li> <li>Assuming a 90% average switching regulator efficiency.</li> <li>Absolute maximum current values are used for all devices, actual current will likely be much lower under most circumstances.</li> <li>A target max 80% utilization is used for all current ratings unless otherwise</li> </ol>						

System Block Diagrams

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## **SOFTWARE**

#### **Software Overview**

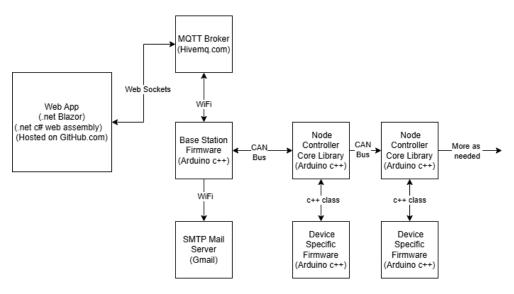


Figure 3 - Software Block Diagram

#### **Node Controller**

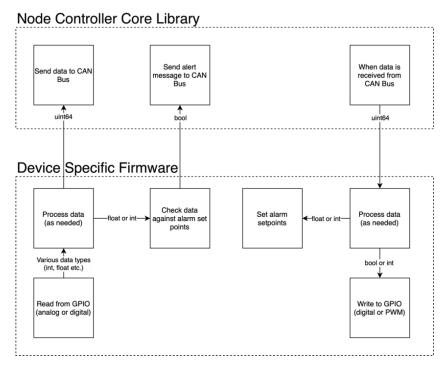


Figure 4 - Node controller software flow chart

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#### **Base Station**

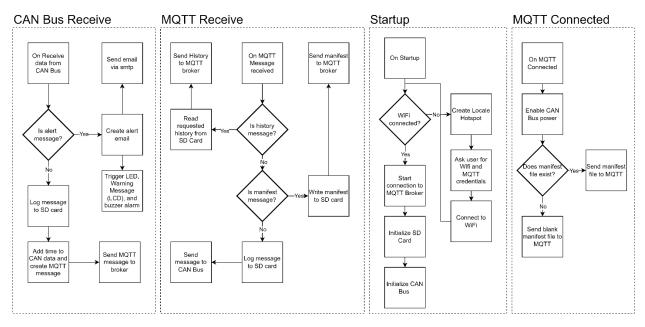
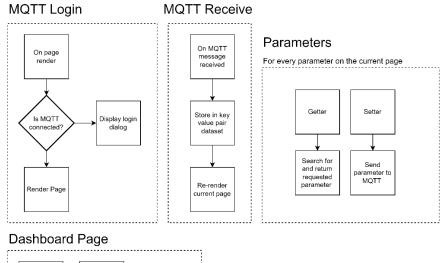


Figure 5 - Base station software flow chart

#### **Web App**



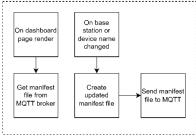


Figure 6 - Web app flow chart

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