

# Intro till assembly

Spooktober 2020



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KODSPORT  
SVERIGE

# Schema

Lördag 17 oktober

- Intro till assembly - 14:00 (Du är här)
- Intro till reverse engineering - 15:00
- Intro till pwn - 16:00

Söndag 18 oktober

- Mini-CTF - 10:00-18:00 - [ctf.sakerhetssm.se](https://ctf.sakerhetssm.se)



Den här föreläsningen antar att du kan  
grundläggande programmering



# Vad är assembly?

- De instruktioner som körs av din CPU. Världigt små, enkla instruktioner.

```
for(int i = 0; i < 10; i++)  
{  
    printf("%d", i);  
}
```



Kompilering

## Assembly:

```
401142: mov     DWORD PTR [rbp-0x4],0x0  
401149: jmp     401165  
40114b: mov     eax,DWORD PTR [rbp-0x4]  
40114e: mov     esi,eax  
401150: lea     rdi,[rip+0xead]  
401157: mov     eax,0x0  
40115c: call    401040 <printf@plt>  
401161: add     DWORD PTR [rbp-0x4],0x1  
401165: cmp     DWORD PTR [rbp-0x4],0x9  
401169: jle     40114b
```

=

## Maskinkod:

```
11000111010001011111110000000  
00000000000000000000000000011  
10101100011010100010110100010  
11111110010001001110001100100  
10001000110100111101101011010  
000111000000000000000000101110  
00000000000000000000000000000  
0111010001101111111111101111  
11111111111110000011010001011  
1111100000000011000001101...
```



# Blir alla program till assembly innan de “körs”?

- Python
  - Interpreterat språk
  - NEJ
- Java
  - Kompilerar till JVM bytekod som sedan körs på JVM
  - NEJ
- C, C++, Rust, Go
  - Kompilerar direkt till maskinkod
  - JA
- JVM skriven i C++, Python skriven i C => Allt blir maskinkod till slut!



# Förkunskaper



# Talbaser

- Matte 1b och 1c i gymnasiet

$$1101_2 = 1 * 2^3 + 1 * 2^2 + 0 * 2^1 + 1 * 2^0 = 13$$

Bas	Namn	Tecken	Exempel
2	Binärt	0, 1	10100111001
8	Oktalt	0-7	2471
10	Decimalt	0-9	1337
16	Hexadecimalt / Hex	0-9, a, b, c, d, e, f	539



# Talbaser

Vanliga notationer:

- Binärt - `0b101000101`
- Oktalt - `0o6256`, `06256`
- Hex - `0xea67fcd8`, `ea67fcd8h`

Lätt att konvertera mellan binärt och hex:

- `0b1111 = 0xf`





# Minne

- Bit: 0 eller 1
- Byte: den minsta enheten som lagras i minne
  - 0 - 255
  - 0b00000000 - 0b11111111
  - 0x00 - 0xff

Minne är en stor array / lista av bytes

- array[index]
- memory[address]



# Minne - Endian

Hur sparas tal större än 255?

- Lägg bytes på rad!
- 2172726 => 0x212736 => 0x21, 0x27, 0x36 => ...?

Big-Endian: stora änden först

- memory = [0x21, 0x27, 0x36] = [21 27 36]

Little-Endian: lilla änden först

- memory = [0x36, 0x27, 0x21] = [36 27 21]



# Minne - text och flyttal

Hur spara text som bytes?

- Använd en stor tabell!
- ASCII
  - “hej” => 104, 101, 106 => [68 65 6a]
- Unicode och UTF-8
  - “hej 🖥️” => [68 65 6a 20 f0 9f 96 a5]

Byte-värde	Tecken
...	...
65	A
66	B
67	C
...	...

Flyttal: tal med decimaler

- 3.14 =>

0x4048f5c3																							
4				0				4				8				f				5			
0	1	0	0	0	0	0	0	0	1	0	0	1	0	0	0	1	1	1	1	0	1	0	1
0	10000000							10010001111010111000011															
sign		exponent						mantissa															



# Minne - Maskinkod

Vi kan ju självklart också spara maskinkod som bytes

## Assembly:

```
401142: mov     DWORD PTR [rbp-0x4],0x0
401149: jmp     401165
40114b: mov     eax,DWORD PTR [rbp-0x4]
40114e: mov     esi,eax
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401169: jle     40114b
```

=

## Maskinkod:

```
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00000000000000000000000000011
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11111110010001001110001100100
10001000110100111101101011010
000111000000000000000000101110
00000000000000000000000000000
01110100011011111111111101111
11111111111110000011010001011
1111100000000011000001101...
```



# Grundläggande assembly



# Nu assembly!

Men vilken?

Beror på CPU:

- x86, ARM, MIPS, AVR, SPARC ...
- 16 bitar, 32 bitar, 64 bitar
- Little-Endian eller Big-Endian

Vi kommer att kolla på Little-Endian 64-bitars x86. (x86-64).



# Syntax

Intel	AT&T
401142: mov    DWORD PTR [rbp-0x4],0x0	401142: movl    \$0x0,-0x4(%rbp)
401149: jmp    401165	401149: jmp    401165
40114b: mov    eax,DWORD PTR [rbp-0x4]	40114b: mov    -0x4(%rbp),%eax
40114e: mov    esi,eax	40114e: mov    %eax,%esi
401150: lea    rdi,[rip+0xead]	401150: lea    0xead(%rip),%rdi
401157: mov    eax,0x0	401157: mov    \$0x0,%eax
40115c: call   401040 <printf@plt>	40115c: callq   401040 <printf@plt>
401161: add    DWORD PTR [rbp-0x4],0x1	401161: addl    \$0x1,-0x4(%rbp)
401165: cmp    DWORD PTR [rbp-0x4],0x9	401165: cmpl    \$0x9,-0x4(%rbp)
401169: jle    40114b	401169: jle    40114b

Vi kommer kolla på Intel-syntax



# Exempel

C	x86
<pre>int a = 5; int b = 7; b += b; b -= a; int c = b; c ^= a;</pre>	<pre>mov rax, 5 mov rbx, 7 add rbx, rbx sub rbx, rax mov rcx, rbx xor rcx, rax</pre>

- Variabler kallas register
- Hur vet CPU:n var den är?
- Ser bara: b80500000bb070000004801db4829c34889d94831c1





# Exempel

C	x86
1. <code>int a = 5;</code>	401140: b8 05 00 00 00 mov rax, 5
2. <code>int b = 7;</code>	401145: bb 07 00 00 00 mov rbx, 7
3. <code>b += b;</code>	40114a: 48 01 db add rbx, rbx
4. <code>b -= a;</code>	40114d: 48 29 c3 sub rbx, rax
5. <code>int c = b;</code>	401150: 48 89 d9 mov rcx, rbx
6. <code>c ^= a;</code>	401153: 48 31 c1 xor rcx, rax

- Variabler kallas register
- Hur vet CPU:n var den är?
- Ser bara: b80500000bb070000004801db4829c34889d94831c1
- Instruktionspekaren: rip



# Exempel

C	x86
1. <code>int a = 5;</code>	401140: b8 05 00 00 00 <code>mov eax, 5</code>
2. <code>int b = 7;</code>	401145: bb 07 00 00 00 <code>mov ebx, 7</code>
3. <code>b += b;</code>	40114a: 48 01 db <code>add rbx, rbx</code>
4. <code>b -= a;</code>	40114d: 48 29 c3 <code>sub rbx, rax</code>
5. <code>int c = b;</code>	401150: 48 89 d9 <code>mov rcx, rbx</code>
6. <code>c ^= a;</code>	401153: 48 31 c1 <code>xor rcx, rax</code>

- Variabler kallas register
- Hur vet CPU:n var den är?
- Ser bara: b80500000bb070000004801db4829c34889d94831c1
- Instruktionspekaren: rip



# Register

Globala variabler i CPU:n

64 bitar
rax
rbx
rcx
rdx



# Register

Globala variabler i CPU:n

64 bitar	Lägsta 32 bitarna
rax	eax
rbx	ebx
rcx	ecx
rdx	edx



# Register

Globala variabler i CPU:n

64 bitar	Lägsta 32 bitarna	Lägsta 16 bitarna
rax	eax	ax
rbx	ebx	bx
rcx	ecx	cx
rdx	edx	dx



# Register

Globala variabler i CPU:n

64 bitar	Lägsta 32 bitarna	Lägsta 16 bitarna	Lägsta 8 bitarna
rax	eax	ax	al
rbx	ebx	bx	bl
rcx	ecx	cx	cl
rdx	edx	dx	dl



# Register

## Globala variabler i CPU:n

64 bitar	Lägsta 32 bitarna	Lägsta 16 bitarna	Lägsta 8 bitarna
rax	eax	ax	al
rbx	ebx	bx	bl
rcx	ecx	cx	cl
rdx	edx	dx	dl

### Alla register

Några extra viktiga: rip, rsp (stackpekaren), rbp (baspekaren)



# Några instruktioner

- `jmp 401165`
  - Samma som: `mov rip, 401165`
- `cmp rax, rbx`  
`je 401165`
  - `if(rax == rbx) { rip = 401165; }`
- `cmp rax, rbx`  
`jle 401165`
  - `if(rax < rbx) { rip = 401165; }`

Finns många olika jump: `je`, `jne`, `jb`, `jnb`, `ja`, `jg`, `jge`, `jle`, ...





# Några instruktioner

- `mov rax, rbx`
- `mov rax, [rbx]`
  - `rax = *rbx;`
- `mov rax, [rbx + 8]`
  - `rax = *(rbx + 8);`
- `lea rax, [rbx + 8]`
  - Load effective address
  - `rax = rbx + 8;`
- `mov [rax], rbx`
  - `*rax = rbx;`



# Några instruktioner

- `mov QWORD PTR [rax], rbx`
- `mov rax, QWORD PTR [rbx]`

Instruktion	Storlek
BYTE PTR	8 bit / 1 byte
WORD PTR	16 bitar / 2 bytes
DWORD PTR	32 bitar / 4 bytes
QWORD PTR	64 bitar / 8 bytes



# Funktioner och stacken



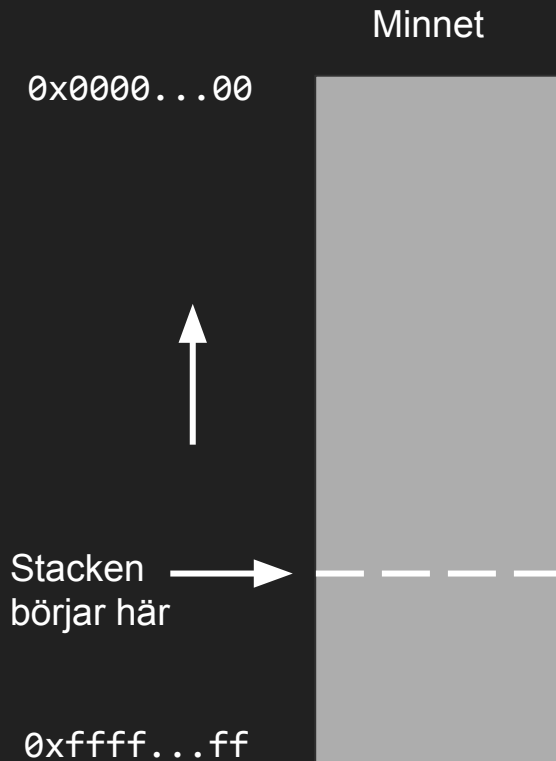
# En stack

- En hög med saker
- Två operationer
  - Push
  - Pop
- Hur använder funktioner stacken?
  - Spara lokala variabler och parametrar
  - Spara returadress och baspekare
  - Push vid anrop, pop vid retur
  - Stack frame



# Exempel

```
int add(int a, int b) {  
    return a + b;  
}  
  
void print(int a, int b) {  
    int r = add(a, b);  
    printf("%d\n", r);  
}  
  
int main() {  
    int x = 5;  
    print(x, 6);  
}
```



# Exempel

```
int add(int a, int b) {  
    return a + b;  
}
```

```
void print(int a, int b) {  
    int r = add(a, b);  
    printf("%d\n", r);  
}
```

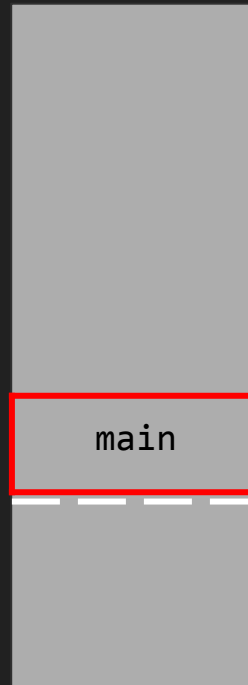
→ 

```
int main() {  
    int x = 5;  
    print(x, 6);  
}
```

0x0000...00



Minnet



0xffff...ff



# Exempel

```
int add(int a, int b) {  
    return a + b;  
}  
  
void print(int a, int b) {  
    int r = add(a, b);  
    printf("%d\n", r);  
}  
  
int main() {  
    int x = 5;  
    print(x, 6);  
}
```



0x0000...00



0xffff...ff

Minnet



# Exempel

```
int add(int a, int b) {  
    return a + b;  
}
```

→ 

```
void print(int a, int b) {  
    int r = add(a, b);  
    printf("%d\n", r);  
}
```

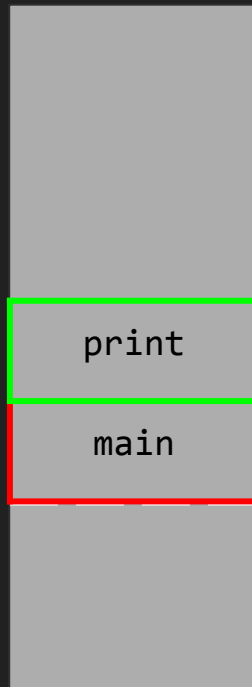
```
int main() {  
    int x = 5;  
    print(x, 6);  
}
```

0x0000...00



0xffff...ff

Minnet





# Exempel

```
int add(int a, int b) {  
    return a + b;  
}
```



```
void print(int a, int b) {  
    int r = add(a, b);  
    printf("%d\n", r);  
}
```

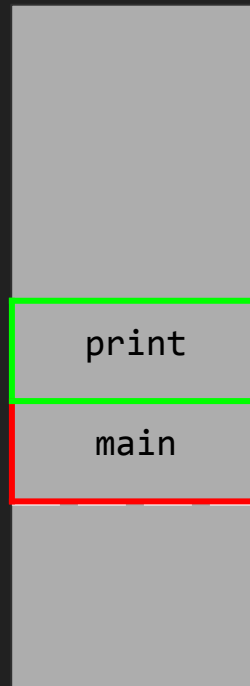
```
int main() {  
    int x = 5;  
    print(x, 6);  
}
```

0x0000...00



0xffff...ff

Minnet



# Exempel

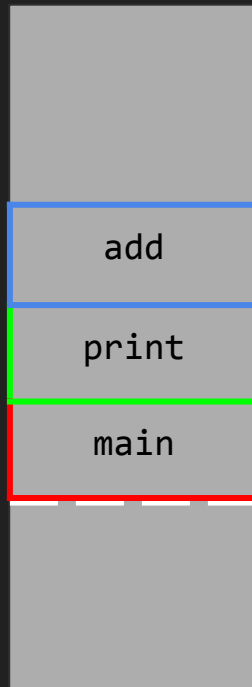
```
→ int add(int a, int b) {  
    return a + b;  
}  
  
void print(int a, int b) {  
    int r = add(a, b);  
    printf("%d\n", r);  
}  
  
int main() {  
    int x = 5;  
    print(x, 6);  
}
```

0x0000...00



0xffff...ff

Minnet



# Exempel

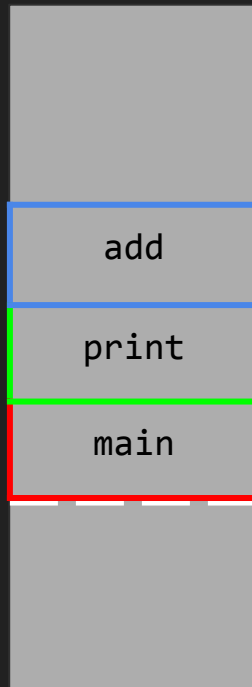
```
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    printf("%d\n", r);  
}  
  
int main() {  
    int x = 5;  
    print(x, 6);  
}
```

0x0000...00



0xffff...ff

Minnet



# Exempel

```
int add(int a, int b) {  
    return a + b;  
}
```

→

```
void print(int a, int b) {  
    int r = add(a, b);  
    printf("%d\n", r);  
}
```

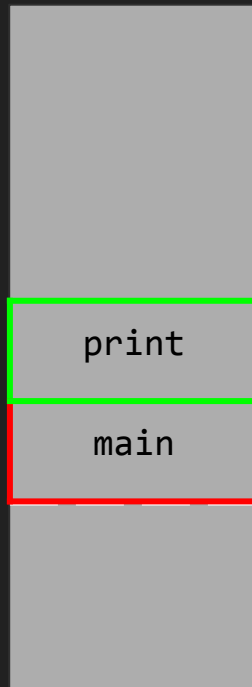
```
int main() {  
    int x = 5;  
    print(x, 6);  
}
```

0x0000...00



0xffff...ff

Minnet



# Exempel

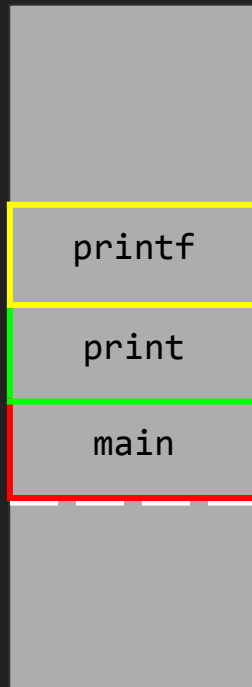
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}  
  
void print(int a, int b) {  
    int r = add(a, b);  
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}
```

0x0000...00



0xffff...ff

Minnet



# Exempel

```
int add(int a, int b) {  
    return a + b;  
}
```

→

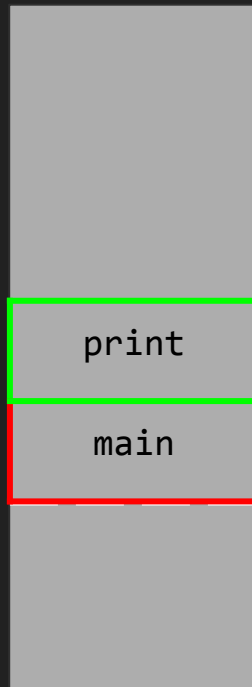
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void print(int a, int b) {  
    int r = add(a, b);  
    printf("%d\n", r);  
}
```

```
int main() {  
    int x = 5;  
    print(x, 6);  
}
```

0x0000...00



Minnet



0xffff...ff



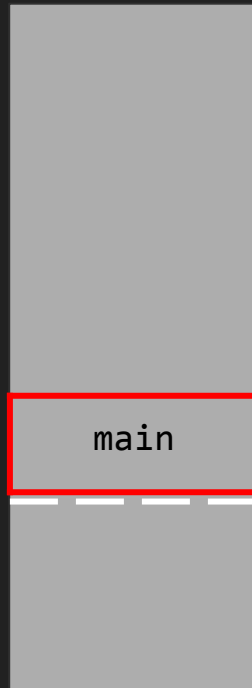
# Exempel

```
int add(int a, int b) {  
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}  
  
void print(int a, int b) {  
    int r = add(a, b);  
    printf("%d\n", r);  
}  
  
int main() {  
    int x = 5;  
    print(x, 6);  
→ }
```

0x0000...00



Minnet



0xffff...ff



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```
int add(int a, int b) {  
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    int x = 5;  
    print(x, 6);  
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0x0000...00



0xffff...ff

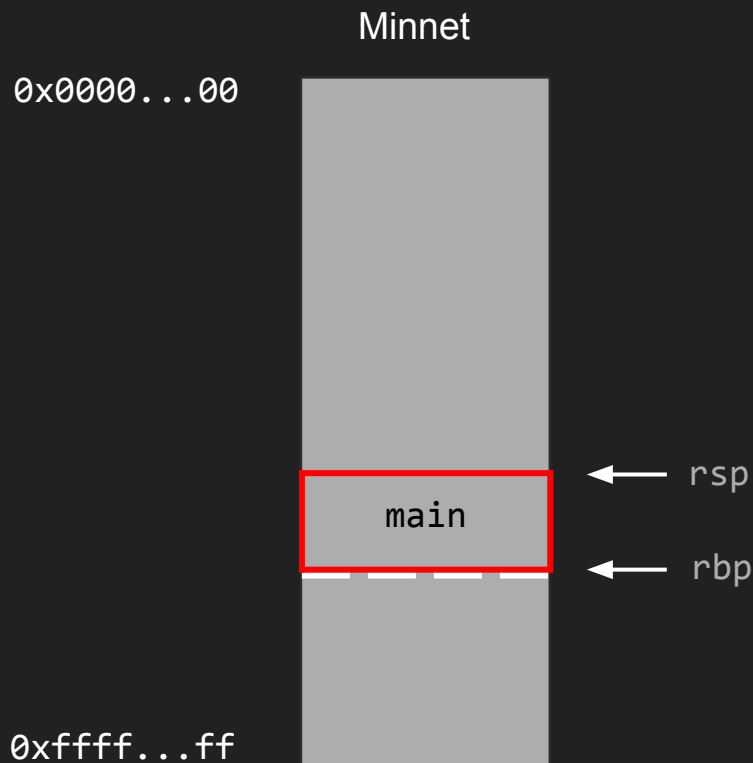
Minnet





# Stack- och baspekaren

- `rsp`
  - Pekar på toppen av stacken
- `rbp`
  - Pekar på botten av översta stack framen

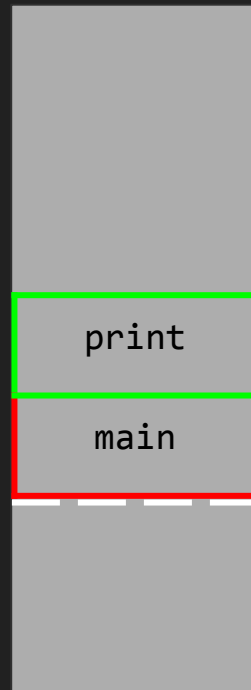


# Stack- och baspekaren

- `rsp`
  - Pekar på toppen av stacken
- `rbp`
  - Pekar på botten av översta stack framen

0x0000...00

Minnet



← `rsp`

← `rbp`

0xffff...ff



# Stackrelaterade instruktioner

- `push rax`
  - `sub rsp, 8`  
`mov [rsp], rax`
- `pop rax`
  - `mov rax, [rsp]`  
`add rsp, 8`
- `call 401040`
  - `push [address till nästa instruktion]`  
`jmp 401040`
- `ret`
  - `pop rip`



# Calling conventions

- Hur händer ett anrop i detalj?
- Beror på [calling convention](#)
- Vi kollar på Linux, GCC, 64 bit, calling convention:
  - Argument skickas via: `rdi`, `rsi`, `rdx`, `rcx`, `r8`, `r9`
  - Om fler, lägg på stacken
  - ...
- OBS: vid 32 bitar skickas alla argument på stacken



# Ett anrop i detalj

main:

...

→ 40119e: mov eax,DWORD PTR [rbp-0x4]  
4011a1: mov esi,0x6  
4011a6: mov edi,eax  
4011a8: call 40114e <print>  
4011ad: ...

print:

40114e: endbr64  
401152: push rbp  
401153: mov rbp,rsp  
401156: sub rsp,0x20  
40115a: mov DWORD PTR [rbp-0x14],edi  
40115d: mov DWORD PTR [rbp-0x18],esi  
...  
401189: leave  
40118a: ret

0x0000...00

Minnet



← rsp

← rbp

0xffff...ff



# Ett anrop i detalj

main:

...

40119e: mov eax,DWORD PTR [rbp-0x4]

→ 4011a1: mov esi,0x6

4011a6: mov edi,eax

4011a8: call 40114e <print>

4011ad: ...

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0x0000...00

Minnet



0xffff...ff



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main:

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40119e: mov eax,DWORD PTR [rbp-0x4]

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→ 4011a6: mov edi,eax

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...

401189: leave

40118a: ret

0x0000...00

Minnet



← rsp

← rbp

0xffff...ff



# Ett anrop i detalj

main:

...

40119e: mov eax,DWORD PTR [rbp-0x4]

4011a1: mov esi,0x6

4011a6: mov edi,eax

→ 4011a8: call 40114e <print>

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...

401189: leave

40118a: ret

0x0000...00

Minnet



0xffff...ff





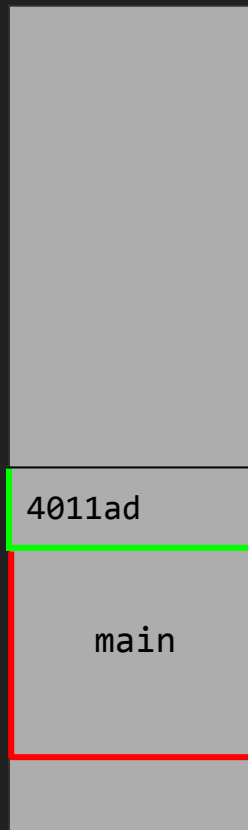
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...
401189: leave
40118a: ret
```

0x0000...00

Minnet



← rsp

← rbp

0xffff...ff



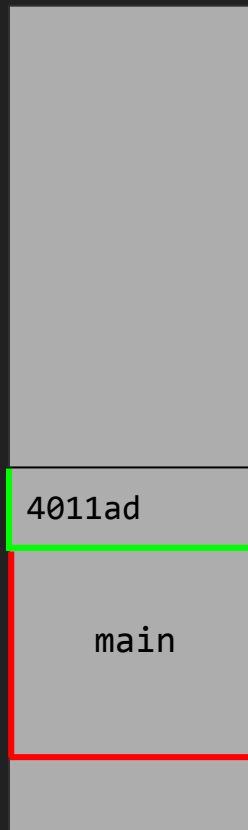
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print:
40114e: endbr64
→ 401152: push    rbp
401153: mov     rbp,rsp
401156: sub     rsp,0x20
40115a: mov     DWORD PTR [rbp-0x14],edi
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0x0000...00

Minnet



← rsp

← rbp

0xffff...ff



# Ett anrop i detalj

```
main:
...
40119e: mov     eax,DWORD PTR [rbp-0x4]
4011a1: mov     esi,0x6
4011a6: mov     edi,eax
4011a8: call    40114e <print>
4011ad: ...

print:
40114e: endbr64
401152: push    rbp
→ 401153: mov     rbp,rsp
401156: sub     rsp,0x20
40115a: mov     DWORD PTR [rbp-0x14],edi
40115d: mov     DWORD PTR [rbp-0x18],esi
...
401189: leave
40118a: ret
```

0x0000...00

Minnet

sparad rbp

4011ad

main

0xffff...ff

← rsp

← rbp



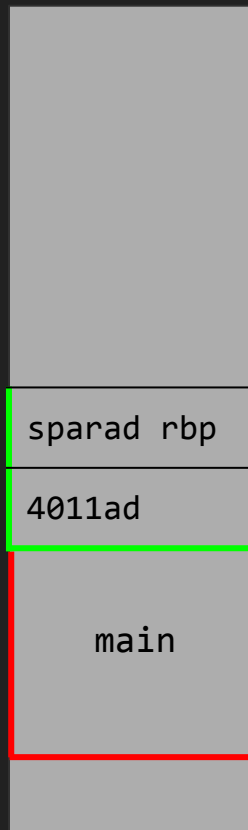
# Ett anrop i detalj

```
main:
...
40119e: mov     eax,DWORD PTR [rbp-0x4]
4011a1: mov     esi,0x6
4011a6: mov     edi,eax
4011a8: call    40114e <print>
4011ad: ...

print:
40114e: endbr64
401152: push    rbp
401153: mov     rbp,rsp
→ 401156: sub     rsp,0x20
40115a: mov     DWORD PTR [rbp-0x14],edi
40115d: mov     DWORD PTR [rbp-0x18],esi
...
401189: leave
40118a: ret
```

0x0000...00

Minnet



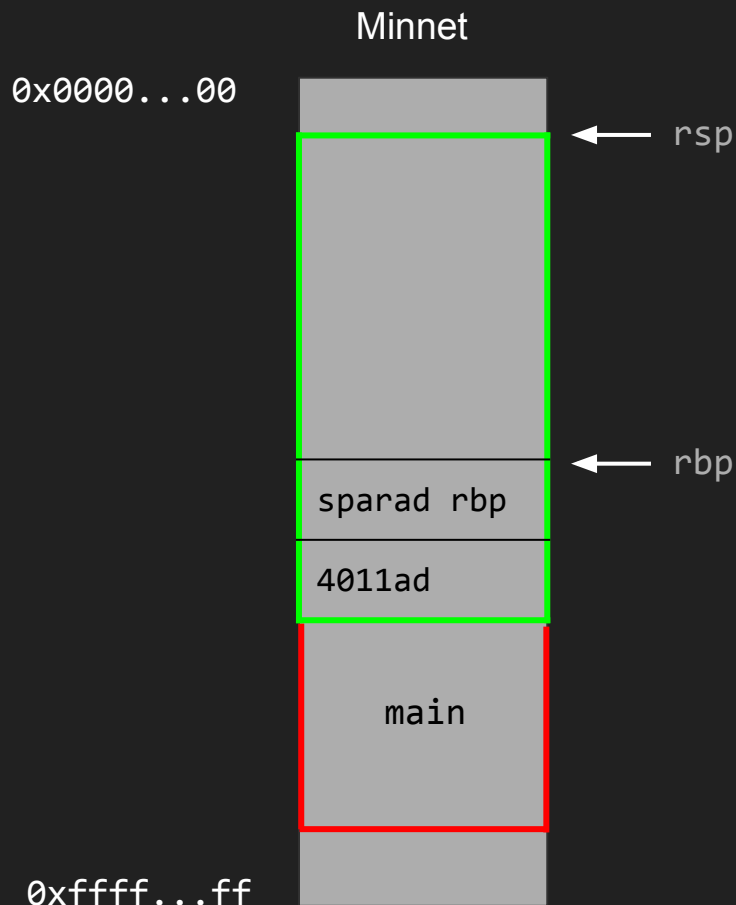
← rsp, rbp



# Ett anrop i detalj

```
main:
...
40119e: mov     eax,DWORD PTR [rbp-0x4]
4011a1: mov     esi,0x6
4011a6: mov     edi,eax
4011a8: call    40114e <print>
4011ad: ...

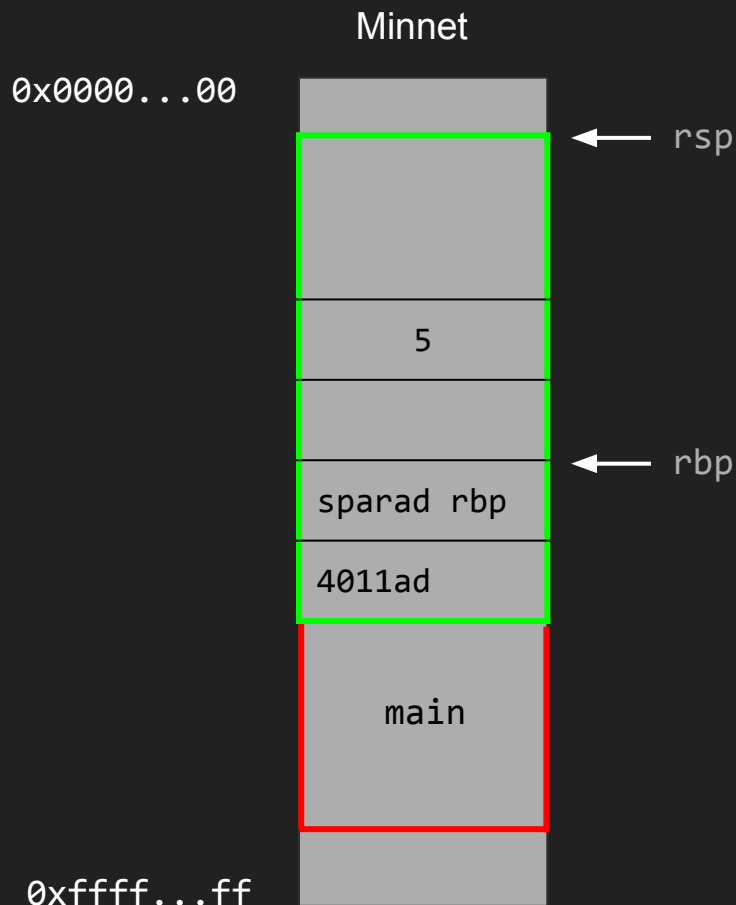
print:
40114e: endbr64
401152: push    rbp
401153: mov     rbp,rsp
401156: sub     rsp,0x20
→ 40115a: mov     DWORD PTR [rbp-0x14],edi
40115d: mov     DWORD PTR [rbp-0x18],esi
...
401189: leave
40118a: ret
```



# Ett anrop i detalj

```
main:
...
40119e: mov     eax,DWORD PTR [rbp-0x4]
4011a1: mov     esi,0x6
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...
401189: leave
40118a: ret
```



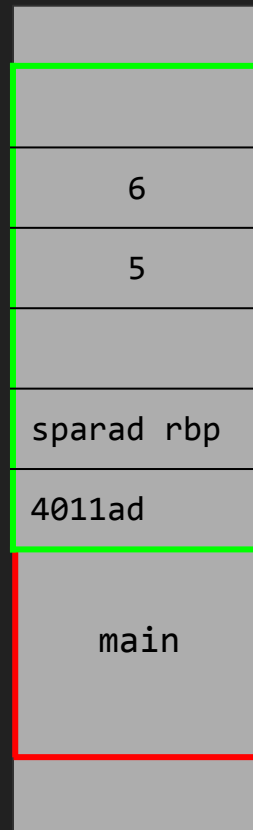
# Ett anrop i detalj

```
main:
...
40119e: mov     eax,DWORD PTR [rbp-0x4]
4011a1: mov     esi,0x6
4011a6: mov     edi,eax
4011a8: call    40114e <print>
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40115d: mov     DWORD PTR [rbp-0x18],esi
→ ...
401189: leave
40118a: ret
```

0x0000...00

Minnet



← rsp

← rbp

0xffff...ff



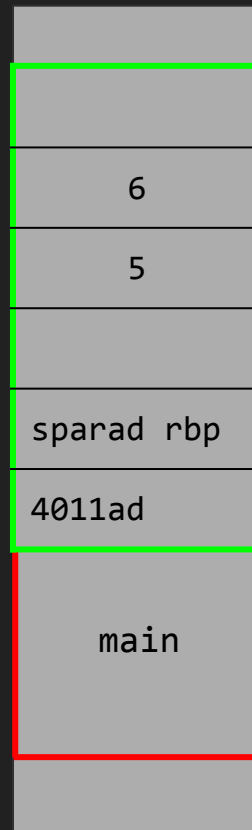
# Ett anrop i detalj

```
main:
...
40119e: mov     eax,DWORD PTR [rbp-0x4]
4011a1: mov     esi,0x6
4011a6: mov     edi,eax
4011a8: call    40114e <print>
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40115d: mov     DWORD PTR [rbp-0x18],esi
...
401189: leave
40118a: ret
```

0x0000...00

Minnet



← rsp

← rbp



0xffff...ff





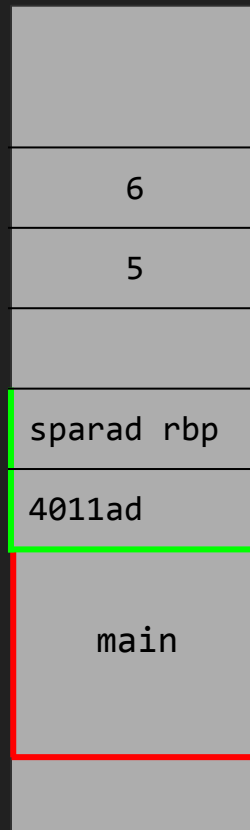
# Ett anrop i detalj

```
main:
...
40119e: mov     eax,DWORD PTR [rbp-0x4]
4011a1: mov     esi,0x6
4011a6: mov     edi,eax
4011a8: call    40114e <print>
4011ad: ...
```

```
print:
40114e: endbr64
401152: push    rbp
401153: mov     rbp,rsp
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40115a: mov     DWORD PTR [rbp-0x14],edi
40115d: mov     DWORD PTR [rbp-0x18],esi
...
401189: leave
40118a: ret
```

0x0000...00

Minnet



← rsp, rbp



0xffff...ff



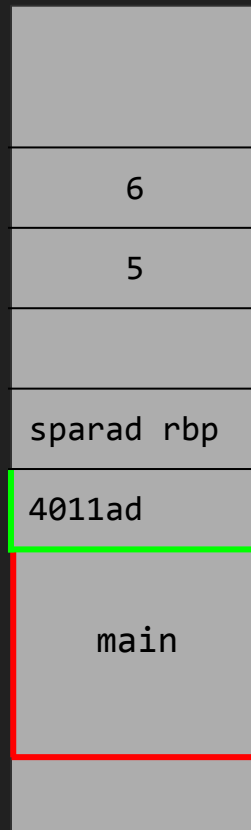
# Ett anrop i detalj

```
main:
...
40119e: mov     eax,DWORD PTR [rbp-0x4]
4011a1: mov     esi,0x6
4011a6: mov     edi,eax
4011a8: call    40114e <print>
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print:
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401152: push    rbp
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40115a: mov     DWORD PTR [rbp-0x14],edi
40115d: mov     DWORD PTR [rbp-0x18],esi
...
401189: leave
40118a: ret
```

0x0000...00

Minnet



← rsp

← rbp

0xffff...ff



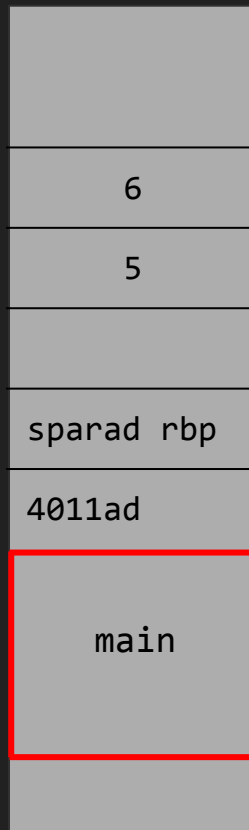
# Ett anrop i detalj

```
main:
...
40119e: mov     eax,DWORD PTR [rbp-0x4]
4011a1: mov     esi,0x6
4011a6: mov     edi,eax
4011a8: call    40114e <print>
→ 4011ad: ...

print:
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40115a: mov     DWORD PTR [rbp-0x14],edi
40115d: mov     DWORD PTR [rbp-0x18],esi
...
401189: leave
40118a: ret
```

0x0000...00

Minnet



← rsp

← rbp

0xffff...ff



# Syscalls



# Syscalls - eller, hur saker händer

- Hur kan man skriva till skärmen?
- Hur skriver printf till skärmen?
- Syscalls!
- Man ger kommandon till OS:et
- Finns många olika:
  - Skriv till skärmen
  - Hantera filer
  - Nätverk
  - Andra processer
  - ...
- [Lista över syscalls](#)

```
; write
401000: mov     eax,0x1
401005: mov     edi,0x1
40100a: movabs  rsi,0x402000
401014: mov     edx,0x15
401019: syscall

; exit
40101b: mov     eax,0x3c
401020: xor     rdi,rdi
401023: syscall
```



# Verktvg



# objdump - en disassembler (bland annat)

```
$ gcc main.c -o program  
$ objdump -d -M intel ./program
```

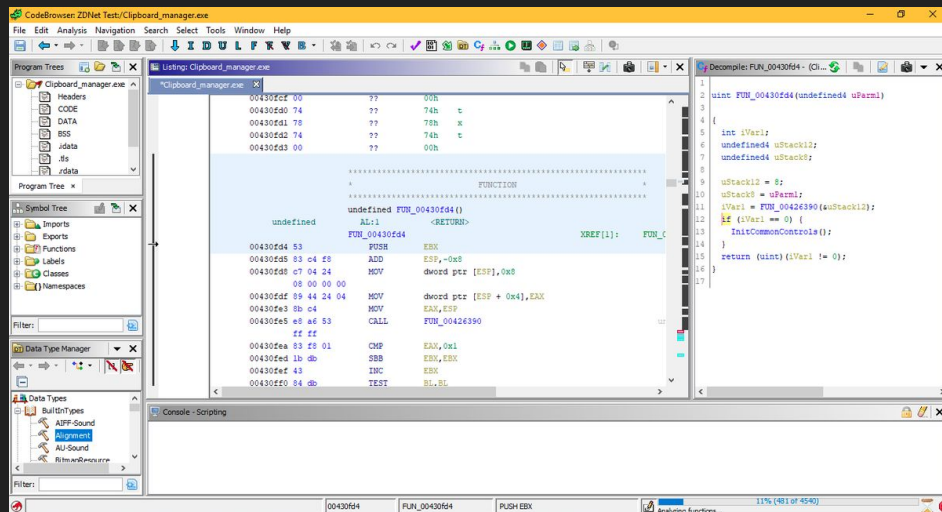
...

```
000000000040118b <main>:  
  40118b:    f3 0f 1e fa          endbr64  
  40118f:    55                   push    rbp  
  401190:    48 89 e5             mov rbp, rsp  
  401193:    48 83 ec 10          sub rsp, 0x10  
  401197:    c7 45 fc 05 00 00 00 mov DWORD PTR [rbp-0x4], 0x5  
  40119e:    8b 45 fc             mov eax, DWORD PTR [rbp-0x4]  
  4011a1:    be 06 00 00 00       mov esi, 0x6  
  4011a6:    89 c7               mov edi, eax  
  4011a8:    e8 a1 ff ff ff      call    40114e <print>
```

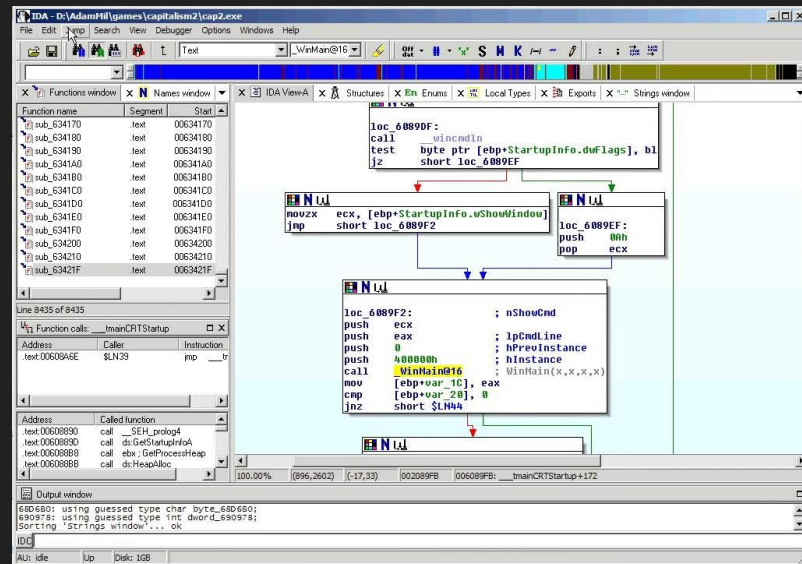
...



# Bättre disassemblers



[Ghidra](https://ghidra.sre.google/)

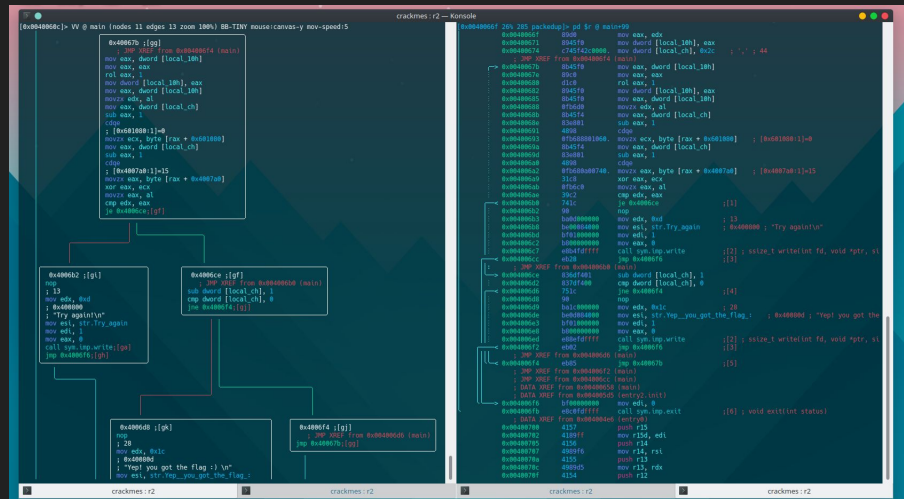


[IDA](https://hex-rays.com/)





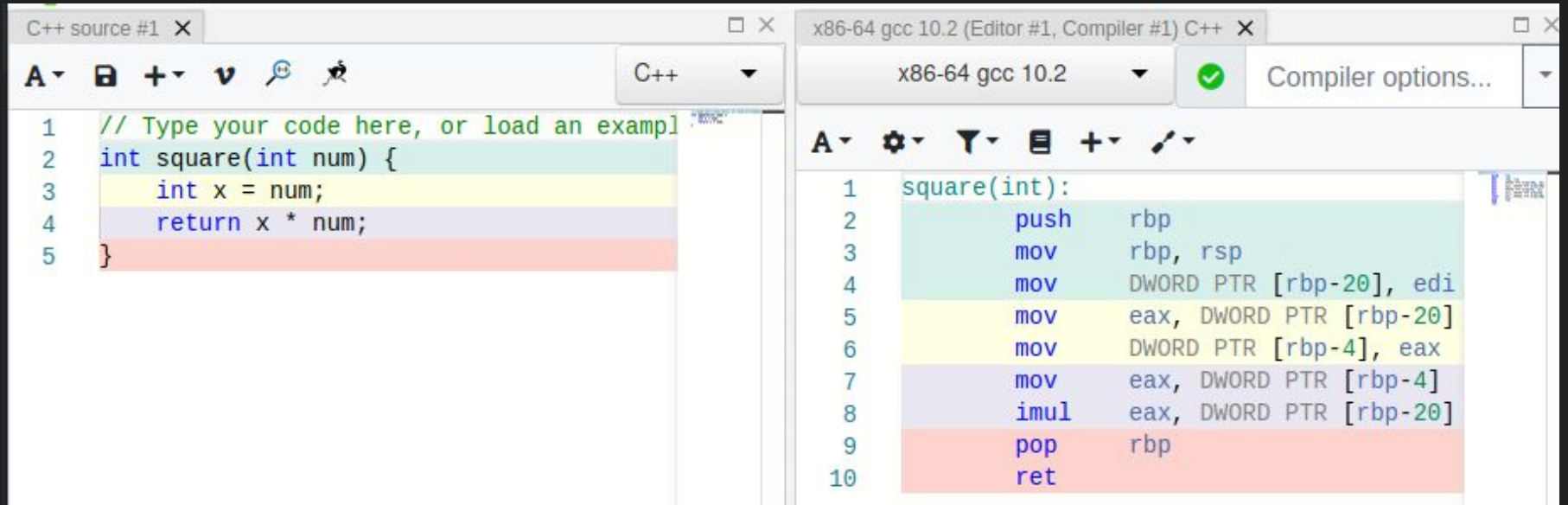
The image displays a debugger window with assembly code on the left and a control flow graph (CFG) on the right. The assembly code includes instructions such as `sub_100000f08`, `sub_100000f1a`, `sub_100000f2f`, `sub_100000f74`, `sub_100000fc2`, `sub_100001007`, `sub_100001055`, `sub_10000109a`, `sub_1000010d8`, `sub_10000111a`, `sub_100001155`, `sub_10000119a`, `__start`, `sub_100001b2c`, `sub_100001d90`, `sub_100001e5f`, `sub_100002884`, `sub_100002950`, `sub_100002a6d`, `sub_1000032f4`, `sub_10000335a`, `sub_100003498`, `sub_10000357c`, `sub_1000035b2`, `sub_100003640`, and `sub_100003734`. The CFG on the right shows a function `int64_t __start(int32_t arg1, int64_t arg2) __noreturn` with various branches and calls, including a call to `atoi` and a jump to `sub_100000127e`. A red arrow points to the `atoi` call in the CFG.



## radare2



# godbolt



The screenshot displays the Godbolt online compiler interface. On the left, the 'C++ source #1' editor shows a simple C++ function: `int square(int num) { int x = num; return x * num; }`. On the right, the 'x86-64 gcc 10.2' editor shows the corresponding assembly code generated by the compiler. The assembly includes stack frame setup, argument passing, and the calculation of the square.

```
1 // Type your code here, or load an example
2 int square(int num) {
3     int x = num;
4     return x * num;
5 }
```

```
1 square(int):
2     push    rbp
3     mov     rbp, rsp
4     mov     DWORD PTR [rbp-20], edi
5     mov     eax, DWORD PTR [rbp-20]
6     mov     DWORD PTR [rbp-4], eax
7     mov     eax, DWORD PTR [rbp-4]
8     imul    eax, DWORD PTR [rbp-20]
9     pop     rbp
10    ret
```

[godbolt.org](https://godbolt.org)



# `gdb` - GNU debugger

```
$ gcc main.c -o program
```

```
$ gdb ./program
```

```
(gdb) b main
```

```
Breakpoint 1 at 0x40118b
```

```
(gdb) r
```

```
Starting program: /home/mkg/program
```

```
Breakpoint 1, 0x000000000040118b in main ()
```

```
(gdb) info registers
```

<code>rax</code>	<code>0x40118b</code>	<code>4198795</code>
<code>rbx</code>	<code>0x4011c0</code>	<code>4198848</code>
<code>rcx</code>	<code>0x4011c0</code>	<code>4198848</code>
<code>rdx</code>	<code>0x7fffffffefe338</code>	<code>140737488347960</code>
<code>...</code>		



# gdb - Några kommandon

Kommando	Beskrivning
<code>run</code> eller <code>r</code>	Startar programmet
<code>b *0x40118b</code>	Sätter breakpoint vid adress
<code>b main</code>	Sätter breakpoint vid funktion
<code>continue</code> eller <code>c</code>	Fortsätter till nästa breakpoint
<code>backtrace</code> eller <code>bt</code>	Visar ett stacktrace
<code>si</code>	“Step instruction”
<code>ni</code>	“Next instruction”, hoppar över <code>call</code>
<code>info registers</code> eller <code>i r</code>	Skriver ut alla register



# gdb - Några kommandon

Kommando	Beskrivning
<code>i r rax</code>	
<code>set \$rax = 5</code>	
<code>quit</code> eller <code>q</code>	Gå ur gdb
<code>x/4gx \$rsp</code>	Visa 4 stycken 64-bitarstal i hex som ligger vid <code>rsp</code>
<code>x/20i \$rip</code>	Visa 20 instruktioner från <code>rip</code> och framåt
<code>x/s \$rbx - 0x20</code>	Visa strängen som ligger vid <code>rbx - 0x20</code>

[Många fler sätt att använda x/...](#)

Tips: installera [pwndbg](#)



# Vidare läsning / Saker jag inte hann ta upp

- Symboler
- Minnessektioner
- Virtuellt minne
- Heapen
- Disassembly vs. dekompilering
- ...
- Säkert många mer saker



# Frågor?

Slides: [bit.ly/spooktober-2020-intro-assembly](https://bit.ly/spooktober-2020-intro-assembly)

