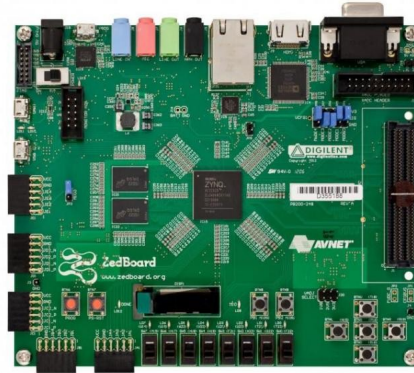
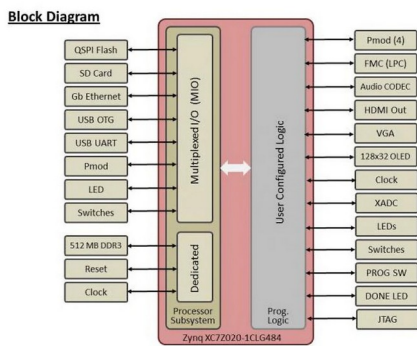


# ZedBoard vs Z-Turn

## ZedBoard:



Manuf. : Digilent/Avnet (2012)

Revision C

<https://digilent.com/reference/programmable-logic/zedboard/start>

<https://www.avnet.com/wps/portal/us/products/avnet-boards/avnet-board-families/zedboard/>

FPGA: XC7Z020-CLG484

CPU:

Dual-core ARM Cortex-A9  
CPU frequency 866MHz  
ARMv7-A architecture  
L1/L2Cache (512Kb)

PL:

85K PL Cells  
53200 LUTs  
4,9 Mb BRAM

Memory:

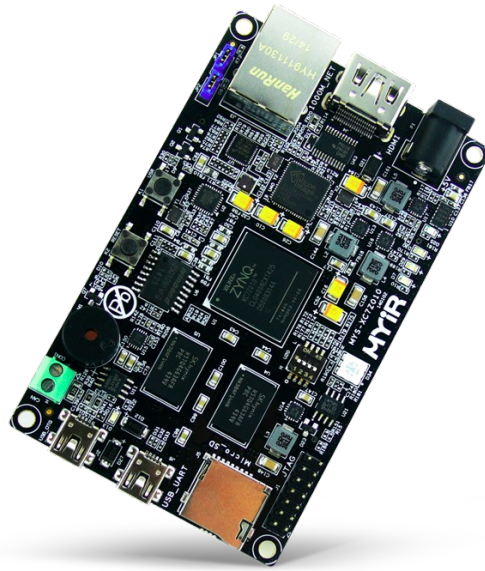
512 MB DDR3 (128M x 32) (Micron)

256 Mb QSPI Flash

Features : HDMI, VGA, OLED (Integrated USB-JTAG Programming)

POWER: 12V @5A

# Z-Turn



Manuf: Myir

Revision: Unkown

<http://www.myirtech.com/list.asp?id=502>

FPGA: XC7Z010-1CLG400

CPU:

Dual-core ARM Cortex-A9

CPU frequency 667MHz

ARMv7-A architecture

L1/L2Cache (512Kb)

PL:

28K PL Cells

17600 LUTs

4,9 Mb BRAM

Memory:

1 GB DDR3 (128M x 32) SKhynix

16 Mb QSPI Flash

Features : HDMI, G Sensor, Temp Sensor,

POWER: USB (3.3V) or DV 5V @2A