# INFO-F-404 : Real-Time Operating Systems Project 2 : Bitonic

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### 1 Implementation choices

There are two main types of processes: one *master node* and multiple *compute nodes*. Each node has its own purpose and interacts with the other processes using *MPI*. Inside the code, the main function assigns a type to the current process, by calling the masterNode function if the process's rank is 0, and the computeNode function otherwise. We have implemented the bonus part with the same code basis as the main part, please refer to section 2 to learn about the its usage.

#### 1.1 Master

The objective of the *master node* is to synchronize and deal *merge tasks* to the *compute nodes*. It does so by executing one or more successive masterMerge calls, which consist of sending two values to each *compute node*, and receiving back two values. Here is an example of an execution of masterMerge:

- 1. size is inferred from the sequence size and the goal (more on that later)
- 2. for each *compute node*, identified by i = rank 1:
  - (a) send values  $(v_k, v_l)$  to i, where

$$k = \left\lfloor \frac{i}{size/2} \right\rfloor \cdot size + (i \bmod size) , \qquad l = k + \frac{size}{2}$$

(b) receive values from i, and place them at indexes (2\*i, 2\*i+1) of the sequence

More precisely, if the goal is to sort an arbitrary list, N calls will be made: the first N-1 will result in the sequence being bitonic, and the last will ensure the sequence is sorted. In that case the value size will be initialized at 2 and then doubled for each call, until reaching the size of the sequence. If instead the goal is to sort a provided bitonic sequence, only one call will be required, with size initialized as the sequence size.

The intuition of the masterMerge function is that it executes the first and last steps of the Merge 2k algorithm seen in the course, all other steps being executed by the *compute* nodes. The way k and l are computed allows us to parallelize multiple independent executions of the Merge 2k algorithm on increasingly large independent subsets of the sequence, until we can run a single instance over the whole sequence. The  $\left\lfloor \frac{i}{size/2} \right\rfloor \cdot size$  factor of k is what separates these subsets, and always equals 0 in the last call.

#### 1.2 Compute

The purpose of the *compute nodes* is to compare pairs of values and to determine to which other *compute node* they have to send the outcome of the comparison to. The mechanism is defined in the computeMerge function and works as follows:

1. compute node i = rank - 1 receives values  $(v_0, v_1)$  from the master node

- 2. the value depth is inferred from the sequence size and the selected outcome
- 3. values are reordered according to order O, which can be assumed to be non-decreasing order for now
- 4. if depth equals 0, send both ordered values to the master node
- 5. the rank of a paired process i' is determined using the formula

$$i' = i \oplus 2^{depth}$$

- 6. the value to send and replace with value from i' is  $v_l$  where l is the  $(depth-1)^{th}$  least significant bit in the binary unsigned integer representation of i.
- 7. the selected value is sent and replaced by the value received from i'
- 8. depth is decreased and the process is repeated from step 3

This computeMerge function is called exactly once for each computeMerge call. It is easy to verify that the number of calls does not depend on the values to sort, only on the size of the sequence (which is known by all processes since it only depends on the total number of processes). In each of these successive calls, the initial value of depth is set to match  $log_2(size) - 1$ . That value then represents the comparisons left to perform, and is decreased after each loop.

The intuition of computeMerge is that is executes a generalized version of the Merge 2k algorithm. After each comparison, processes split into two independent groups (of size  $2^{depth}$ ), and the distance between compared values is divided by two. By that logic, the paired process is simply the process that will have the same relative position in the other group after the split, and which needs the result of the comparison. When a group splits, in order to avoid unnecessary interactions, the lower half of the processes hangs on to their (consecutive)  $v_0$  values whilst the rest keep their (consecutive)  $v_1$  values, leaving two (consecutive) sub-sequences of values to exchange in total. Finally, the order O of the comparison is the desired order of the list, but it is inverted in alternate groups during the computeMerge calls used to turn a sequence bitonic.

Figure 1 shows the complete sequence of comparisons involved in the bitonic sort for an arbitrary sequence of size 16. Arrows represent compare-swap instructions, and point towards the resulting  $v_1$ . It features four boxes corresponding to four calls to masterMerge and computeMerge. Withing each box, stacks of red sub-boxes are independent groups or processes. The background indicades if the order of the comparison is normal (blue) or reversed (green).

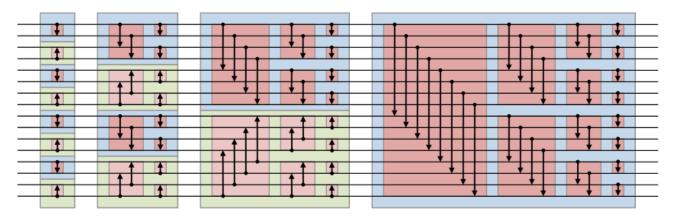


Figure 1: Network representation of the complete bitonic sort (16 inputs)

### 2 Project usage and configuration

As mentioned in the assignment statements, the source code can either be used with a *bitonic* list as input or with an unsorted list.

In the case of a *bitonic* list input, the user is invited to set the macro SORT\_FIRST (main.cpp line 8) to false. Doing so, will make the program to sort the hardcoded list written line 73.

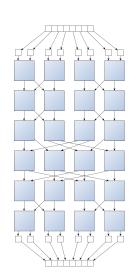
The bonus part mention the possibility to use full random list. This is implemented on the file bitonicbonus (which could be compile with the option bitonicbonus on the Makefile).

Notice that the list generation does not ensure the generation of a *bitonic* list. Hence, it is required to set the macro SORT\_FIRST to *true*.

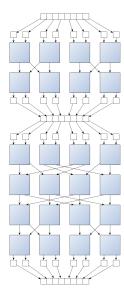
Once that the manipulation have been done, the user is invited to compile the project using the provided Makefile.

### 3 Inter-processes communications

The implemented inter-processes communication protocol (2a) differs a bit from the one suggested in the reference (2b). In fact, in order to ease the implementation of the *parallel bitonic sorting algorithm* and to preserve the merge functions used for sorting bitonic sequences intact, it has been decided that after each merge the *compute nodes* have to send their results to the master node instead of dealing themselves with the communication. Consequently, parallelism is not exploited as far as possible and this has an impact on the expected performances.



(a) State of the art communications



(b) Project communications

Figure 2

## 4 Limitations and performances

The biggest limitation of the project is due to the inter-node communication protocol that has been implemented. For reminder, the  $master\ node$  oversees the important steps of the communication scheme (See section 1.1 for further information). Even though introducing such a protocol has eased the development of the project, useless communications have also been introduced resulting in an non-optimal (but more than correct) use of MPI. This flaw could be addressed by implementing a formula that would deterministically choose the correct destination (=  $compute\ node$ ). Consequently, the  $master\ node$  would be less solicited as it would not have to oversee the communications anymore (i.e. its job would only consists of sending the initial list and receiving the sorted list).

Further more, provided that the above "optimisation" has been implemented, one could also consider using the master mode as a *computation node*. There would be several benefits as the overall number of required communications would decrease and as the number of required processes would be reduced by 1. Notice that this "optimisation" would however breaks one of the rules of the assignment statement.

#### 5 Encountered difficulties

Regardless of the difficulties that directly involved MPI, the main difficulties encountered during the realisation of the project have been the derecurssivication of the  $classic\ bitonic\ sort$  and the distribution of the comparisons modules into several processes.