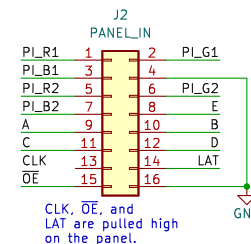
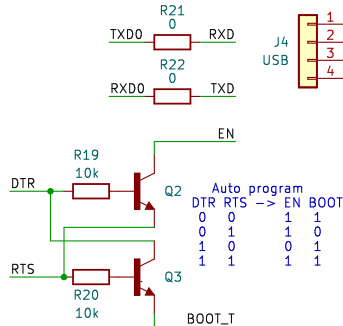
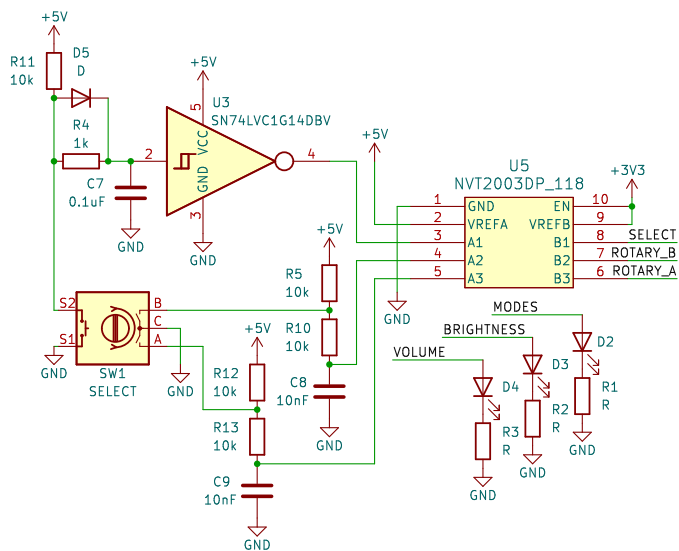


By connecting the input and output connectors, we create one big shift register, allowing us to use SPI to drive it.

SD_DATA3	PI_R1
PO_R1	PI_R2
PO_R2	PI_G1
PO_G1	PI_G2
PO_G2	PI_B1
PO_B1	PI_B2

GPIO19	A
GPIO23	B
GPIO18	C
GPIO5	D
GPIO15	E

SD_CLK	CLK
GPIO16	OE
GPIO22	LAT



CLK, OE, and LAT are pulled high on the panel.

