



# AND-TFT-35VX-4HB 3.5" TFT LCD LCD Color Monitor

The AND-TFT-35VX-4HB is a compact full color TFT LCD module, that is suitable for security, video games, door phones, video phones, portable TV and instrument displays and other media applications which require a high quality flat panel display. This device consists of an amorphous silicon TFT LCD panel with 12V CCFL backlightthat has 640 x 480 pixels on a 3.5 inch diagonal screen.

#### **Features**

· Amorphous silicon TFT LCD panel with CCFL B/L

· Module with resistive type touch panel

· Pixel in stripe configuration

• High Resolution: 262,144 Dots (640 x 480)

· Optimum viewing direction: 6 o'clock

· Auto-detect input signal when power on

• Input Signal: D-Sub VGA Signal, Dual Composite Video

Channals and S-Video

• Operating Temperature: 0°C ~ 60°C

• Storage Temperature: -20°C ~ 80°C

#### **Mechanical Characteristics**

Item	Specification	Unit
Screen Size	3.5 inch diagonal	inch
Display Format	640 x (R, G, B) x 480	dot
Display Colors	262,144	-
Active Area	72 (W) x 52.56 (H)	mm
Pixel Pitch	0.1125 (W) x 0.1095 (H)	mm
Pixel Configuration	Stripe	-
Outline Dimension	84.25 (W) x 65.40 (H) x 4.45 (D) (Typ.)	mm
Weight	52 ± 5	g
Surface Treatment	AG	-
Surface Treatment of Touch Panel	3H	ı

#### Absolute Maximum Rating VSS1 = VSS2 = GND = )V. Ta=25°C

Item	Symbol	Remark	Min.	Max.	Unit
	V <sub>DD1</sub>	-	-0.3	2	V
	V <sub>CC</sub>	-	-0.3	5	V
Cupply Voltage	V <sub>DD2</sub>	-	-0.5	12.0	V
Supply Voltage	V <sub>GG</sub>	-	-0.3	40.0	V
	V <sub>GG</sub> -V <sub>EE</sub>	-	-	40.0	V
	V <sub>EE</sub>	-	-20	0.3	V
Storage Temperature	T <sub>ST</sub>	-	-20	+80	°C
Operation Temperature	T <sub>OP</sub>	Note 1	0	+60	°C

Note 1: Operating Temperature defines that contrast, response time, other display optical character are Ta=+25.

Product specifications contained herein may be changed without prior notice.

It is therefore advisable to contact Purdy Electronics before proceeding with the design of equipment incorporating this product.



### **Power Consumption**

Item	Symbol	Conditions	Specific	Units	
iteiii	Symbol	Conditions	Тур.	Max.	Uiillis
Supply Current for Gate Driver (Hi level)	I <sub>GG</sub>	V <sub>GG</sub> =+17V	0.12	0.15	mA
Supply Current for Gate Drive (Low level)	I <sub>EE</sub>	V <sub>EE</sub> =-10V	0.15	0.19	mA
Supply Current for Source Driver (Digital)	I <sub>DD1</sub>	V <sub>DD1=</sub> +3.3V	4.8	8.0	mA
Supplly Current for Source Driver (Analog)	I <sub>DD2</sub>	V <sub>DD2</sub> =+10V	16.0	30.0	mA
Supply Current for Gate Driver (Digital)	I <sub>cc</sub>	V <sub>CC</sub> =+3.3V	0.17	0.21	mA
LCD Panel Power Consumption	_	-	180	332	mW
Backlight Power Consumption	P <sub>LED</sub>	-	384	456	mW
Total Power Consumption	_	-	564	788	mW

**Recommended Operating Conditions** (VSS1 = VSS2 = GND = 0V, Ta = 25°C)

Item	Specifica Specifica				Unit
item	Symbol	Min.	Тур.	Max.	Oilit
Supply Voltage for Source Driver	V <sub>DD1</sub>	3.0	3.3	3.6	V
Supply voltage for Source Driver	V <sub>DD2</sub>	9.5	10	10.5	V
	V <sub>GG</sub>	-	+17	-	V
Supply Voltage for Gate Driver	V <sub>EE</sub>	-	-10	-	V
	V <sub>CC</sub>	3.0	3.3	3.6	V
Digital Input Voltage	V <sub>IH</sub>	0.8 V <sub>DD1</sub>	-	V <sub>DD1</sub>	V
Digital Input voltage	V <sub>IL</sub>	0	-	0.2 V <sub>DD1</sub>	V

### Recommended Driving Conditions for LED Backlight (GND= 0V, Ta=25°C)

Item	Symbol	Min.	Тур.	Max.	Unit	Remarks	
Supply Voltage of CCFL Backlight	upply Voltage of CCFL Backlight V <sub>LED</sub>		12	_	V	I <sub>L</sub> = 20MA	
Supply Current of CCFLBacklight	I <sub>LED1</sub>	_	20	_	mA	Note 1	
Supply durient of dor Ebacklight	I <sub>LED2</sub>	_				Note 1	
Backlight Power Consumption	P <sub>LED</sub>	360	384	456	mW	Note 2	

Note 1: CCFL B/L applied information, please refer to the appendix at the end.

Note 2:  $P_{LED} = V_{LED} * I_{LED1} + V_{LED} * I_{LED2}$ .



Optical Specifications (Ta=25°C)

la	Item		Symbol Conditions		Specifications		
"	lem	Syllibol	Conditions	Min.	Тур.	Max.	Unit
	Horizontal	θ		± 45	± 50	_	
Viewing Angle	Vertical	$\theta$ (to 12 o'clock)	CR>10	10	15	_	deg
	Vertical	$\theta$ (to 6 o'clock)		30	35	_	
Contrast Ratio Luminance when L Luminance when L		CR	-	200	400	-	-
Response Time	Rise	Tr	θ =0°	_	15	30	mo
nesponse fille	Fall	Tf		_	25	50	ms
Brightness	•	LUM	θ =0°	_	1,000	_	cd/m <sup>2</sup>
Uniformity		U	<i>θ</i> =0°	70	75	-	%
Cross Talk		-	θ=0°	_	-	3	%
NAVIL: it - Ole ti - it -		X		0.28	0.31	0.34	
White Chromatici	ıy	Υ	_	0.30	0.33	0.36	1 -
Lamp Life Time	Ta=25°C	-	-	-	10,000	-	hrs

#### **Electronic Characteristics**

Symbol	I/O	Conditions	Min.	Тур.	Max.	Unit
V in		DC(+)	4.8	5	5.2	V
l in	I	DC(+5V)	400	450	500	mA
P in		DO(+3V)		2.25		W

### **Touch Panel Characteristics - Electrical Performance**

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Terminal Resistance	Х	120	240	370	Ω	
Terminal Hesistance	Y	280	570	860	Ω	
Input Voltage	VT	-	5.0	7.0	V	
Linearity (X, Y direction)		20		±1.5	%	
Insulation Impedance		20			ΜΩ	DC25V
Response Time				5	ms	
Operation Force				35	g	Note 1

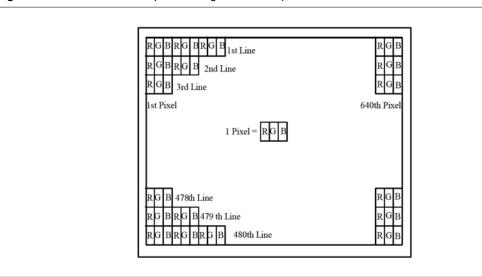
Note 1: Input through 0.8R stylus or finger.



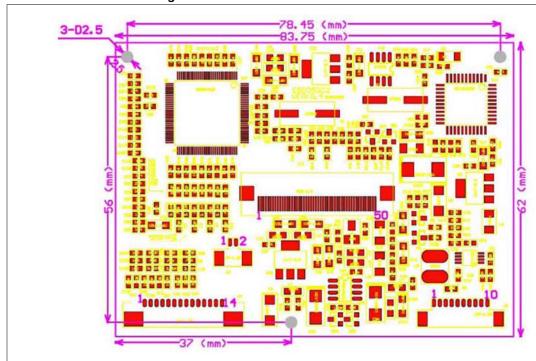
### **Touch Panel Characteristics - Durability Performance**

Hitting Durability	At least 1,000,000 times with 48.0mm silicon rubber, 200g, 3 times/second.
Sliding Durability	At least 1,000,000 times with r0.8mm polyacetal stylus, 200g, 60mm/second.

### Pixel Arrangement - The LCD module pixel arrangement is stripe.



### **Driver Board Outline Drawing**

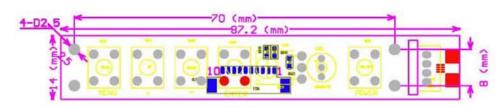


### **Description:**

Outline:  $83.75 \times 62 \times 7.2 \text{ mm}$ Top Layer High (Max): 6 mm Board thickness: 1.2 mmThree Screw Holes:  $\Phi 2.5 \text{ mm}$ 



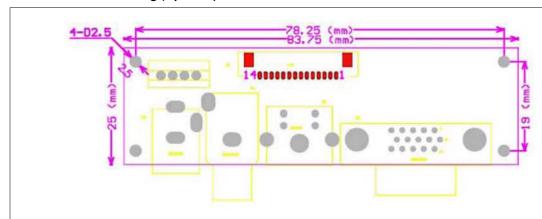
### **Keyboard Outline Drawing (Optional)**



**Description:** 

Outline:  $87.2 \times 14 \times 12.7 \text{ mm}$ Top Layer High (Max): 8 mmBottom Layer High (Max): 3.5 mmBoard thickness: 1.2 mmFour Screw Holes:  $\Phi 2.5 \text{ mm}$ 

#### **Demo Kit Outline Drawing (Optional)**



**Description:** 

Outline: 83.75 x 25 x 19.7 mm Top Layer High (Max): 15 mm Bottom Layer High (Max): 3.5 mm

Board thickness: 1.2 mm Four Screw Holes:  $\Phi$  2.5 mm



## **Input/Output Terminals**

J12 Output to Panel Signal Terminal (50FPC)

J4 Outside Signal Input Terminal (14 pin)

J5 Control Signal Input Terminal (10 pin)

J3 Video 2 Input Terminal (2 pin) (Optional)

J4 Outside Signal Input Terminal

Connector: Molex 53261-1471 or Compatible

Pin No.	Pin Name	I/O	Pni Description	Remarks
1	GND	I	Ground	-
2	VSYNC	I	VSYNC input for RGB input	-
3	HSYNC	I	HSYNC input for RGB input	-
4	GND	I	Ground	-
5	В	I	Video BLUE input	-
6	G	I	Video GREEN input	-
7	R	I	Video RED input	-
8	GND	I	Ground	-
9	S-C	I	S-video Chroma input	-
10	S-Y	I	S-video luma input	-
11	VIDEO1	I	Composite Video 1	-
12	GND	I	Ground	-
13	GND	I	Ground	-
14	V <sub>CC</sub>		5V	4.8V – 5.2V

## J5 Control Signal Input/Output Terminal Connector: Molex 53261-1071 or Compatibility

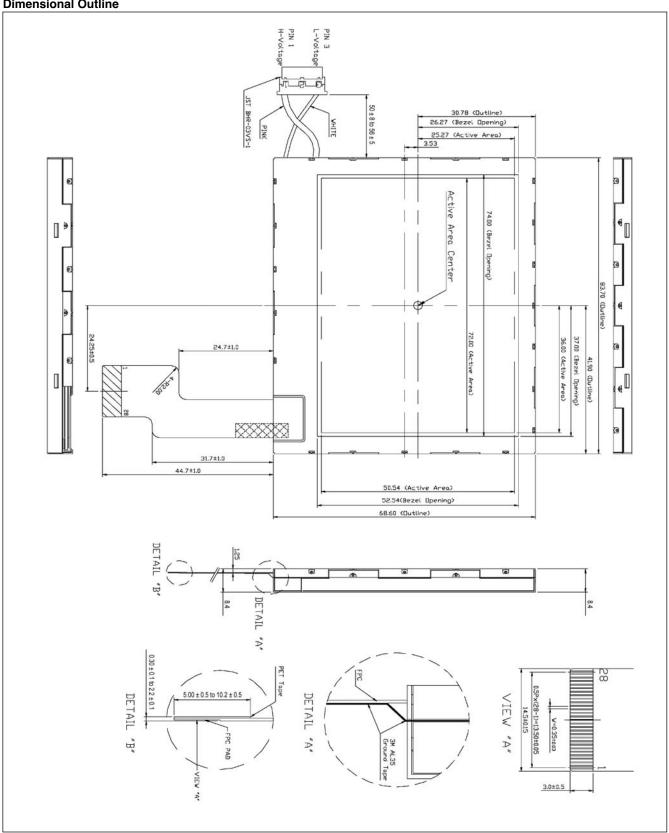
Pin No.	Pin Name	I/O	Pin Description	Remarks
1	LED	0	Red Lamp	_
2	LED	0	Green Lamp	_
3	SENSOR	I	Remote Sensor	-
4	GND	I	Ground	-
5	3.3V	0	+3.3V	-
6	ON/OFF	I	Power On/Off	_
7	MENU	1	Menu	-
8	DOWN	I	Down	-
9	UP	I	Up	_
10	ENTER		Enter	_

# J5 Control Signal Input/Output Terminal Connector: Molex 53261-1071 or Compatibility

Pin No.	Pin Name	I/O	Pin Description	Remarks
1	GND	1	Ground	_
2	VIDEO2	I	Composite Video 2	-



### **Dimensional Outline**





Timing Parameters - AC Electrical Characteristics (VCC=VDD1=3.3V, VDD2 = 10V, GND = VSS1 = VSS2 = 0V, Ta=25°C)

Item	Symbol	Min.	Тур.	Max.	Unit
CLK Frequency	F <sub>CLK</sub>	_	25	40	MHz
CLK Pulse Width	ТСРН	25	-	_	ns
Data Set-up Time	T <sub>SU</sub>	4	-	_	ns
Data Hold Time	T <sub>HD</sub>	2	-	_	ns
Propagation Delay of DIO2/1	T <sub>PHL</sub>	6	10	15	ns
Time That The Last Data to LD	T <sub>LD</sub>	1	-	_	T <sub>CPH</sub>
Pulse Width of LD	T <sub>WLD</sub>	2	-	_	T <sub>CPH</sub>
Time That LD to DIO1/2	T <sub>LDS</sub>	5	-	_	T <sub>CPH</sub>
POL Set-up Time	T <sub>PSU</sub>	6	_	_	ns
POL Hold Time	T <sub>PHD</sub>	6	-	_	ns
OE Pulse Width	T <sub>OEV</sub>	1	-	_	μ\$
CKV Pulse Width	T <sub>CKV</sub>	500	-	_	ns
STV Set-up Time	T <sub>SUV</sub>	400	-	_	ns
STV Hold Time	T <sub>HDV</sub>	400	_	_	ns
Horizontal Display Period	T <sub>HDP</sub>	_	640	_	T <sub>CPH</sub>
Horizontal Period Timing Range	T <sub>HP</sub>	_	800	-	T <sub>CPH</sub>
Horizontal Lines Per Field	T <sub>V</sub>	520	525	640	T <sub>HP</sub>
Vertical Display Timing Range	T <sub>DV</sub>	_	480	-	T <sub>HP</sub>