



ANDpSi12C505-HB

12.1" XGA Color p-Si TFT LCD Module

The ANDpSi12C505-HB is 1024 x 768 Color TFT display that utilizes new poly-silicon (p-Si) technology to provide a brighter, thinner and lighter display with high-resolution. The p-Si TFT technology allows the row and column LCD drivers to be fabricated directly on the LCD glass. This eliminates the need for discrete TAB drivers and also reduces the thickness, weight and overall size of the display. The 12.1" XGA resolution expands applications in such areas as electronic books and personal digital-picture viewers.

Features

- p-Si construction with drivers on glass
- High luminance
- Single CCFL, Sidelight type
- 202 pixels per inch - equivalent to printed materials
- LVDS interface system
- XGA (1024 x 768 pixels color display)
- Fast response time
- Applications: electronic books and personal digital-picture viewers

Mechanical Characteristics

Item	Specification	Unit
Outline Dimensions	261.0 (H) x 199.0 (V) x 5.0 max (D)	mm
Number of Pixels	1024 (H) x 768 (V)	pixels
Active Area	245.76 (H) x 184.32 (V)	mm
Pixel Pitch	0.24 (H) x 0.24 (V)	mm
Weight (approx.)	275	gram
Backlight	CCFL, Sidelight type (1 lamp)	—

Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Supply Voltage	V_{DD}	-0.3	4.0	V
	V_{FL}	0	2.0	kV(rms)
FL Driving Frequency	f_{FL}	—	100	kHz
Input Signal Voltage	V_{IN}	-0.3	$V_{DD} + 0.3$	V
Operating Temperature	T_{op}	0	50	°C
Storage Temperature	T_{stg}	-20	60	°C
Humidity (Max. Wet bulb temp = 29°C)	—	10	90	%(RH)

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min.	Typ.	Max.	Unit
Supply Voltage	V_{DD}	3.0	3.3	3.6	V
V_{FL} : $I_{FL}=6.0$ mA(rms)	V_{FL}	—	(620)	—	V(rms)
FL Start Voltage (Ta = 0°C)	—	1200	—	(1400)	V(rms)
Differential Input	V_{ID}	100	—	600	mV
Common Mode Input Voltage	V_{CM}	1.0	—	2.4- (V_{ID})/2	V
Current Consumption	*1 (I_{DD})	—	(225)	—	mA
	*2 (I_{FL})	—	6.0	—	mA(rms)
*2 *3 Power Consumption	P	—	(4.46)	—	W

Notes:

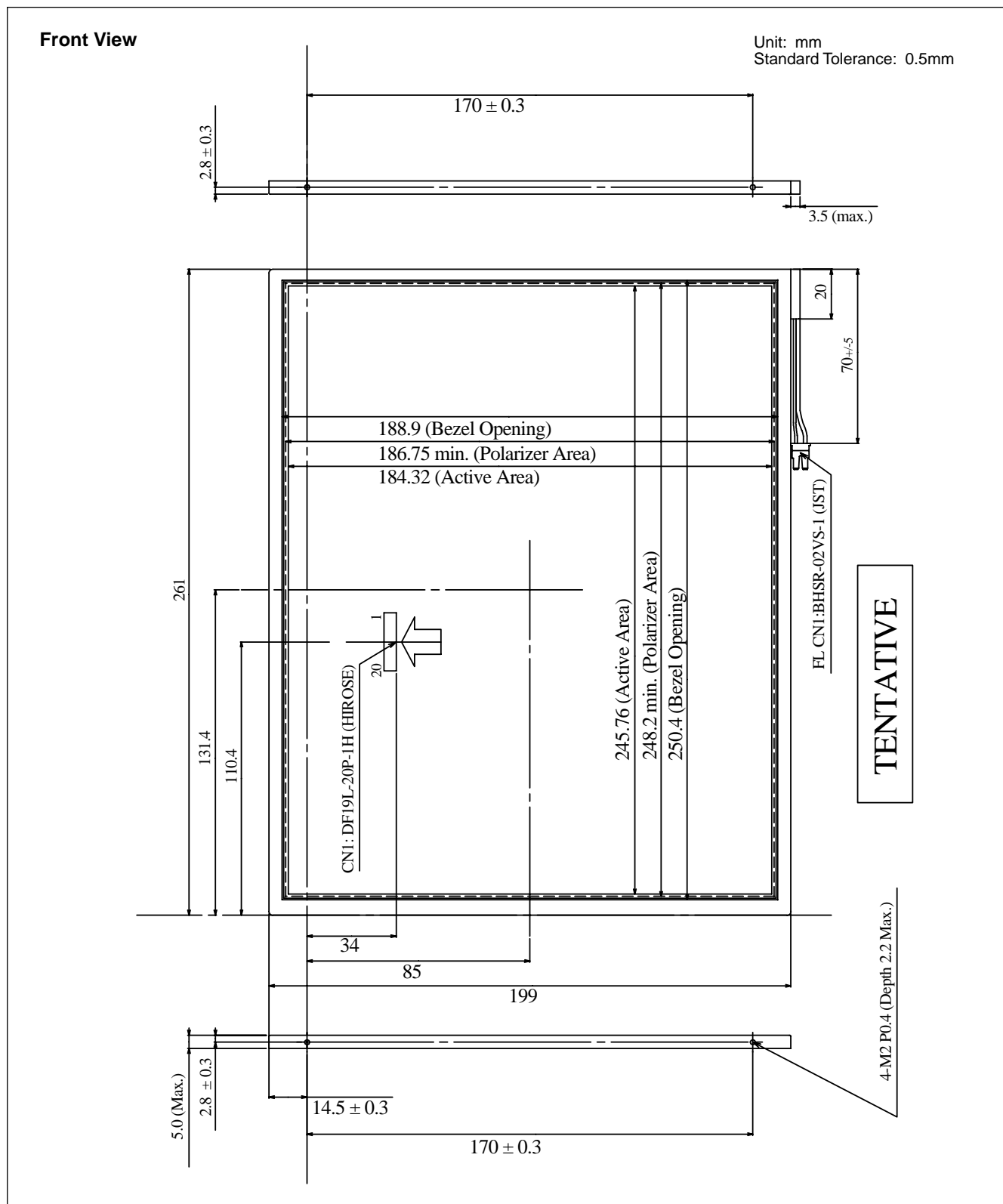
*1: 8 color bars pattern

*2: Excepting the efficiency FL inverter

Optical Characteristics (Ta = 25°C)

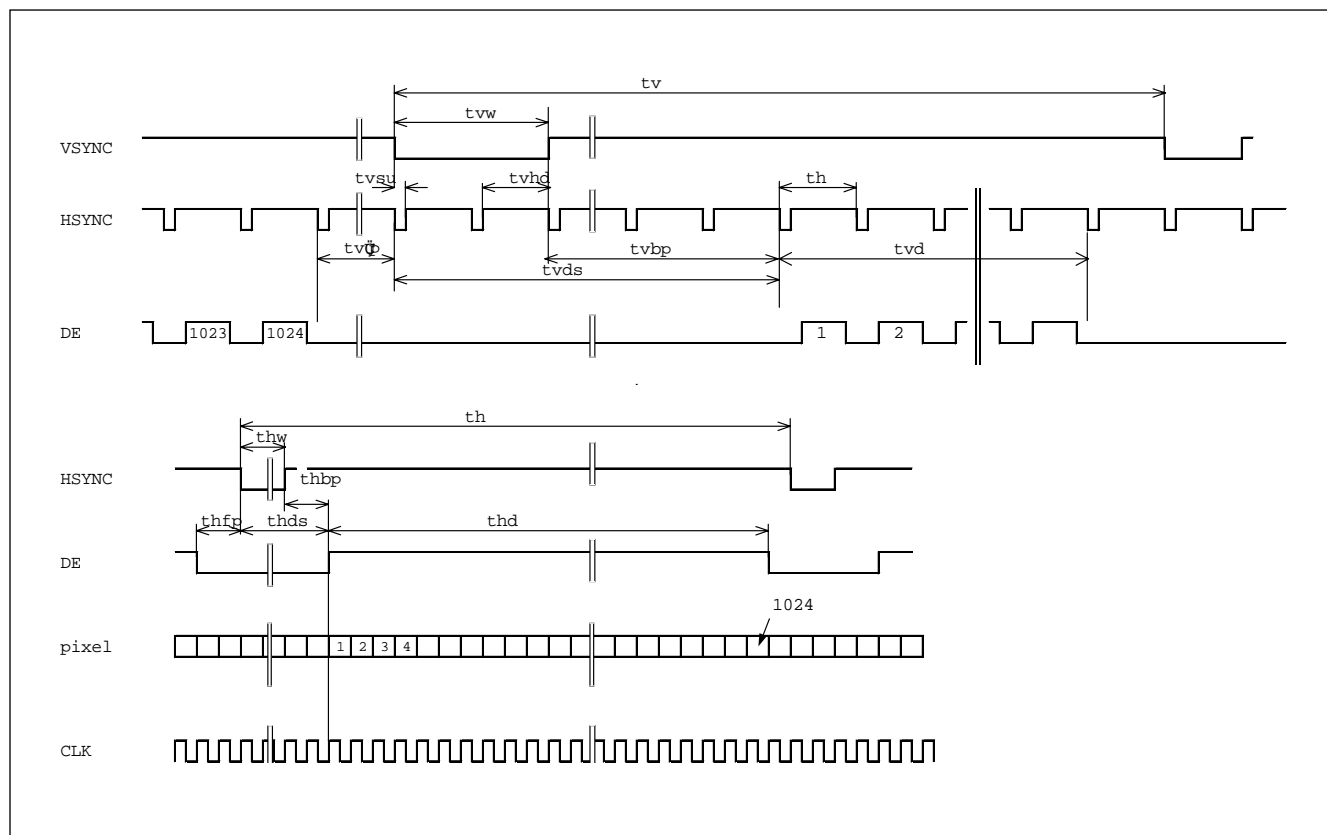
Item	Sym.	Condition	Min.	Typ.	Max.	Unit
Contrast Ratio	CR	$\phi = 0^\circ, \theta = 0^\circ$	100	250	—	—
Response Time	t_{on}	$\phi = 0^\circ, \theta = 0^\circ$	—	—	50	ms
	t_{off}		—	—	50	ms
Luminance	L	$I_{FL}=(6.0)$ mA(rms)	—	(165)	—	cd/m ²

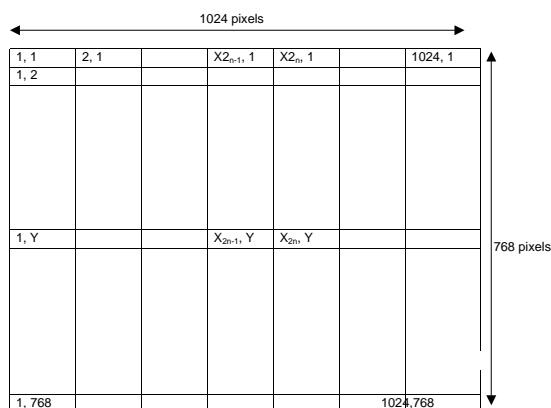
Dimensional Outline



Timing Specifications

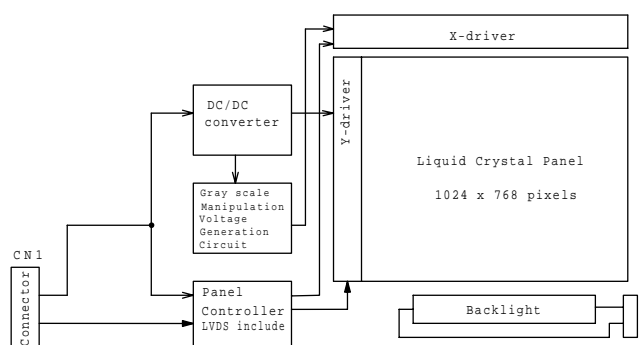
Item	Symbol	Min	Typ	Max	Unit
Horizontal Scanning Term	t_h	$1334 \times t_c$	$1344 \times t_c$	—	clock
H-sync Pulse Width	t_{hw}	$4 \times t_c$	$136 \times t_c$	—	clock
Horizontal Front Porch	t_{hfp}	$4 \times t_c$	$24 \times t_c$	—	clock
Horizontal Back Porch	t_{hbp}	$24 \times t_c$	$160 \times t_c$	—	clock
Horizontal Data Sync Period	t_{hds}	$32 \times t_c$	$296 \times t_c$	—	clock
Horizontal Display Term	t_{hd}	$1024 \times t_c$	$1024 \times t_c$	$1024 \times t_c$	clock
Frame Period	t_v	$778 \times t_h$	$806 \times t_h$	$860 \times t_h$	line
V-sync Pulse Width	t_{vw}	$2 \times t_h$	$6 \times t_h$	—	line
V-sync Set Up Time (to H-sync)	t_{vsu}	$8 \times t_c$	—	—	clock
V-sync Hold Time	t_{vhd}	$(t_{hbp}+16) \times t_c$	—	—	clock
Vertical Front Porch	t_{vfp}	$1 \times t_h$	$3 \times t_h$	—	line
Vertical Back Porch	t_{vbp}	$2 \times t_h$	$29 \times t_h$	—	line
Vertical Data Sync Period	t_{vds}	$8 \times t_h$	$35 \times t_h$	—	line
Vertical Display Time	t_{vd}	$768 \times t_h$	$768 \times t_h$	$768 \times t_h$	line
Clock Period	t_c	15.0	15.38	—	ns

Timing Chart




Recommended Inverter:

Block Diagram



1) Drivers are fabricated on the LCD glass

2) Connectors

CN1: DF19L-20P-1H / Hirose Electric Co., Ltd.

Mating Connector - DF19G-20S-1F / (FPC)

Mating Connector - DF19G-20S-1C / (Cable)

CN2: BHSR-02VS-1/Japan Solderless Terminal Mfg. Co., Ltd

Mating Connector - SM02B-BHSS-1 / JST

Connector Pin Assignment for Interface

CN1 Input Signal

(DF19L-20P-1H / Hirose Electric Co., Ltd.)

Terminal No.	Symbol	Function
1	V _{DD}	Power Supply: +3.3V
2	V _{DD}	Power Supply: +3.3V
3	V _{SS}	Ground
4	V _{SS}	Ground
5	RxIN0-	Negative LVDS differential data input (R0-R5-G0)
6	RxIN0+	Positive LVDS differential data input (R0-R5-G0)
7	V _{SS}	GND
8	RxIN1-	Negative LVDS differential data input (G1-G5, B0-B1)
9	RxIN1+	Positive LVDS differential data input (G1-G5, B0-B1)
10	V _{SS}	GND
11	RxIN2-	Negative LVDS differential data input (B2-B5, HS, VS, DE)
12	RxIN2+	Positive LVDS differential data input (B2-B5, HS, VS, DE)
13	V _{SS}	GND
14	CLK-	Clock Signal (-)
15	CLK+	Clock Signal (+)
16	V _{SS}	GND
17	NC	
18	NC	
19	V _{SS}	GND
20	V _{SS}	GND

Note: Please connect GND pin to ground. Don't use it as no-connect or nor connection with high impedance.

CN2 CCFL Power Source

(BHSR-02VS-1/Japan Solderless Terminal Mfg Co., Ltd.)

Terminal No.	Symbol	Function
1	V _{FLH}	CCFL Power Supply (High Voltage)
2	V _{FLL}	CCFL Power Supply (Low Voltage)

Note (2): 256K colors are displayed by the combinations of 18 data bits.

	Display	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0	Gray Scale Level	
Basic Color	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	—	
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	—	
	Green	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L	L	L	L	—	
	Lt. Blue	L	L	L	L	L	L	H	H	H	H	H	H	H	H	H	H	H	H	—	
	Red	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	—	
	Purple	H	H	H	H	H	H	L	L	L	L	L	L	H	H	H	H	H	H	—	
	Yellow	H	H	H	H	H	H	H	H	H	H	H	H	L	L	L	L	L	L	—	
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	—	
Gray Scale of Red	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0	
	▲ Dark ↕ Light ▼	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L1	
		L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L2	
		:						:						:						L3~L60	
		:						:						:							
		H	H	H	H	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L61	
		H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L62	
	Red	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	Red L63	
Gray Scale of Green	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0	
	▲ Dark ↕ Light ▼	L	L	L	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L1	
		L	L	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L2	
		:						:						:						L3~L60	
		:						:						:							
		L	L	L	L	L	L	H	H	H	H	L	H	L	L	L	L	L	L	L61	
		L	L	L	L	L	L	H	H	H	H	H	L	L	L	L	L	L	L	L62	
	Green	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L	L	L	L	Green L63	
Gray Scale of Blue	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0	
	▲ Dark ↕ Light ▼	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L1	
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L	L2	
		:						:						:						L3~L60	
		:						:						:							
		L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	L	H	L61
		L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	L	L62
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	Blue L63
Gray Scale of White & Black	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0	
	▲ Dark ↕ Light ▼	L	L	L	L	L	H	L	L	L	L	L	H	L	L	L	L	L	H	L1	
		L	L	L	L	H	L	L	L	L	H	L	L	L	L	L	L	H	L	L2	
		:						:						:						L3~L60	
		:						:						:							
		H	H	H	H	L	H	H	H	H	L	H	L	H	H	H	H	L	H	L61	
		H	H	H	H	H	L	H	H	H	H	H	L	L	H	H	H	H	L	L62	
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	White L63	