



Features

· RoHS Compliant

- · p-Si construction with drivers on glass
- High luminance
- · Single CCFL, Sidelight type
- 202 pixels per inch equivalent to printed materials
- LVDS interface system
- XGA (1024 x 768 pixels color display)
- · Fast response time
- Applications: electronic books and personal digital-picture viewers

Mechanical Characteristics

Item	Specification	Unit
Outline Dimensions	261.0 (W) x 199.0 (H) x 5.0 max (D)	mm
Number of Pixels	1024 (W) x 768 (H)	pixels
Active Area	245.76 (W) x 184.32 (H)	mm
Pixel Pitch	0.24 (W) x 0.24 (H)	mm
Weight (approx.)	275	gram
Backlight	CCFL, Sidelight type (1 lamp)	_

Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Supply Voltage	V_{DD}	-0.3	4.0	٧
Supply voltage	V _{FL}	0	2.0	kV(rms)
FL Driving Frequency	f _{FL}	_	100	kHz
Input Signal Voltage	V _{IN}	-0.3	V _{DD} + 0.3	V
Operating Temperature	T _{op}	0	50	°C
Storage Temperature	T _{stg}	-20	60	°C
Humidity (Max. Wet bulb temp = 29°C)	_	10	90	%(RH)

ANDpSi121C505-HB

12.1" XGA Color p-Si TFT LCD Module

The ANDpSi121C505-HB is 1024 x 768 Color TFT display that utilizes new poly-silicon (p-Si) technology to provide a brighter, thinner and lighter display with high-resolution. The p-Si TFT technology allows the row and column LCD drivers to be fabricated directly on the LCD glass. This eliminates the need for discrete TAB drivers and also reduces the thickness, weight and overall size of the display. The 12.1" XGA resolution expands applications in such areas as electronics books and personal digital-picture viewers.

Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min.	Тур.	Max.	Unit
Supply Voltage	V _{DD}	3.0	3.3	3.6	V
V _{FL} : I _{FL} =6.0 mA(rms)	V _{FL}	-	(620)	_	V(rms)
FL Start Voltage (Ta = 0°C)	_	1200	_	(1400)	V(rms)
Differential Input	V _{ID}	100	_	600	mV
Common Mode Input Voltage	V _{CM}	1.0	_	2.4- (V _{ID})/2	V
Current	*1 (I _{DD})	_	(225)	-	mA
Consumption	*2 (I _{FL})	_	6.0	-	mA(rms)
*2 *3 Power Consumption I _{FL} =6.0 mA(rms)	Р	-	(4.46)	-	W

Notes

- *1:8 color bars pattern
- *2: Excepting the efficiency FL inverter

Optical Characteristics (Ta = 25°C)

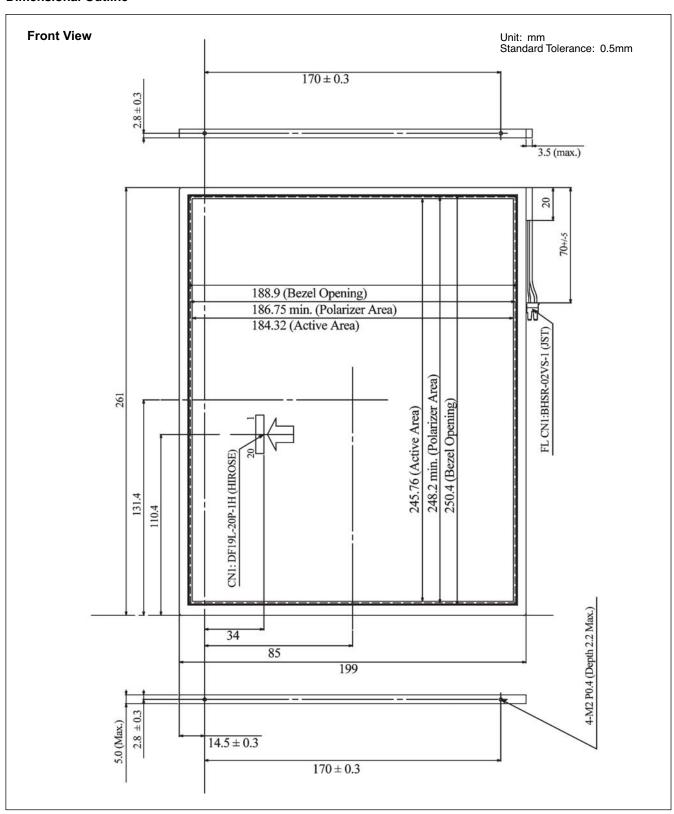
Item	Sym.	Condition	Min.	Тур.	Max.	Unit
Contrast Ratio	CR	$\phi = 0^{\circ}, \theta = 0^{\circ}$	100	250	1	ı
Response	t _{on}		-	-	50	ms
Time	me t _{off} φ	$\phi = 0^{\circ}, \theta = 0^{\circ}$	_	_	50	ms
Luminance	L	I _{FL} =(6.0)mA(rms)	_	(165)	-	cd/m ²

Product specifications contained herein may be changed without prior notice. It is therefore advisable to contact Purdy Electronics before proceeding with the design of equipment incorporating this product.

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Dimensional Outline

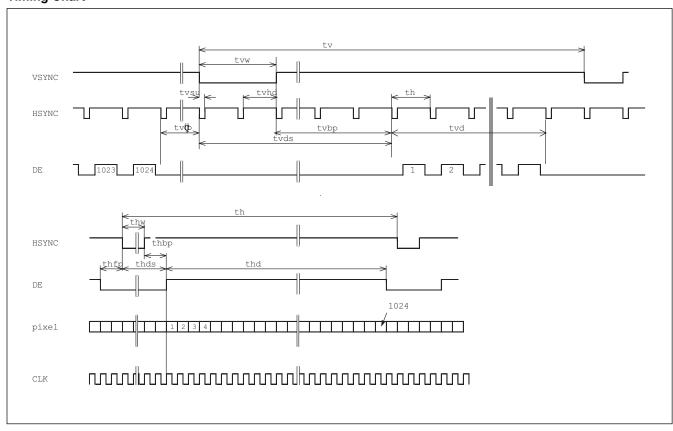




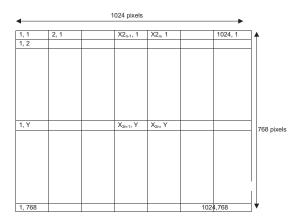
Timing Specifications

Item	Symbol	Min	Тур	Max	Unit
Horizontal Scanning Term	<i>t</i> h	1334 x tc	1344 x tc	_	clock
H-sync Pulse Width	<i>t</i> hw	4 x tc	136 x tc	_	clock
Horizontal Front Porch	<i>t</i> hfp	4 x tc	24 x tc	_	clock
Horizontal Back Porch	<i>t</i> hbp	24 x tc	160 x tc	_	clock
Horizontal Data Sync Period	<i>t</i> hds	32 x tc	296 x tc	_	clock
Horizontal Display Term	<i>t</i> hd	1024 x tc	1024 x tc	1024 x tc	clock
Frame Period	tv	778 x th	806 x th	860 x th	line
V-sync Pulse Width	<i>t</i> vw	2 x th	6 x <i>t</i> h	_	line
V-sync Set Up Time (to H-sync)	<i>t</i> vsu	8 x tc	_	_	clock
V-sync Hold Time	<i>t</i> vhd	(thbp+16) x tc	_	_	clock
Vertical Front Porch	<i>t</i> vfp	1 x th	3 x th	_	line
Vertical Back Porch	<i>t</i> vbp	2 x th	29 x th	_	line
Vertical Data Sync Period	tvds	8 x th	35 x th	_	line
Vertical Display Time	<i>t</i> vd	768 x th	768 x th	768 x th	line
Clock Period	tc	15.0	15.38	_	ns

Timing Chart

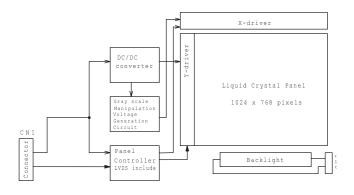






Recommended Inverter:

Block Diagram



1) Drivers are fabricated on the LCD glass

2) Connectors

CN1: DF19L-20P-1H / Hirose Electric Co., Ltd.

Mating Connector - DF19G-20S-1F / (FPC)

Mating Connector - DF19G-20S-1C / (Cable)

CN2: BHSR-02VS-1/Japan Solderless Terminal Mfg. Co., Ltd

Mating Connector - SM02B-BHSS-1 / JST

Connector Pin Assignment for Interface

CN1 Input Signal (DF19L-20P-1H / Hirose Electric Co., Ltd.)

Terminal No.	Symbol	Function
1	V_{DD}	Power Supply: +3.3V
2	V_{DD}	Power Supply: +3.3V
3	V _{SS}	Ground
4	V _{SS}	Ground
5	RxIN0-	Negative LVDS differential data input (R0-R5-G0)
6	RxIN0+	Positive LVDS differential data input (R0-R5-G0)
7	V _{SS}	GND
8	RxIN1-	Negative LVDS differential data input (G1-G5, B0-B1)
9	RxIN1+	Positive LVDS differential data input (G1-G5, B0-B1)
10	V _{SS}	GND
11	RxIN2-	Negative LVDS differential data input (B2-B5, HS, VS, DE)
12	RxIN2-	Positive LVDS differential data input (B2-B5, HS, VS, DE)
13	V _{SS}	GND
14	CLK-	Clock Signal (-)
15	CLK+	Clock Signal (+)
16	V _{SS}	GND
17	NC	
18	NC	
19	V _{SS}	GND
20	V _{SS}	GND

Note: Please connect GND pin to ground. Don't use it as no-connect or nor connection with high impedance.

CN2 CCFL Power Source (BHSR-02VS-1/Japan Solderless Terminal Mfg Co., Ltd.)

Terminal No.	Symbol	Function
1	V_{FLH}	CCFL Power Supply (High Voltage)
2	V _{FLL}	CCFL Power Supply (Low Voltage)



Note (2): 256K colors are displayed by the combinations of 18 data bits.

	Display	R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	В3	B2	B1	В0	Gray S	
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	_	
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	_	
	Green	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	_	
Basic	Lt. Blue	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	_	
Color	Red	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	_	
	Purple	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	_	
	Yellow	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	_	
	White	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н		_	
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L		L0
	Dark	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L		L1
Gray		L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L		L2
Scale					:						:						:			L3~l	∟60
of Red	\ \				:																
	,	Н	Н	Н	Н	L	Н	L	L	L	L	L	L	L	L	L	L	L	L		L61
	Light	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	L		L62
	Red	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	Red	L63
	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L		L0
	Dark	L	L	L	L	L	L	L	L	L	L	L	Н	L	L	L	L	L	L		L1
Gray		L	L	L	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L		L2
Scale					:						•			:						L3~L60	
of Green	븇	÷ :			:								•								
	'	L	L	L	L	L	L	Н	Н	Н	Н	L	Н	L	L	L	L	L	L		L61
	Light	L	L	L	L	L	L	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	_	L62
	Green	L	L ·	L ·	L	L	L ·	H	H	H	H	H	H	L	_ <u>L</u>	L ·	L	L	L	Green	L63
	Black	L	L	L	L	L	L	L	L	L	L	L	_L	L	L	L	L	L	L		L0
	Dark	L	L	L	L	L	L	L	L	L	L	L	L	L	L	_ <u>L</u>	L	H	H .		L1
Gray		L	L	L		L	L	L	L	L	L	L	L	L	L	L		П	L		L2
Scale of					:						•						•			L3~L60	
Blue	♦				<u> </u>									ļ			·				
		L	L	L	L	L	L	L	L	L ·		L	L	Н	H	H	Н	L	H		L61
	Light Blue	L	L	L	L	L -	L	L	L	L	L	L	L	Н	H	Н	H	H	L	Dive	L62
	Black	L	L L	L	L L	L L	L L	L L	L L	L L	L L	L L	L L	H L	H L	H L	H L	H L	H L	Blue	L63 L0
	Dark	L	L	L	L	L	 Н	L	L	L	L	L	 Н	L	L	L	L	L	<u></u> Н		L1
Gray	Daik	L	L	L	L	<u></u> Н		L	L	L	L	<u></u> Н	 	L	L	L	L	Н	 		L2
Scale	A				:							- ' '		-							
of White					· :			:											L3~l	_60	
&	♥	Н	Н				Н	Н	LI		Н		Н	Н	Н				LI		L61
Black	Light	Н	H	H	H	L H	H L	Н	H	H	H	L H	H L	Н	H	H	H	L H	H L		L61 L62
	White	Н	Н	 Н	 Н	 Н	 Н	Н	 Н	 Н	Н	 Н	<u></u> Н	Н	 Н	 Н	Н	 Н	<u></u> Н	White	L62
	VVIIILE	17	17	17	ıΠ	П	П	רו	ıΠ	17	17	П	П	רו	17	17	17	17	17	VVIIILE	LUJ