

General Specification

Item	Dimension	Unit
Number of Characters	100x 32 Dots	_
Module dimension	98.0 x 60.0 x 10.0(MAX)	mm
View area	77.0x25.20	mm
Active area	58.95 x 19.15	mm
Dot size	0.54 x 0.55	mm
Dot pitch	0.59x 0.60	mm
LCD type	OLED , Yellow	,
Duty	1/16	

Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit	Notes
Operating Temperature	T _{OP}	-40	+80	$^{\circ}\!\mathbb{C}$	
Storage Temperature	T _{ST}	-40	+80	$^{\circ}\!\mathbb{C}$	
Input Voltage	Vı	-0.3	VDD	V	
Supply Voltage For Logic	VDD-V _{SS}	-0.3	5.3	V	



Electrical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	VDD-VSS	_	3	5.0	5.3	V
Input High Volt.	VIH	_	0.9 VDD	_	VDD	V
Input Low Volt.	VIL	_	GND	_	0.1VDD	V
Output High Volt.	VOH	IOH=-0.5mA	0.8 VDD	_	VDD	V
Output Low Volt.	VOL	IOL=0.5mA	GND	_	0.2 VDD	V
Supply Current	IDD	VDD=5V	_	43	_	mA
CIEx(Yellow)		x,y(CIE1931)	0.44	0.48	0.52	
CIEy(Yellow)		x,y(CIE1931)	0.46	0.50	0.54	

Optical Characteristics

Item	Symbol	Condition	Min	Тур	Max	Unit
View Angle	(V)θ		160			deg
View Angle	(Η)φ		160			deg
Contrast Ratio	CR	Dark	2000:1		_	_
Dannana Tima	T rise	_		10		μs
Response Time	T fall	_		10		μs
Supply Voltage For Logi	c 5V	With polarizer		90		nits
50% Check Board Brightı	ness	215mW(5V*43mA)				Note1
Supply Voltage For Logi	c 3V	With polarizer		60		nits
50% Checkboard Bright	ness					

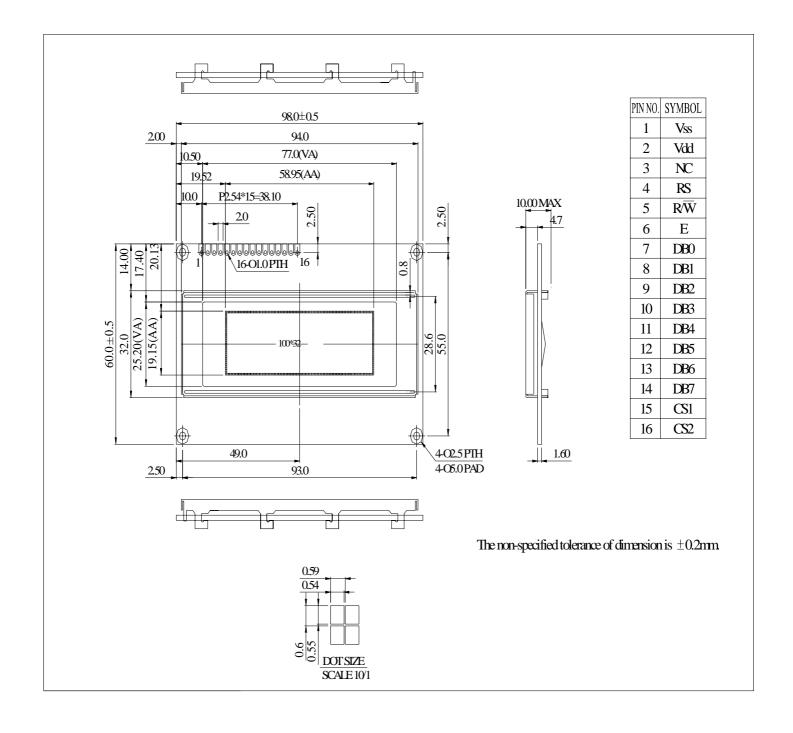


Interface Pin Function

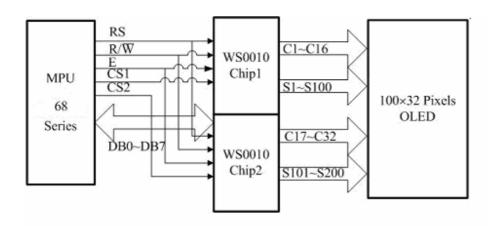
Pin No.	Symbol	Level	Description
1	VSS	0V	Ground
2	VDD	5.0V	Supply Voltage for logic
3	NC	_	
4	RS	H/L	H: DATA, L: Instruction code
5	R/W	H/L	H: Read(MPU→Module) L: Write(MPU→Module)
6	E	H,H→L	Chip enable signal
7	DB0	H/L	Data bit 0
8	DB1	H/L	Data bit 1
9	DB2	H/L	Data bit 2
10	DB3	H/L	Data bit 3
11	DB4	H/L	Data bit 4
12	DB5	H/L	Data bit 5
13	DB6	H/L	Data bit 6
14	DB7	H/L	Data bit 7
15	CS1	_	Chip1 select input pin
16	CS2	_	Chip2 select input pin



Counter Drawing & Block Diagram







Ad	Address Format					D	B7	D	B6	DB5	DB4	D	ВЗ		B2	DI	31	DE	30			
GXA(Graphic X-axis Address			c X-axis Address			GXA(Graphic X-axis Address					Α[DD6	ADD5	ADD4	ΑE)D3	AD	D2	AD	D1	AD	D0
GYA(Gra	phic Y	-axis	Ad	dre	SS		0		1	0	0		0		0	()	CG	A0			
		1		2	2	3			1.2			9	7	9	8	9	9	10	00			
CS1=0 CS2=1	CGA=0	GXA=10000000	GYA=010000000	GXA=10000001	GYA=01000000	GXA=10000010	GYA=01000000	GXA=10000011	GYA=01000000			GXA=11100000	GYA=01000000	GXA=11100001	GYA=010000000	GXA=11100010	GYA=010000000	GXA=11100011	GYA=01000000			
C32-1	CGA=1		GYA=01000001	GXA=10000001 GXA=10000001	GYA=01000001		GYA=010000001	GXA=10000011	GYA=01000001			GXA=11100000	GYA=01000001	GXA=11100001	GYA=010000001	GXA=11100010 GXA=11100010	GYA=01000001 GYA=01000000	GXA=11100011 GXA=11100011	GYA=01000001 GYA=01000000			
		1		2	2	3			1			9	7	9	8	9	9	10	00			
CS1=1 CS2=0	CGA=0	GXA=10000000 GXA=10000000	GYA=01000001 GYA=01000000	GXA=10000001 GXA=10000001	GYA=01000001 GYA=01000000	GXA=10000010 GXA=10000010	GYA=01000001 GYA=01000000	GXA=10000011	GYA=01000000			GXA=11100000 GXA=11100000	GYA=01000000	GXA=11100001	GYA=01000001 GYA=01000000	GXA=11100010 GXA=11100010	GYA=01000001 GYA=01000000	1000 0000	GYA=01000001 GYA=01000000			
CS2=0	CGA=1	GXA=10000000	GYA=01000001	GXA=10000001	GYA=01000001	GXA=10000010	GYA=01000001	GXA=10000011 GXA=10000011	GYA=01000001			GXA=11100000	GYA=01000001	GXA=11100001 GXA=11100001	GYA=01000001	GXA=11100010	GYA=01000001	GXA=11100011 GXA=11100011	GYA=01000001			



OLED Lifetime

ITEM	Conditions	Тур	Remark
Operating Life Time	Ta=25°C /Initial 50% checkboard brightness 90nits	100,000 Hrs	Note

Notes:

- 1. Simulation pattern for operation test: interchanging with 50% checkboard The brightness decay does not exceed 50%
- 2. You can use the display off mode to make long life.
- 3. The average operating lifetime at room temperature is estimated by the accelerated operation at high temperature conditions.



Reliability

Content of Reliability Test

Environmenta	l Test				
Test Item	Content of Test	Test Condition	Applicable Standard		
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 240hrs			
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	80°ℂ 240hrs			
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-40°C 240hrs			
High Temperature/ Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time.	60°C,90%RH 240hrs			
Temperature Cycle	Endurance test applying the low and high temperature cycle. -40°C	-40°C/80°C 100 cycles			
Mechanical Te	st				
Vibration test	Endurance test applying the vibration during transportation and using.	10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hrs			
Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G Half sign wave 11 msedc 3 times of each direction			
Atmospheric pressure test	Endurance test applying the atmospheric pressure during transportation by air.	115mbar 40hrs			
Others					
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5kΩ CS=100pF 1 time			

^{***}Supply voltage for logic system=5V. Supply voltage for LCD system =Operating voltage at 25 $^{\circ}\mathrm{C}$