

## AND064VT8-HB-LED-KIT

# High Bright 6.4" TFT LCD LCD Color Module

The AND064VT8-HB-LED-KIT is a compact full color TFT LCD module, that is suitable for computer peripheral, industrial meter, image communication and multi-media. This device consists of an amorphous silicon TFT LCD panel with 24 LED B/L that has  $640 \times 480$  pixels on a 6.4 inch diagonal screen.

#### **Features**

- Compatible with VGA-480, VGA-400, VGA-350 mode
- Support the DENB mode
- · Pixel in stripe configuration
- · Slim and compact
- Display colors: 262,144 colors
- · Image reversion: Up/Down and Left/Right
- TTL transmission interface
- · RoHS complliant

#### **Mechanical Characteristics**

Item	Standard Value	Unit
Screen size	6.4 inch (diagonal)	inch
Display Format	640 x (R, G, B) x 480	dot
Active Area	129.6 (H) x 97.44 (V)	mm
Outline Dimensions	175.4 (W) x 126.9 (H) x 12.5 (D) (Typ.)	mm
Pixel Pitch	0.2025 (H) x 0.203 (V)	mm
Pixel Configuration	Stripe	-
Surface Treatment	Anti-Glare and Wide View film	-
Display Mode	Normally White	-
Backlight	Middle Power 24 * 2-LED	-
Weight	321 ± 15	grams
Gray scale inversion direction	6 o'clock	_

#### Absolute Maximum Ratings: GND = 0V, Ta = 25°C

Item	Symbol	Absolute Ma	ximum Rating	Unit	Remarks
		Min.	Max.		
Supply Voltage	VCC	-0.3	+7.0	V	
Input Signals Voltage	V-sign	-0.3	VCC +0.3	V	Note 1
Storage Temperature	Tst	-30	+80	°C	
Operation Temperature	Тор	-30	+80	°C	

Note 1: Input signals include CLK, HSYNC, VSYNC, DENB, R[0:5], G[0:5] and B[0:5]

Product specifications contained herein may be changed without prior notice.

 $\Box$ 



## Electrical Characteristics - Recommended Operation Condition for TFT-LCD Panel (Ta = 25°C)

Item	Symbol	Specifications		Unit	Remark	
		Min.	Тур.	Max.		
Supply Voltage	VCC	4.75	5.0	5.25	V	
Current Dissipation	ICC	_	100	120	mA	VCC=+5V
Power Consumption	PCC	_	0.5	0.63	W	
Supply Input Ripple Voltage	VCCRP	_	_	0.1	Vp-p	VCC=+5V
Input Signals Voltage (High)	VIH	+2.6	_	_	V	
Input Signals Voltage (Low)	VIL	_	_	+0.5	V	

#### Recommended Operating Conditions for Backlight (Ta = 25 °C)

Item	Symbol	Min.	Тур.	Max.	Units	Remarks
Supply Voltage for LED Backlight	VLED	_	_	(11)	V	Note 1
Supply Current for LED Backlight	ILED	_	17.5	_	mA	Note 2
Backlight Power Consumption	PLED	-	-	3.08	W	Note 1, 3

Note 1: ILED = 17.5 mA, constant current

Note 2: The LED driving condition is defined for each LED module. (3 LED Serial)

Input current = 17.5 mA \* 8 = 140 mA

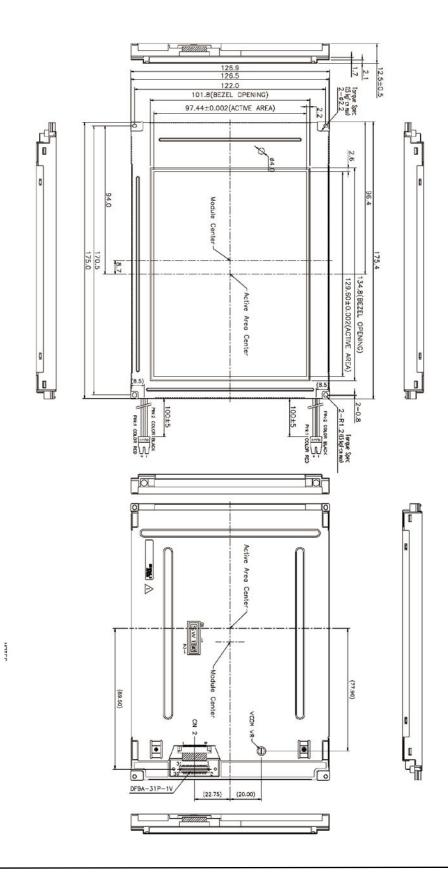
Note 3: PLED = (VLD-1 \* ILED-1 + VLED-2 \* ILED-2 .... + VLED-7 + ILED-7 + VLED-8) \*2 (light bar)

## Optical Specifications (Ta = 25 °C)

Item		Symbol	Remarks		Specifications		Units
		·		Min.	Тур.	Max.	
	Horizontal	θ 21, θ 22		55	60	-	
Viewing Angle	Vertical	θ 12	CR ≥ 10	35	40	-	deg
		θ 11		50	55	-	
Contrast R Luminance when L Luminance when	CD is white	CR	At optimized viewing angle	200	400	1	-
Response Time	Rise	Tr	$\Theta = 0_o$	-	15	30	ms
	Fall	Tf	$\Theta = 0_o$	-	25	50	
Brightne	ss	LUM	θ = 0 <sub>o</sub>	350	400	-	cd/m <sup>2</sup>
Uniformi	ty	U	θ = 0 <sub>o</sub>	75	80	-	%
Cross Ta	ılk	-	$\Theta = 0_o$	-	_	3.5	%
		Χ		0.27	0.31	0.35	
White Chhror	naticity	Υ	$\Theta = 0_{\rm o}$	0.29	0.33	0.37	
LED Life Time	Ta = 25 °C		+25 °C	20,000	30,000	_	hrs



## **Dimensional Outline:**



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Timing Parameters - AC Electrical Characteristics (VCC = VDD1 = 3.3V, VDD2 = 10V, GND = VSS1 = VSS2 = 0V, Ta = 25°C)

	Item	Symbol	Format	Min.	Тур.	Max.	Unit
CLK	Frequency	Fc=1/tc	All	-	25.175	-	MHz
	(tc is the period of sampling click. In case of low-fre- quency, the imae-flicker may occur.)	tc	All	_	40	-	ns
	Period	Нр	All	_	31.778	-	us
				-	800	-	tc
	Display Period	Hd	All	-	640	-	tc
Hsync	Pulse Width	Hpw	All	12	96	139	tc
	Back-porch	Hbp	All	12	46	139	tc
	Front-porch	Hfp	All	-	18	_	tc
	Hpw + Hbp		All	136	142	151	tc
	Hsync-CLK	Hhc	All	10	_	Tc-10	ns
	Vsync-Hsync	Hvh	All	0	0	200	tc
			480	_	16.8	-	ms
				515	525	800	Нр
	Period	Vp	400	_	14.3	-	ms
	i enou	Vρ		446	449	480	Нр
			350	_	14.3	-	ms Hp
				447	449	510	Нр
			480	_	480	-	
	Display Period	Vdp	400	-	400	-	Нр
			350	_	350	-	
\/avma	Pulse Width	Vpw	All	2	2	35	Нр
Vsync			480	2	33	35	
	Back-porch	Vbp	400	2	35	38	Нр
			350	2	60	63	
			480	1	10	-	
	Front-porch	Vfp	400	1	12	-	Нр
			350	1	37	_	
			480	31	35	38	
	Vpw + Vbp		400	33	37	40	Нр
			350	58	62	65	
Data	CLK-DATA	Dcd	All	10	_	_	ns
	DATA-CLK	Ddc	All	10	_	_	ns
	Horizontal scanning period	T1	All	780	800	900	tc
	Horizontal display period	T2	All	_	640	_	tc
DENB	Vertical display period	T3	All	_	480	_	T1
	Frame cycling period	T4	All	515	525	800	T1



## **Input/Output Terminals**

## TFT-LCD Panel Driving - LCD Module Connector (Reference)-DF9A-31P-1V

Pin No.	Symbol	Function
1	GND	Ground (0V)
2	CLK	Clock Signal for Sampling IMage DIgital Data
3	HSYNC	Horizontal Synchronous Signal
4	VSYNC	Vertical Synchronous Signal
5	GND	Ground (0V)
6	R0	Red Image Data Signal (LSB)
7	R1	Red Image Data Signal
8	R2	Red Image Data Signal
9	R3	Red Image Data Signal
10	R4	Red Image Data Signal
11	R5	Red Image Data Signal (MSB)
12	GND	Ground (0V)
13	G0	Green Image Data Signal (LSB)
14	G1	Green Image Data Signal
15	G2	Green Image Data Signal
16	G3	Green Image Data Signal
17	G4	Green Image Data Signal
18	G5	Green Image Data Signal (MSB)
19	GND	Ground (0V)
20	В0	Blue Image Data Signal (LSB)
21	B1	Blue Image Data Signal
22	B2	Blue Image Data Signal
23	B3	Blue Image Data Signal
24	B4	Blue Image Data Signal
25	B5	Blue Image Data Signal (MSB)
26	GND	Ground (0V)
27	DENB	Enable
28	VCC	DC +5.0 V Power Supply
29	VCC	DC +5.0 V Power Supply
30	R/L	Horizontal Image Shift direction Select Signal
31	U/D	Vertical Image Shift Direction Select Signal

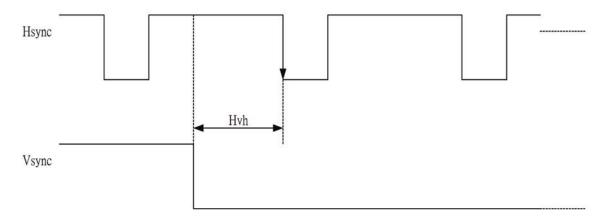
## Backlight Driving Connector: JST BHSR-02VS-1, 2 Pins

Pin No.	Symbol	Function	Remarks
1	+	Input Terminal (Anode)	Red
2	-	Input Terminal (Cathode)	Black

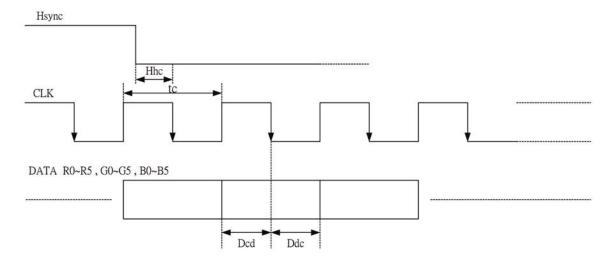


#### Display Time Range

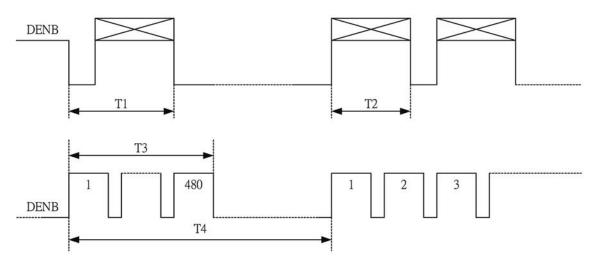
#### (1) Vertical Timing:



## (2) Horizontal Timing:



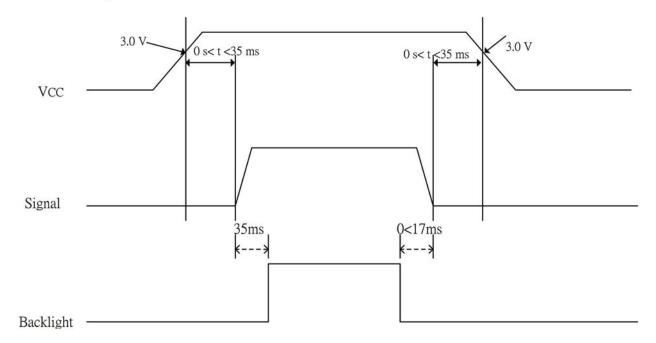
#### (3) DENB Timing:





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## Power On Sequence



- 1. The supply voltage for input signals should be same as V<sub>CC</sub>.
- 2. When the power is off, please keep whole signals (Hsync, Vsync, CLK, Data) low level or high impedance



# PC-TFT-104

#### **Features**

- RoHS Compliant
- Used for AND-TFT-LCD displays
- · Input power: +12V DC
- Input Source: VGA (640 x 480) & NTSC Composite
- Operating Temperature: 0 °C ~ 60 °C
  Storage Temperature: -20 °C ~ 70 ° C

PC-TFT-104 has universal LVDS interface for 5.6", 8.4", 10.4" and 12.1" displays. All these displays are well suitable for security systems, video games, door phones, video phones and portable TVs as well as industrial display applications.

#### **Electrical Characteristics**

Symbol	I//O	Conditions	Min.	Тур.	Max.	Unit
V <sub>IN</sub>		DC (+)	10	12	14	V
I <sub>IN</sub>	1	DC (+12V)	1.02	1.05	1.08	Α
P <sub>IN</sub>		DC (+12V)	12.24	12.60	12.96	W

#### CON 1: External Signal Input Terminal (14 pin) - MOLEX 53261-1471 or Compatible

Pin No.	Symbol	I/O	Conditions
1	Vin	Input	Vin
2	GND	Input	Ground
3	GND	Input	Ground
4	AV1	Input	Composite Video 1
5	NC	Input	No Connect
6	AV2	Input	Compiste Video 2
7	GND	Input	Ground
8	R	Input	VGA Red Signal Input
9	G	Input	VGA Green Signal Input
10	В	Input	VGA Blue Signal Input
11	GND	Input	Ground
12	H-SYNC	Input	HSYNC Input for VGA
13	V-SYNC	Input	VSYNC Input for VGA
14	GND	Input	Ground



#### CON2: Connector for Keypad (10 pin) - Type: MOLEX 53261-1071 or Compatible

Pin	Definition	Pin	Description	Pin	Description
1	LED-RED	5	+3.3 V	9	UP
2	LED-GREEN	6	POWER	10	ENTER
3	SENSOR	7	MENU		
4	GND	8	DOWN		

#### CON4: Connector for LCD Module (LVDS/34 pin) - Type: MOLEX 87758-34 or Compatible

Pin	Symbol	Description		
1	INO-	LVDS Transceiver SIgnal Channel 0		
2	INO+	LVDS Transceiver Signal Channel 0		
3	IN1-	LVDS Transceiver Signal Channel 1		
4	IN1+	LVDS Transceiver Signal Channel 1		
5	IN2-	LVDS Transceiver SIgnal Channel 2		
6	IN2+	LVDS Transceiver Signal Channel 2		
7	GND	Ground		
8	CLK-	LVDS Transceiver Signal Clock		
9	CLK+	LVDS Transceiver Signal Clock		
10	NC	No Connection		
11	NC	No Connection		
12	NC	No Connection		
13	NC	No Connection		
14	GND	Ground		
15	NC	No Connection		
16	NC	No Connection		
17	GND	Ground		

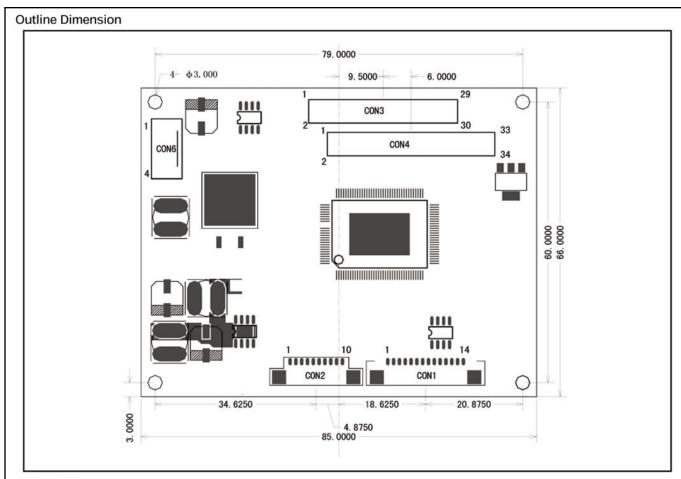
Pin	Symbol	Description No Connection				
18	NC					
19	NC	No Connection  No Connection				
20	NC					
21	NC					
22	NC	No Connection				
23	NC	No Connection				
24	GND	Ground				
25	GND	Ground				
26	GND	Ground				
27	GND	Ground				
28	VCC	+3.3V Power Supply				
29	VCC	+3.3V Power Supply				
30	VCC	+3.3V Power Supply				
31	VCC	+3.3V Power Supply				
32	VCC	+3.3V Power Supply				
33	NC	No Connection				
34	NC	No Connection				

#### CON6: Connector for LCD Backlight Driver Board (4 pin) - Type: JST B4B-XH-A or Compatible

Pin	Definition	Pin	Description	Pin	Description	Pin	Definition	
1	+12V	2	GND	3	ON/OFF*	4	NC	7

<sup>\*</sup> Backligth control switch (ON: 5V, OFF: 0V)





Description:

Outline: 85.0 x 66.0 x 10.1 mm

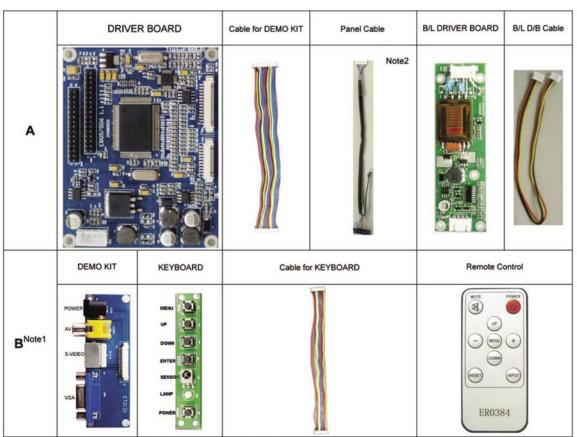
Top Layer Height (Max): 7.0 mm

Bottom Layer Height (Max): 1.9 mm

Board Thickness: 1.2 mm Screws: • 3.0 mm x 4







Note1: The parts in Column B are optional.

Note2: There is a white dot on the panel cable to indicate Pin1, and this pin is connected to Pin1 of Driver Board. See following picture:

