



AND-TFT-5RQ 320 x 234 Pixels LCD Color Monitor

The AND-TFT-5RQ is a compact full color TFT LCD module, that is suitable for applications such as a portable television (PAL and NTSC) and a display for monitors. The display features 2 analog RBG inputs, allowing the user to switch between video sources. This device con-sists of a twisted nematic (TN) liquid crystal cell, that incor-porates a TFT-array that has 320 x 234 pixels on a 5-inch diagonal screen, X and Y drivers, an LSI controller, and a built-in CCFL backlight and inverter.

Features

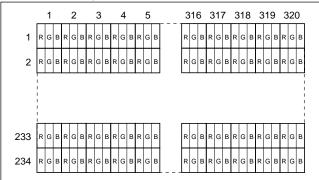
- Analog RGB (0.7Vp-p) Input
- 5 inch (13 cm) diagonal screen
- High brightness CCFL backlight (300 Nits)
- Built-in CCFL inverter
- Allows 2 RGB Inputs

- Operating temperature range -0 to 50° C
- Storage temperature range -40 to 85° C
- 7.5V single power supply
- Low specular reflection.

Mechanical Characteristics

Item	Specification	Unit
Screen Size	5 inch (13 cm) diagonal	
Outline Dimensions	120.5 typ. (W) x 89.6 (H) x 20.0 max. (D)	mm
Active Area	101.6 (W) x 74.7 (H)	mm
Drive System	a-Si TFT Active matrix, A line at a time Non-Interlace Drive	
Pixel Number (RGB trio)	320 (W) x 234 (H)	-
Sub Pixel No.	960 (W) x 234 (H)	-
Sub Pixel Arrangement	RGB stripe	-
Pixel Pitch	0.318 (W) x 0.318 (H)	mm

Sub Pixel Arrangement



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Absolute Maximum Rating

Item		Symbol	Conditions	Absolute Max	Unit		
		Symbol	Conditions	Min.	Max.	Oilit	
Supply	for Video Circuit		VCC	Ta = 25°C	VSS -0.2	11.0	V
Voltage	for Backli	ght Inverter	VBL	Ta = 25°C	VSS -0.2	9.0	V
	Video	Analog RGB	VR, VG, VB		-	1.5	Vp-p
Input Signal	Composit	e sync.	CSYNC	Ta = 25°C, VCC = 7.5V VBL = 7.5V	-	1.5	Vp-p
Voltage	Others		BRT, VSW, U/D, L/R OSR, OSG, OSB		VSS -0.2	VDD +0.2	V
Operating T	Temperature		Тор	-	0	50	°C
Storage Temperature		Tstg	_	-40	85	°C	
Humidity (N	Humidity (No condensation of water)		-	_	10	90	% RH

Note: Operating temperature range of the TFT-LCD module surface is -30 to +85°C. However, heat from the backlight will narrow the range.



Electrical Specification

Item		Symbol Conditions	Specifications			Units	
		Symbol Conditions		Min.	Тур.	Max.	Ullits
Current Consumption	for Video Circuit, Backlight Inverter	ICC, IBL	DIM = Max.	-	0.56	_	А
Output Voltage	•	VDD	VCC = VBL = 7.5V	-	5.0	-	V
Output Current		IDD	VCC = VBL = 7.5V	-	2.0	20.0	mA
Vertical display start		Vpos	NTSC (59.94Hz)	-	22	-	Н
start position			PAL (50.00Hz)	-	27	-	Н
Vertical display term		Vdis	NTSC (59.94Hz)	-	234	-	Н
			PAL (50.00Hz)	_	281	-	Н
Horizontal display		I I	NTSC (15.73kHz)	_	9.35	-	μs
start position		Hpos	PAL (15.63kHz)	-	10.14	-	μs
Horizontal display term		Hdis	NTSC (15.73kHz)	-	50.01	-	μs
			PAL (15.63kHz)	-	50.69	-	μs

(Ta = RT, VSS = 0V)

Recommended Operating Conditions

Item		Symbol	Conditions	Specifications			Unit	
		Symbol		Min.	Тур.	Max.	Oilit	
Supply	for Video Circuit		VCC	-	7.0	7.5	8.0	V
Voltage	for Backlight Inverter		VBL	-	7.0	7.5	8.0	V
	Video	Analog RGB	VR, VG, VB	75Ω	-	0.7	-	Vp-p
Input	Composite sync.		CSYNC	75Ω	-	1.0	-	Vp-p
Signal Voltage	Others		BRT, VSW, U/D, L/R, OSR, OSG, OSB	Level "H"	4.6	-	VDD	V
Voltago				Level "L"	VSS	-	0.4	V
Frame Frequency	France France Control of the Control		fVDN	NTSC	58	59.94	62	Hz
Frame Frequency	1		fHDN	11130	15.2 15.7 16.2		16.2	kHz
Color Sub-carrier Frequency		fCOLOR	NTSC	3.579395	3.579545	3.579695	MHz	
Color Sub-carrier Amplitude		VCOLOR	NTSC	40	-	-	mV	

Optical Specifications

Item	Symbol Con	Conditions	Specifications			Unit
item	Syllibol	Conditions	Min.	Тур.	Max.	Oilit
Contrast Ratio	CR	RGB = 0/0.7V	30	100	-	-
Maximum Luminance	LUM	Dim = 3.90V, RGB = 0.7V	220	300	-	cd/m ²
Viewing Angle	φ L/ φ R	RGB = 0/0.7V	-	45/45	-	deg
Viewing Angle	φ U/ φ D	RGB = 0/0.7V	-	30/15	-	deg

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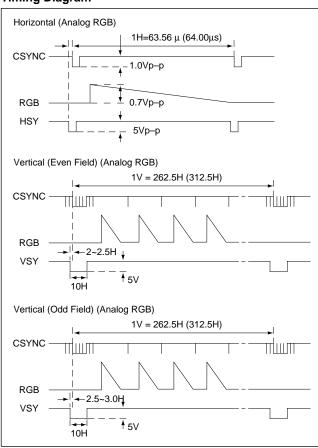
Interface Pin Assignment (Video Signals) Connector: 52207-1490 (Molex)

Pin No.	Symbol	Funct	Input/ Output	
1	NC	No Connect		-
2	GND	Ground (0V)		1
3	SYNC1	Composite Sync.Input 1	Negative 1.0Vp-p, 75Ω	Input
4	R1	Video Input R1	(0.7Vp-p, 75Ω)	Input
5	G1	Video Input G1	(0.7Vp-p, 75Ω)	Input
6	B1	Video Input G1	(0.7Vp-p, 75Ω)	Input
7	GND	Ground (0V)		-
8	HSY	Horizontal Sync. Output	(Negative, C-MOS)	Output
9	VSY	Vertical Sync. Output	(Negative, C-MOS)	Output
10	SYNC2	Composite Sync.Input 2	Negative 1.0Vp-p, 75Ω	-
11	R2	Video Input R2	(0.7Vp-p, 75Ω)	-
12	G2	Video Input G2	(0.7Vp-p, 75Ω)	-
13	B2	Video Input G2	(0.7Vp-p, 75Ω)	
14	GND	Ground (0V)		

Interface Pin Assignment (Power and Control Signals) Connector: & 52207-1890 (Molex)

Pin No.	Symbol	Function		
1	VCC1	Dower Supply 7 F for Backlight	Input	
2	VCCI	Power Supply 7.5 for Backlight	IIIput	
3	GND	Cround (OV) for Docklight		
4	GND	Ground (0V) for Backlight	_	
5	VCC2	Power Supply 7.5V for Video Circuit	Input	
6	GND	Ground (0V) for Video Circuit	-	
7	NC	No Connect	-	
8	SSW	Sync. Signal Selection (0V: RGB1, 5V: RGB2)	Input	
9	GND	Ground (0V)	-	
10	VDD	5V Output for Control Terminals	Output	
11	Ī/R	Scanning Direction 0V: Left to Right Switch 5V: Right to Left	Input	
12	Ū/D	Scanning Direction 0V: Up to Down Switch 5V: Down to Up	Input	
13	GND	Ground (0V)	-	
14	BRT	Brightness Control (0V to 5V)	Input	
15	DIM	Dimmer Control	Input	
16	NC	No Connect	-	
17	NC	No Connect	-	
18	NC	No Connect	-	
19	VSW	Video Signal Selection (0V:RGB1, 5V:RGB2)		
20	N/P	NTSC/PAL Selection (OV:NTSC, 5V:PAL)		

Timing Diagram





Dimensional Outline

