



#### 2. General Specification

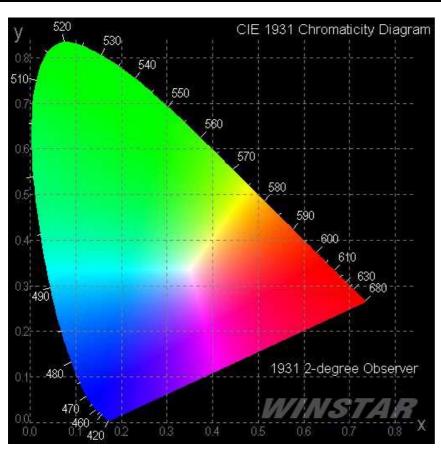
Item	Dimension	Unit
Number of Characters	16 characters x 2 Lines	-
Module dimension	80.0 x 36.0 x 10.0(MAX)	mm
View area	66.0 x 16.0	mm
Active area	56.95 x 11.85	mm
Dot size	0.55 x 0.65	mm
Dot pitch	0.60x 0.70	mm
Character size	2.95 x 5.55	mm
Character pitch	3.6 x 6.3	mm
LCD type	OLED , Blue	
Duty	1/16	

## 3. Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit	Notes
Operating Temperature	T <sub>OP</sub>	-40	+80	$^{\circ}$ C	
Storage Temperature	T <sub>ST</sub>	-40	+80	$^{\circ}\! \mathbb{C}$	
Input Voltage	Vı	-0.3	VDD	V	
Supply Voltage For Logic	VDD-V <sub>SS</sub>	-0.3	5.3	V	

## **4. Electrical Characteristics**

Item	Symbol	Condition	Min	Тур	Max	Unit
Supply Voltage For Logic	VDD-VSS	_	3.0	5.0	5.3	V
Input High Volt.	VIH	_	0.9 VDD	_	VDD	V
Input Low Volt.	VIL	_	GND	_	0.1VDD	V
Output High Volt.	VOH	IOH=-0.5mA	0.8 VDD	_	VDD	V
Output Low Volt.	VOL	IOL=0.5mA	GND	_	0.2 VDD	V
Supply Current	IDD	VDD=5V	_	30	_	mA
CIEx(Blue)		x,y(CIE1931)	0.12	0.16	0.20	
CIEy(Blue)		x,y(CIE1931)	0.19	0.23	0.27	



# 5. Optical Characteristics

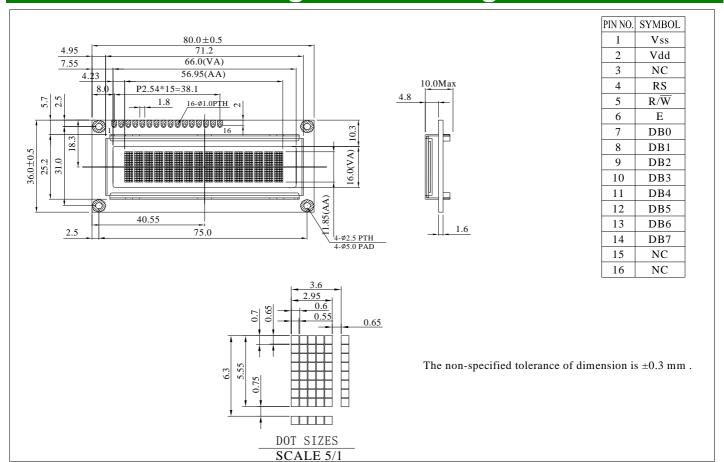
Item	Symbol	Condition	Min	Тур	Max	Unit
\('\)	(V)θ		160			deg
View Angle	(Η)φ		160			deg
Contrast Ratio	CR	Dark	2000:1		_	_
D Time	T rise	_		10		μs
Response Time	T fall	_		10		μs
Supply Voltage For L	ogic 5V	With polarizer		80		Nits
50% CheckBoard Bri	ghtness	150mW(5V*30mA)				Note1
Supply Voltage For L	ogic 3V	With polarizer		50		nits
50% CheckBoard Bri	ghtness					

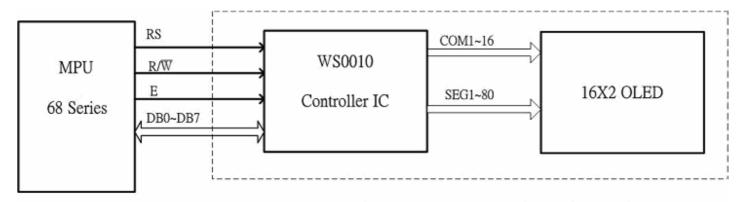
Notes: 1. When random texts pattern is running, averagely, at any instance, about 1/2 of pixels will be on. 2. You can to use the display off mode to make long life.

# 6. Interface Pin Function

Pin No.	Symbol	Level	Description
1	VSS	0V	Ground
2	VDD	5.0V	Supply Voltage for logic
3	NC	_	
4	RS	H/L	H: DATA, L: Instruction code
5	R/W	H/L	H: Read(MPU→Module) L: Write(MPU→Module)
6	E	H,H→L	Chip enable signal
7	DB0	H/L	Data bit 0
8	DB1	H/L	Data bit 1
9	DB2	H/L	Data bit 2
10	DB3	H/L	Data bit 3
11	DB4	H/L	Data bit 4
12	DB5	H/L	Data bit 5
13	DB6	H/L	Data bit 6
14	DB7	H/L	Data bit 7
15	NC	_	
16	NC		

### 7. Counter Drawing & Block Diagram





P	Address	Forma	at		DE	37	DE	36	DE	35	DE	34	DB	3	DB2	DB1	DB0
CA	(Chara	cter Ad	dress)		. 1	1	AD	D6	ΑD	D5	ΑD	D4	ADI	D3	ADD2	ADD1	ADD0
1	2	3	4	į		;;;;		1	3	1	4	1	5	1	6		
CA10000000	CA10000001	CA10000010	CA10000011	3.3		***	,,,	CA10001100		C A 10001101	101100120	CA10001110		CA10001111			

CA11001100

CA11000000

CA11000010

CA11000001

CA11000011

CA11001101

CA11001110

CA11001111

#### 8. OLED Lifetime

ITEM	Conditions	Тур	Remark
Operating Life Time	Ta=25°C /Initial 50% checkboard brightness 80nits	50,000 Hrs	Note

#### Notes:

- 1. Simulation pattern for operation test: interchanging with 50% checkboard The brightness decay does not exceed 50%.
- 2. You can use the display off mode to make long life.
- 3. The average operating lifetime at room temperature is estimated by the accelerated operation at high temperature conditions.

### 9. Reliability

**Content of Reliability Test** 

Environmenta	ll Test				
Test Item	Content of Test	Test Condition	Applicable Standard		
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 240hrs			
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	80℃ 240hrs			
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-40℃ 240hrs			
High Temperature/ Humidity Storage	Endurance test applying the high temperature and high humidity storage for a long time.	60°C,90%RH 240hrs			
Temperature Cycle	Endurance test applying the low and high temperature cycle.  -40°C	-40°C/80°C 100 cycles			
Mechanical Te	st				
Vibration test	Endurance test applying the vibration during transportation and using.	10~22Hz→1.5mmp-p 22~500Hz→1.5G Total 0.5hrs			
Shock test	Constructional and mechanical endurance test applying the shock during transportation.	50G Half sign wave 11 msedc 3 times of each direction			
Atmospheric pressure test	Endurance test applying the atmospheric pressure during transportation by air.	115mbar 40hrs			
Others					
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5kΩ CS=100pF 1 time			

<sup>\*\*\*</sup>Supply voltage for logic system=5V. Supply voltage for LCD system =Operating voltage at 25°C

#### Test and measurement conditions

1. All measurements shall not be started until the specimens attain to temperature stability. After the completion of the described reliability test, the samples were left at room