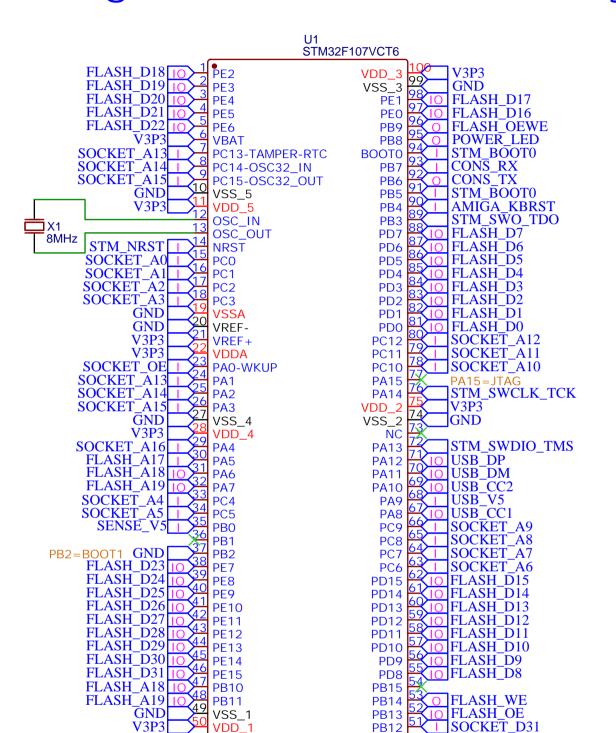
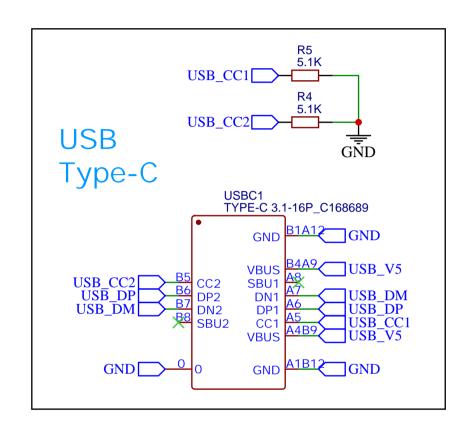
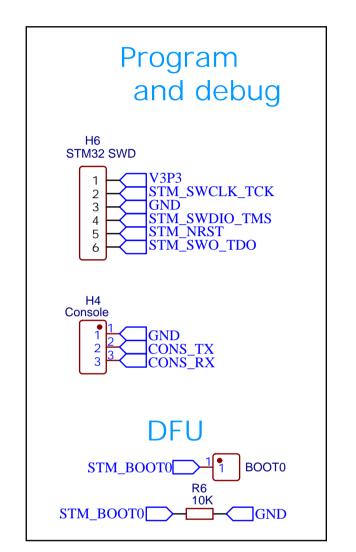
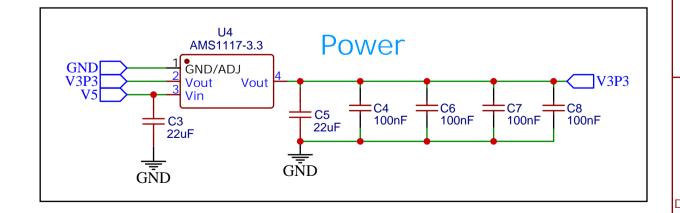
Amiga KickSmash 32 - In-system smart flash EEPROM



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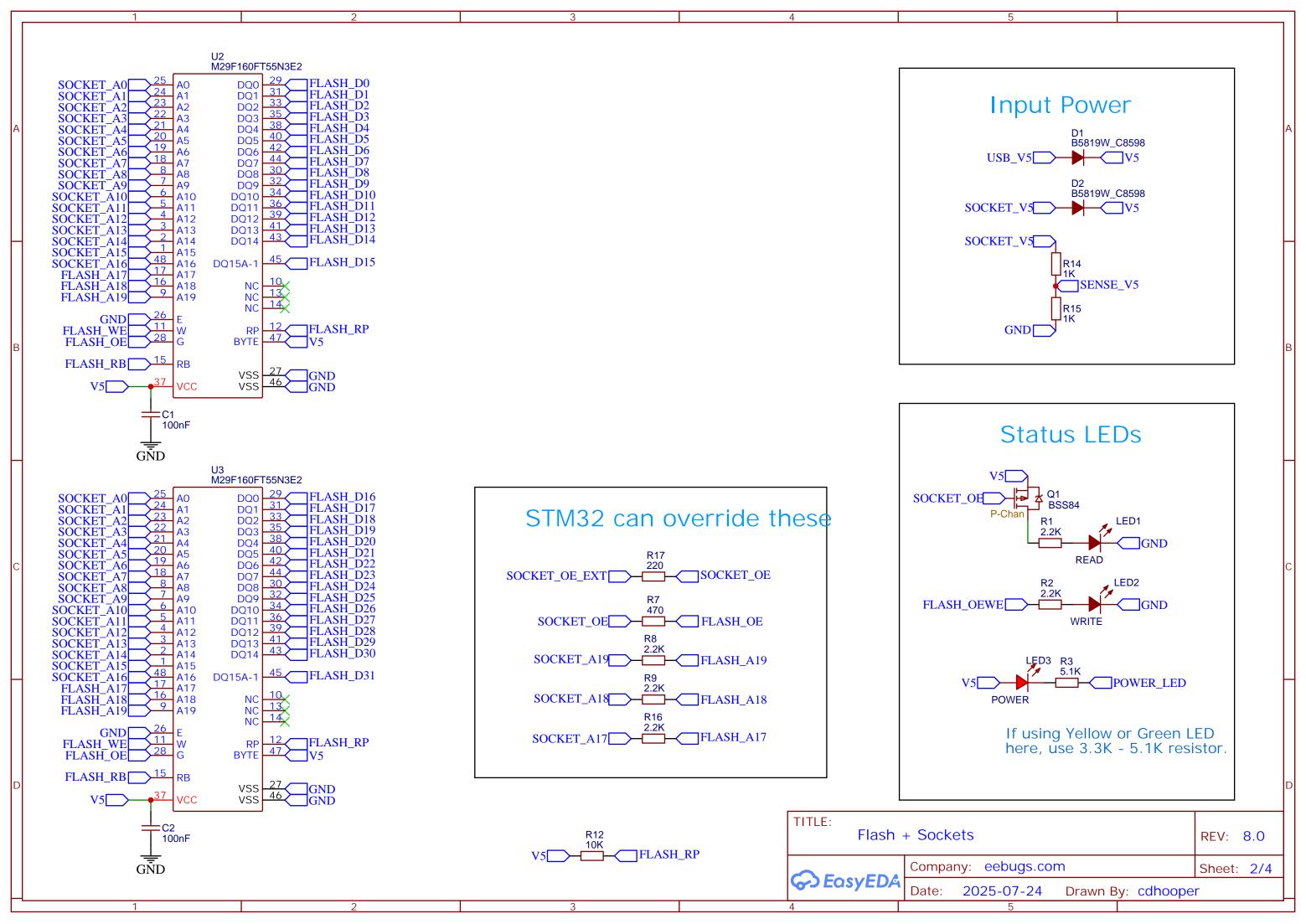


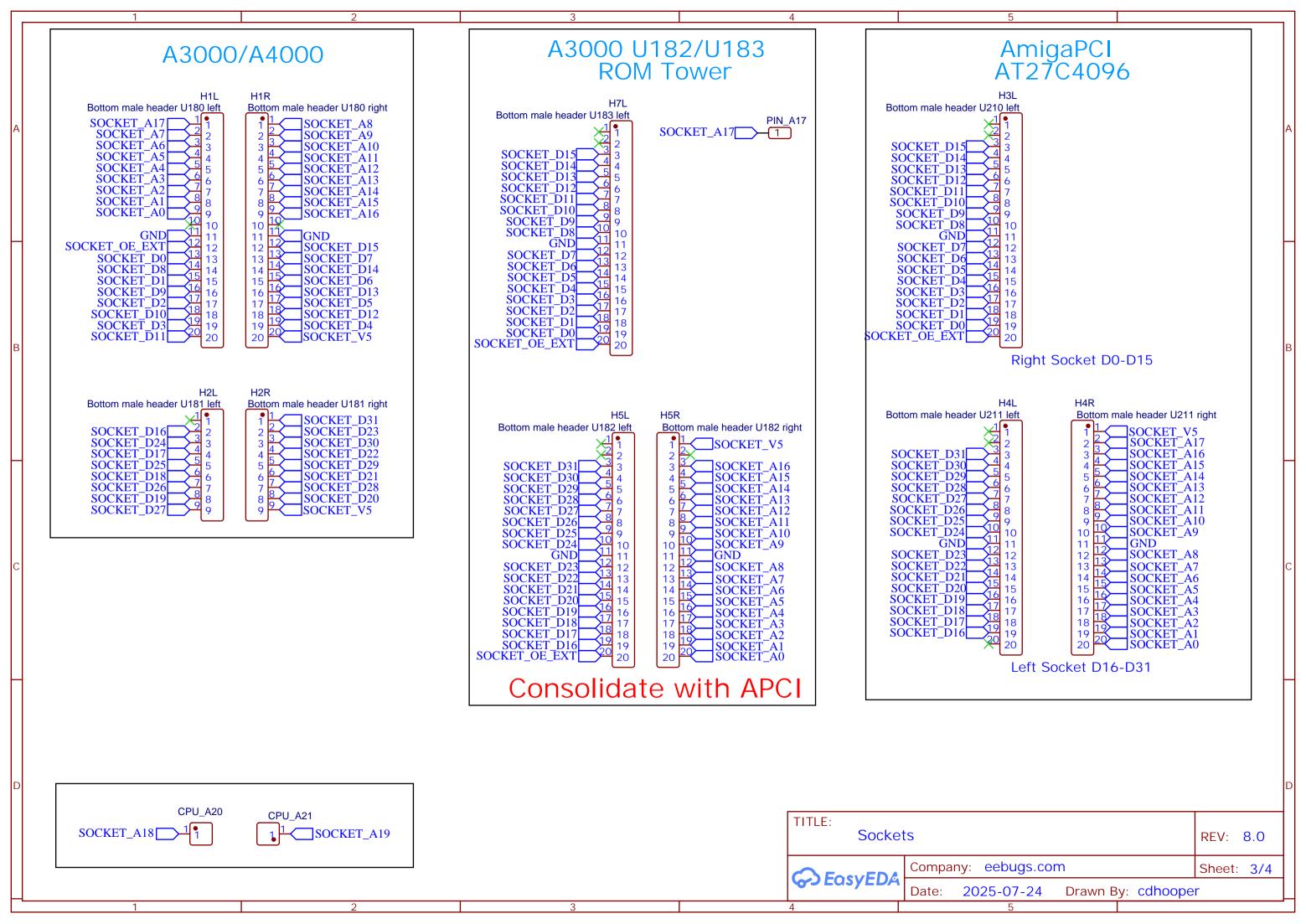




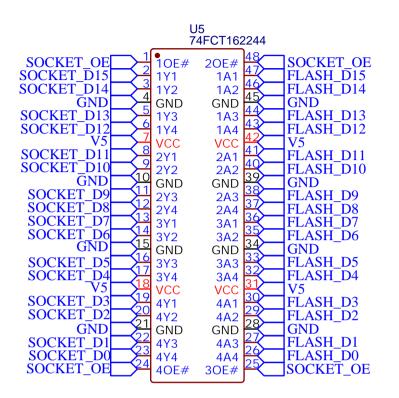
Capture Amiga reset

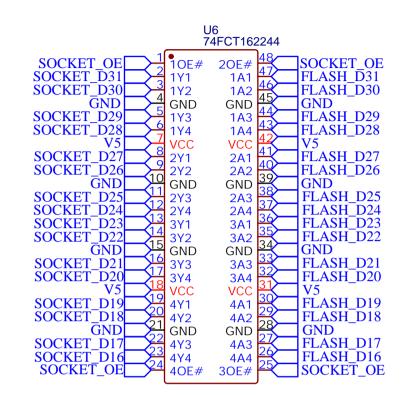
AMIGA_KBRST 1 KBRST





Bus drivers are used to decouple STM32 putting response and flash write data on the bus from when the Amiga CPU does a read.





When FLASH_OEWE is high, FLASH_WE will be driven low when SOCKET_OE is low. Otherwise, FLASH_WE is managed by the STM32. This will allow the Amiga CPU to terminate a flash command write at the end of a bus cycle.

FLASH_OEWE

N-Chan

R13

IK

N-Chan

FLASH_WE

SOCKET_OE

OR

R10

INTERIOR

TITLE: Buffer		REV: 8.0
C FORVEDA	Company: eebugs.com	Sheet: 4/4

EasyEDA Date:

te: 2025-07-24 Drawr

Drawn By: cdhooper

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