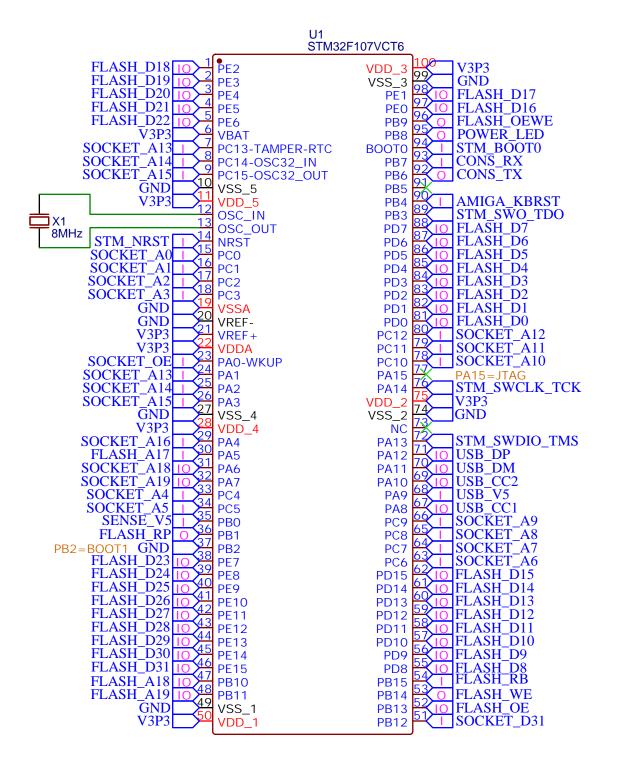
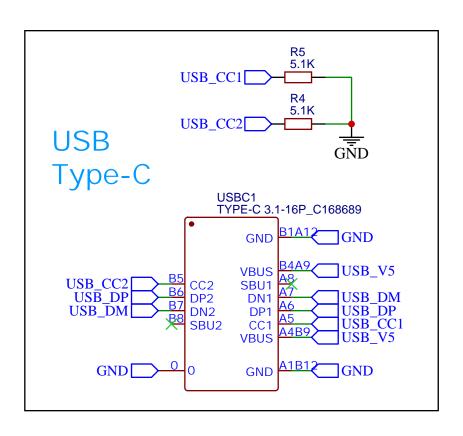
## Amiga KickSmash 32 - In-system flash EEPROM

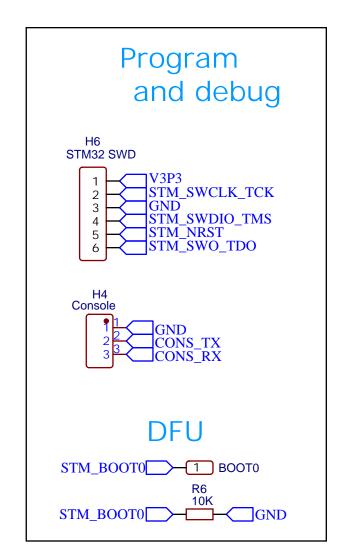


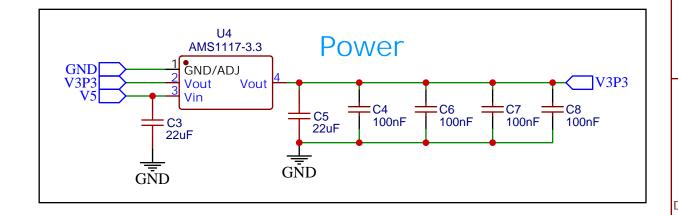
Capture Amiga reset

AMIGA\_KBRST 1 KBRST

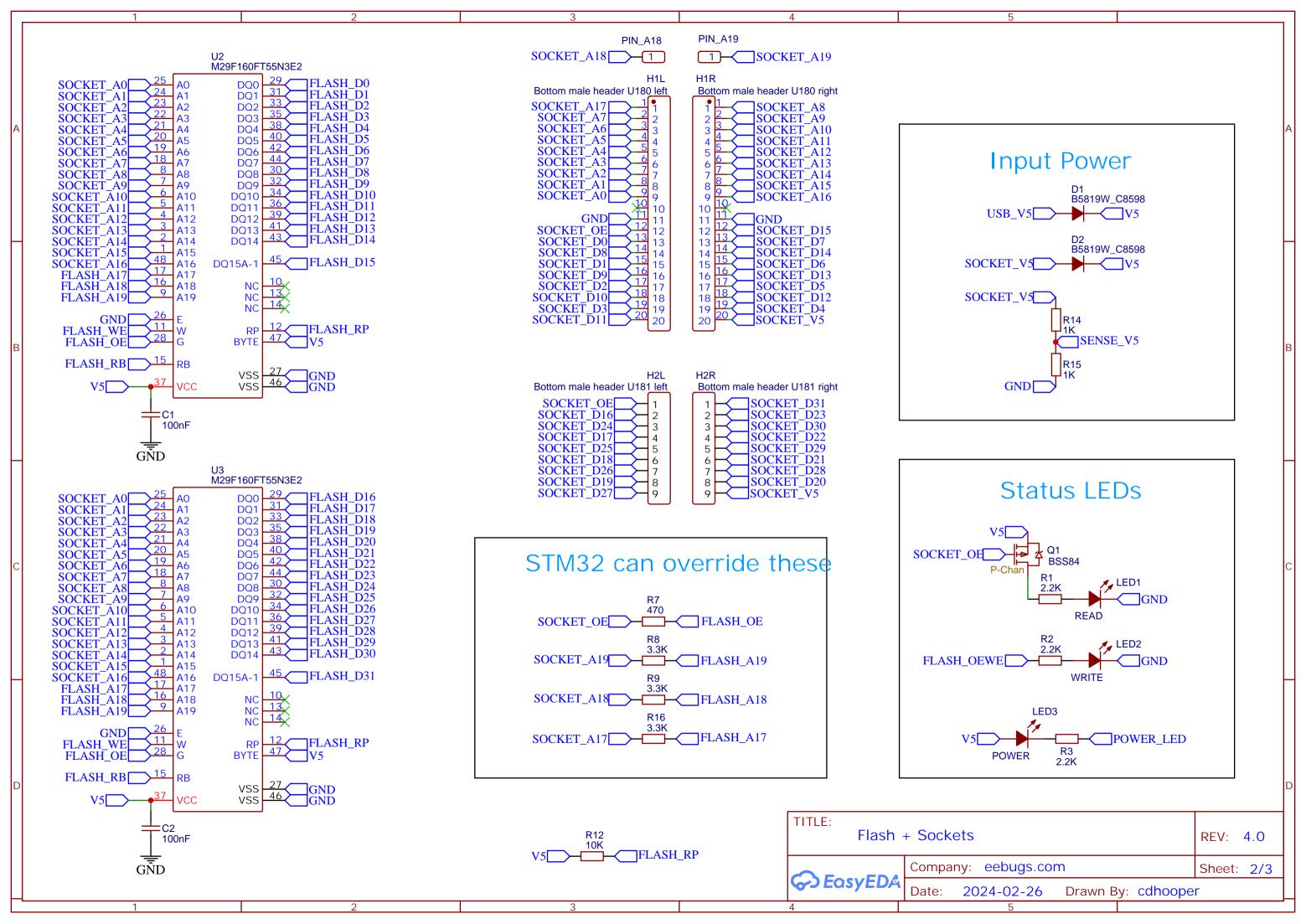
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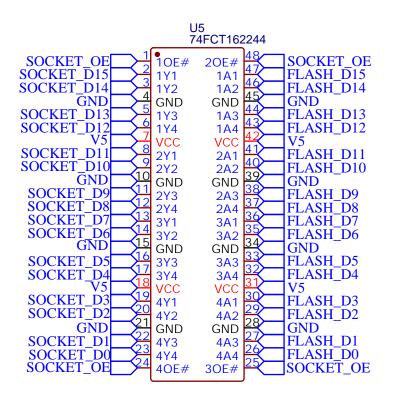


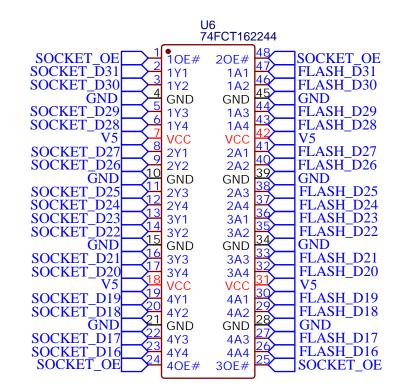






## Bus drivers are used to decouple STM32 putting response and flash write data on the bus from when the CPU does a read.





When FLASH\_OEWE is high, FLASH\_WE will be driven low when SOCKET OE is low. Otherwise, FLASH\_WE is managed by the STM32. Meant to allow CPU to terminate flash write at end of bus cycle. FLASH\_WE N-Chan FLASH OEWE Q2 AO3400A N-Chan **Q3** AO3400A SOCKET\_OE **GND** 

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2024-02-26 Drawn By: cdhooper