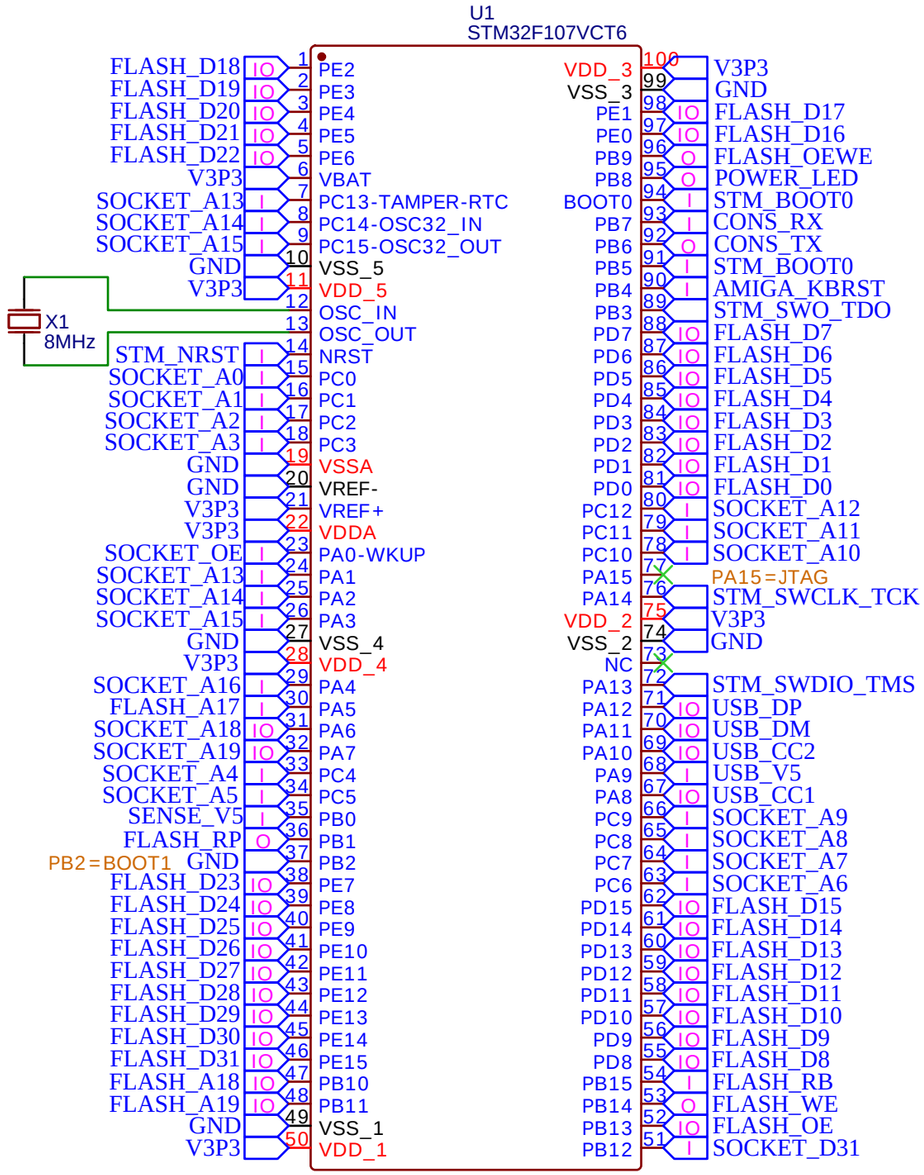
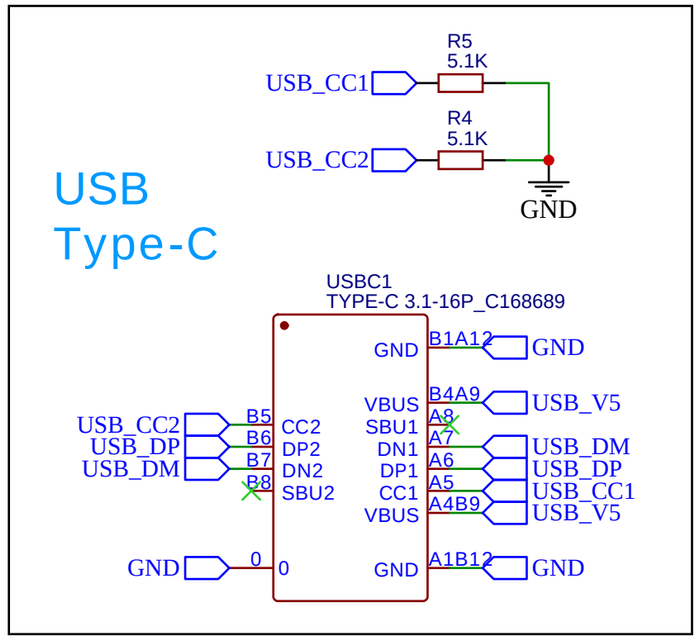
Amiga KickSmash 32 - In-system smart flash EEPROM

Rev 7 2025-03-12

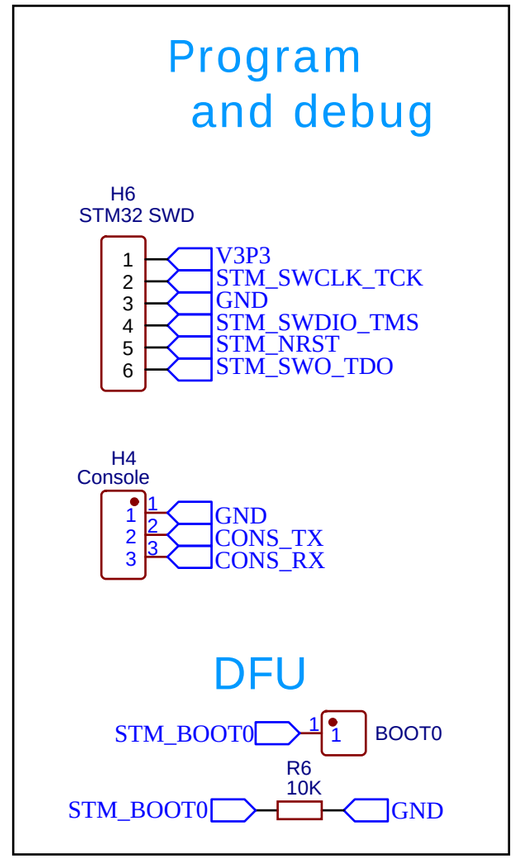


Capture Amiga reset

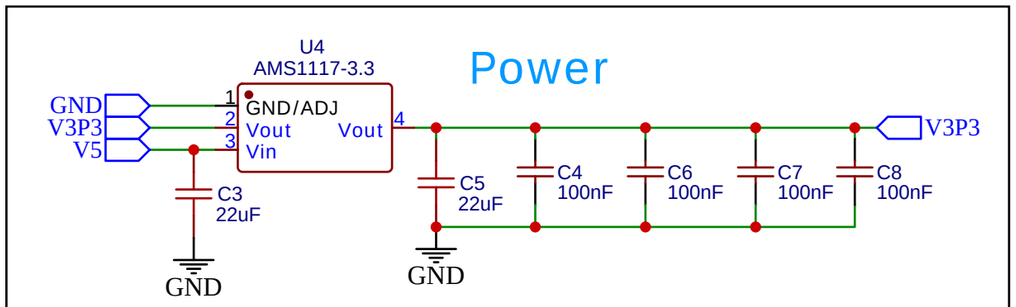
USB Type-C



Program and debug



Power



TITLE: STM32		REV: 7.0
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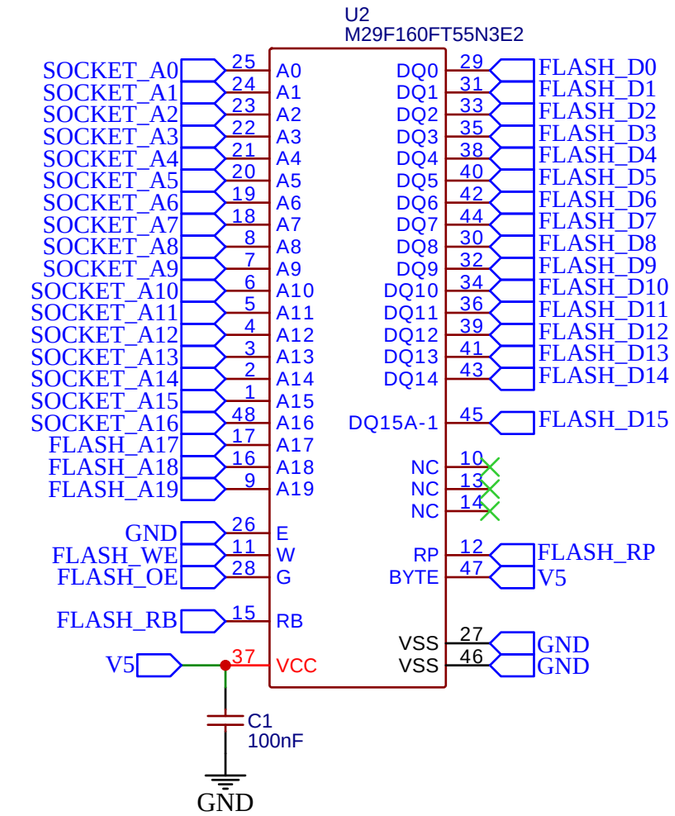
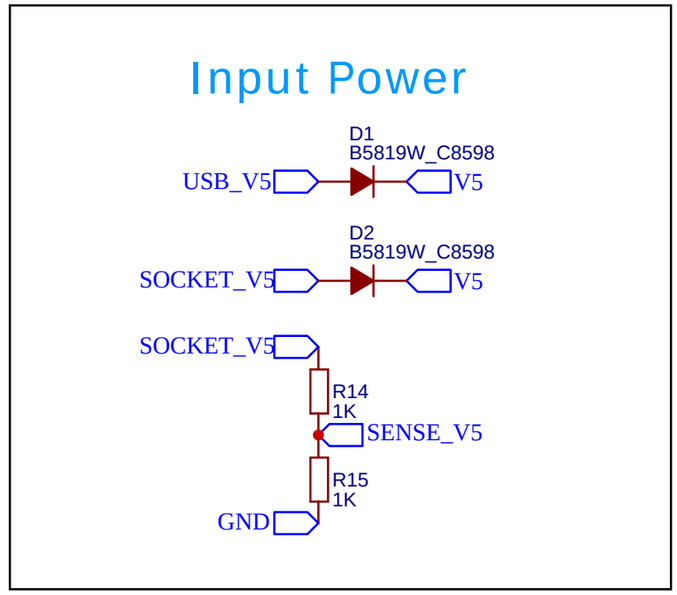
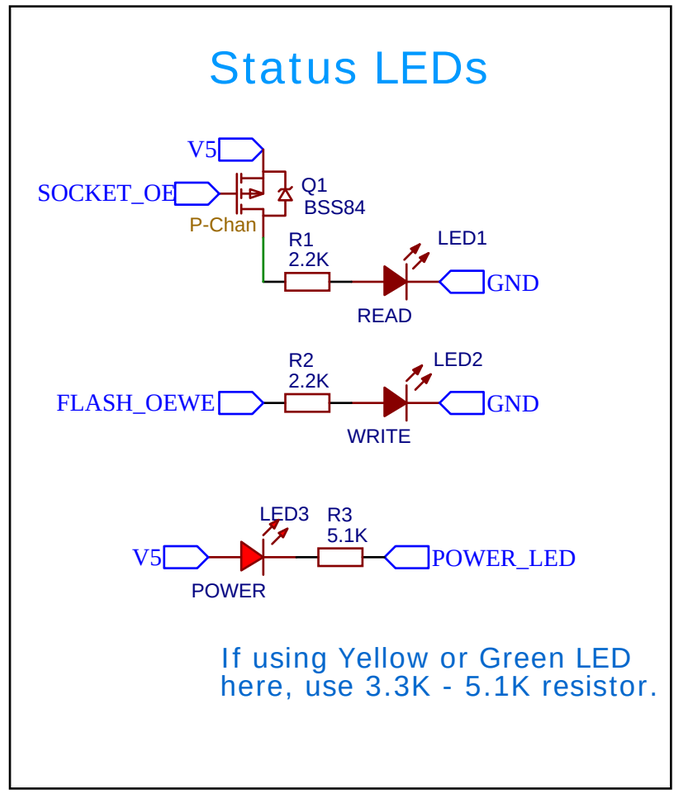
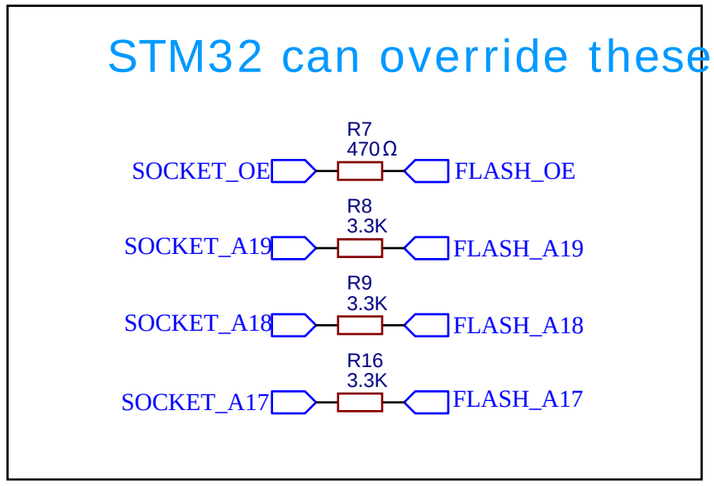
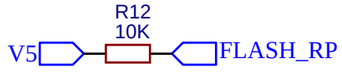
	Date: 2025-03-12 Drawn By: cdhooper	











TITLE: Flash + Sockets		REV: 7.0
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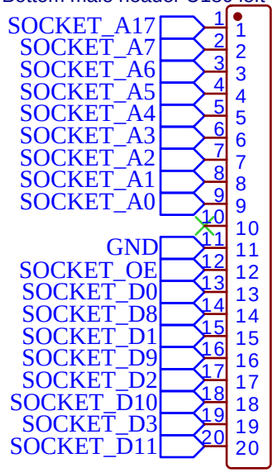
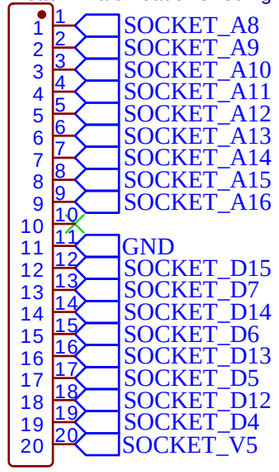
	Date: 2025-03-12 Drawn By: cdhooper	

A3000/A4000

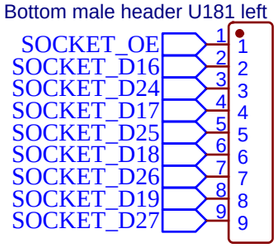
H1L
Bottom male header U180 left



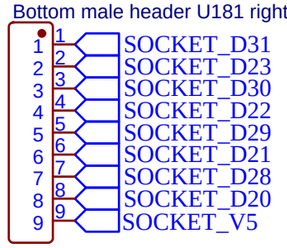
H1R
Bottom male header U180 right



H2L
Bottom male header U181 left

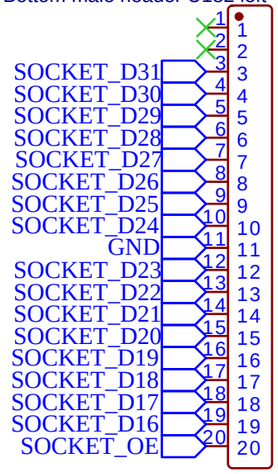


H2R
Bottom male header U181 right

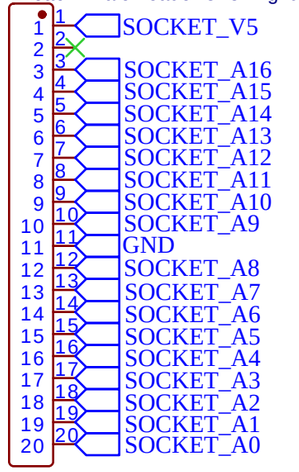


A3000 U182/U183
ROM Tower

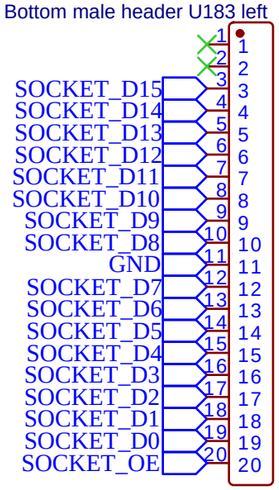
H5L
Bottom male header U182 left



H5R
Bottom male header U182 right



H7L
Bottom male header U183 left



PIN_A17

CPU_A20

CPU_A21

TITLE: Sockets	REV: 7.0
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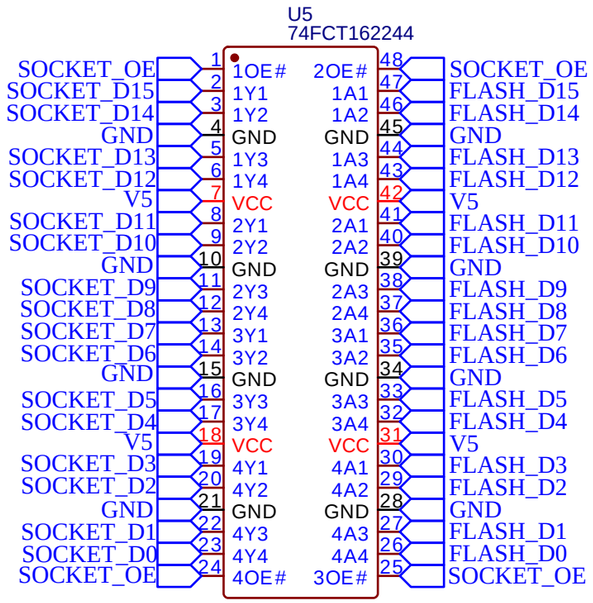
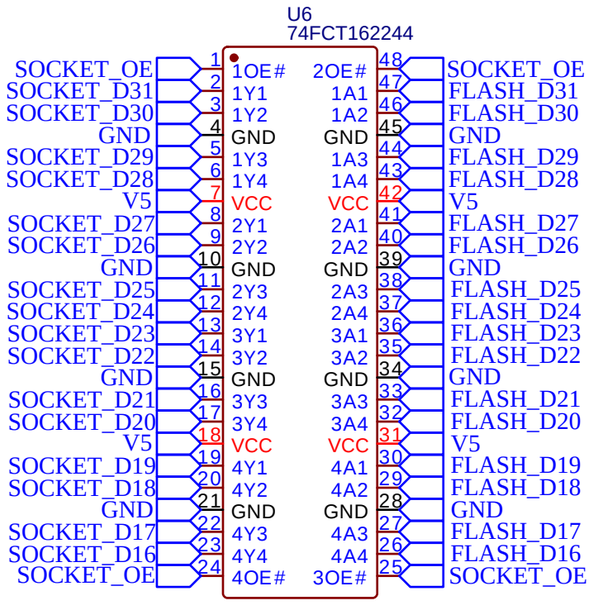
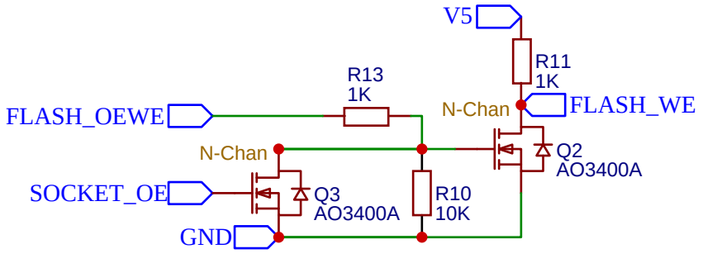
Date: 2025-03-12 Drawn By: cdhooper	

Bus drivers are used to decouple STM32 putting response and flash write data on the bus from when the Amiga CPU does a read.





When FLASH_OEWE is high, FLASH_WE will be driven low when SOCKET_OE is low. Otherwise, FLASH_WE is managed by the STM32. This will allow the Amiga CPU to terminate a flash command write at the end of a bus cycle.



TITLE: Buffer		REV: 7.0
EasyEDA	Company: eebugs.com	Sheet: 4/4
	Date: 2025-03-12 Drawn By: cdhooper	