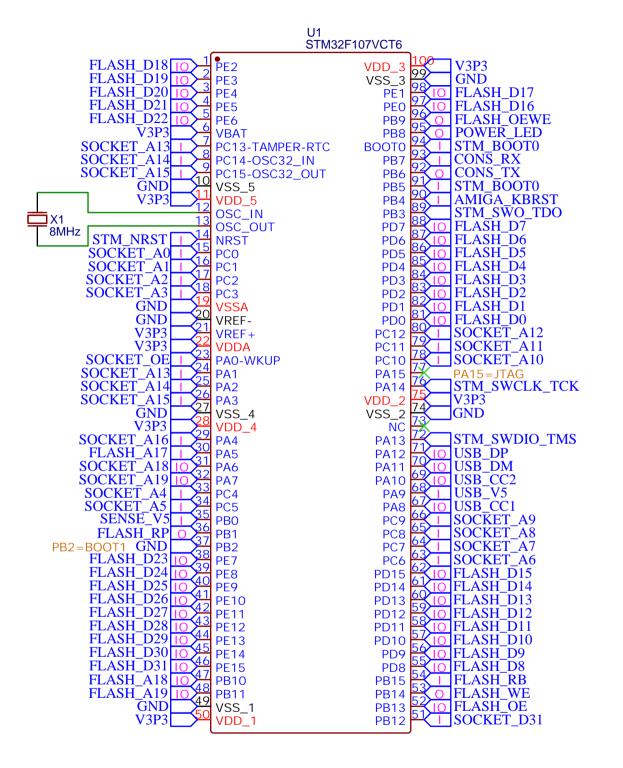
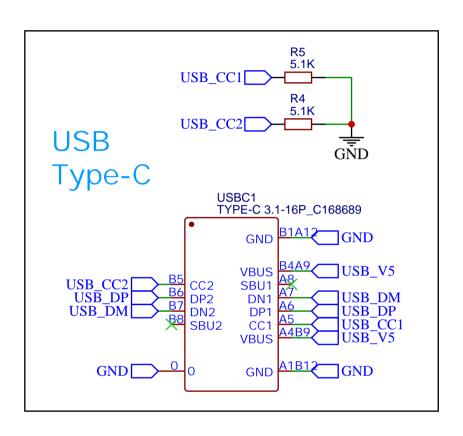
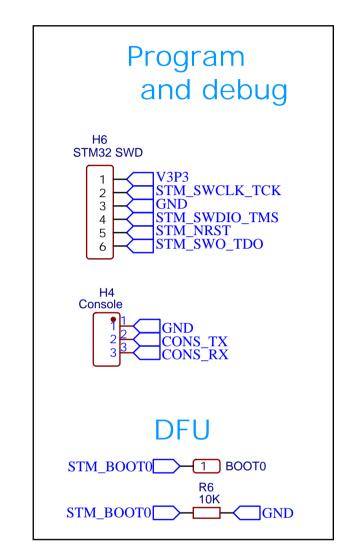
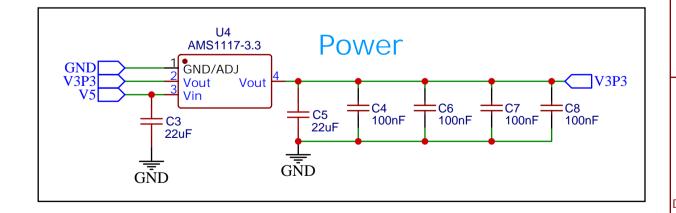
Amiga KickSmash 32 - In-system flash EEPROM



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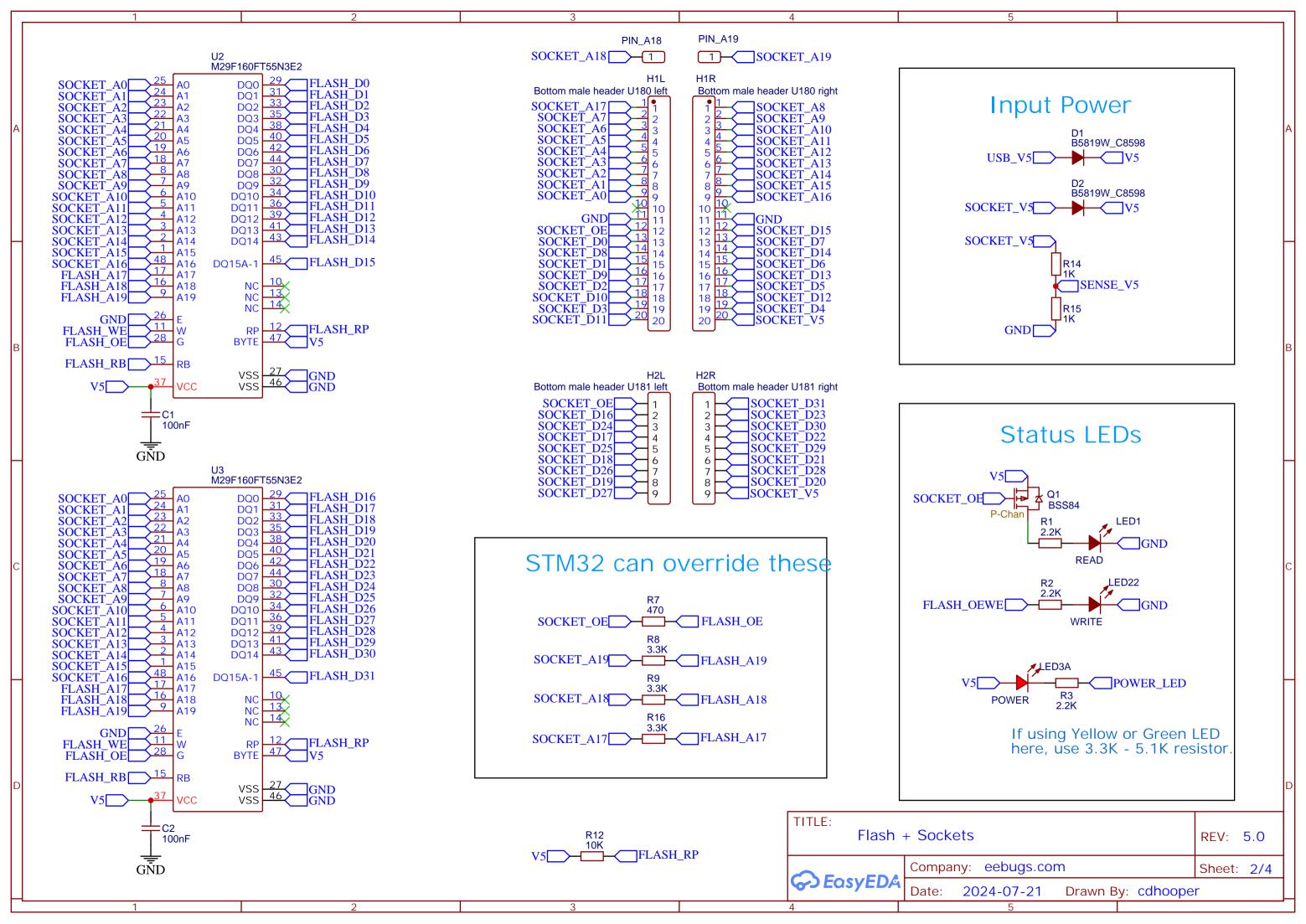


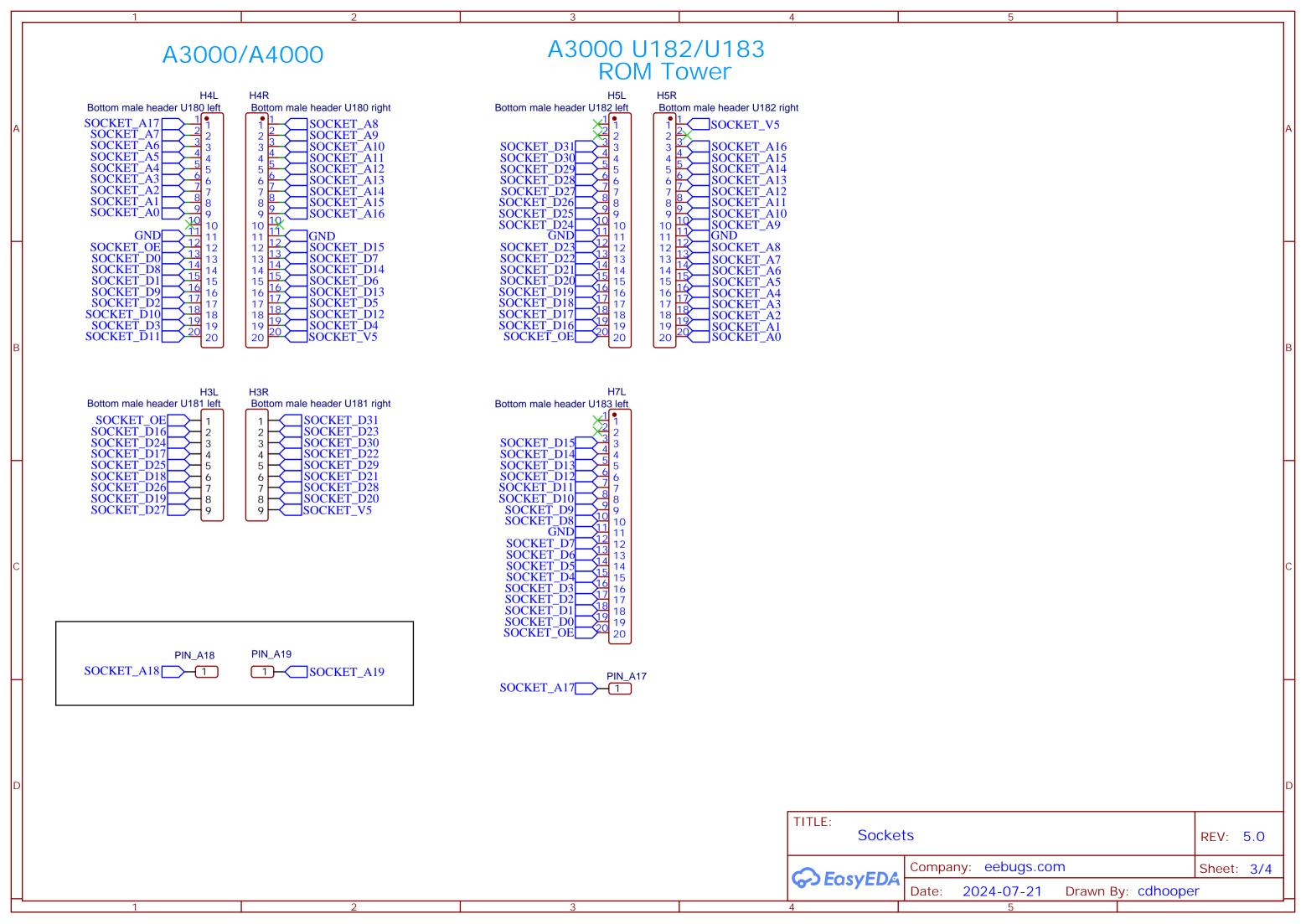




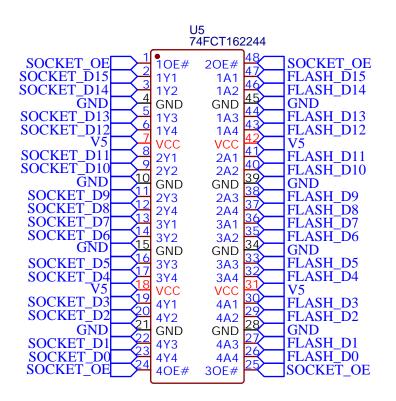
Capture Amiga reset

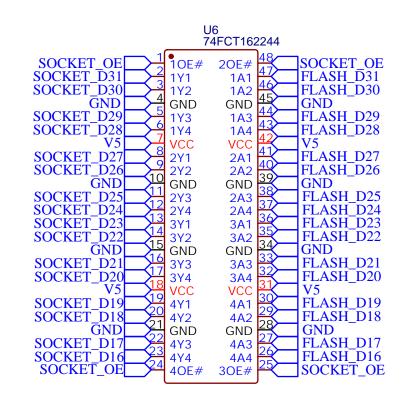
AMIGA KBRST 1 KBRST





Bus drivers are used to decouple STM32 putting response and flash write data on the bus from when the Amiga CPU does a read.





When FLASH_OEWE is high, FLASH_WE will be driven low when SOCKET OE is low. Otherwise, FLASH_WE is managed by the STM32. This will allow the Amiga CPU to terminate a flash command write at the end of a bus cycle. FLASH_WE N-Chan FLASH OEWE Q2 AO3400A N-Chan **Q3** AO3400A SOCKET_OE **GND**

	TITLE: Buffer		REV: 5.0
	€ FosvED&	Company: eebugs.com	Sheet: 4/4

2024-07-21 Drawn By: cdhooper