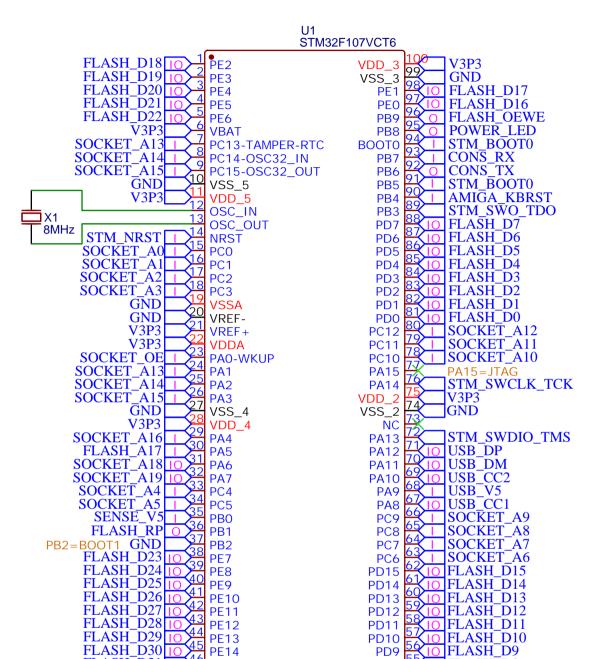
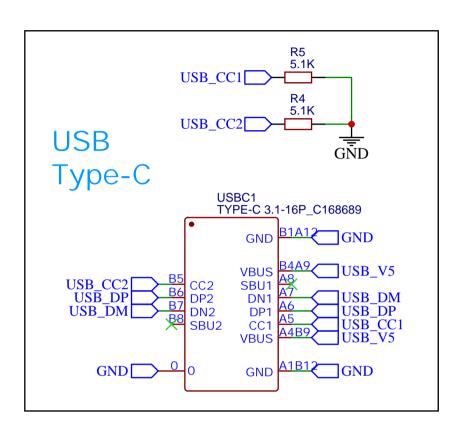
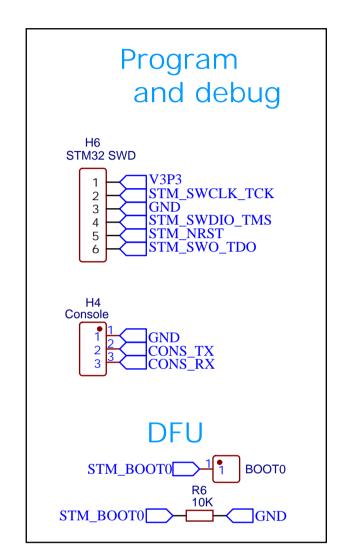
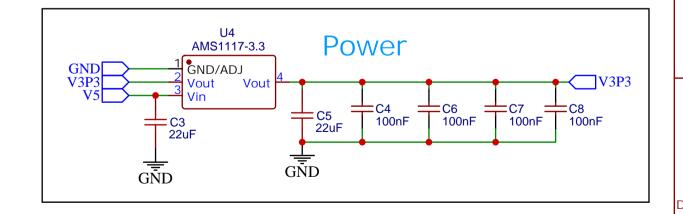
## Amiga KickSmash 32 - In-system smart flash EEPROM



Rev 7 2025-03-12









Capture Amiga reset

FLASH D8

O FLASH OE

SOCKET D31

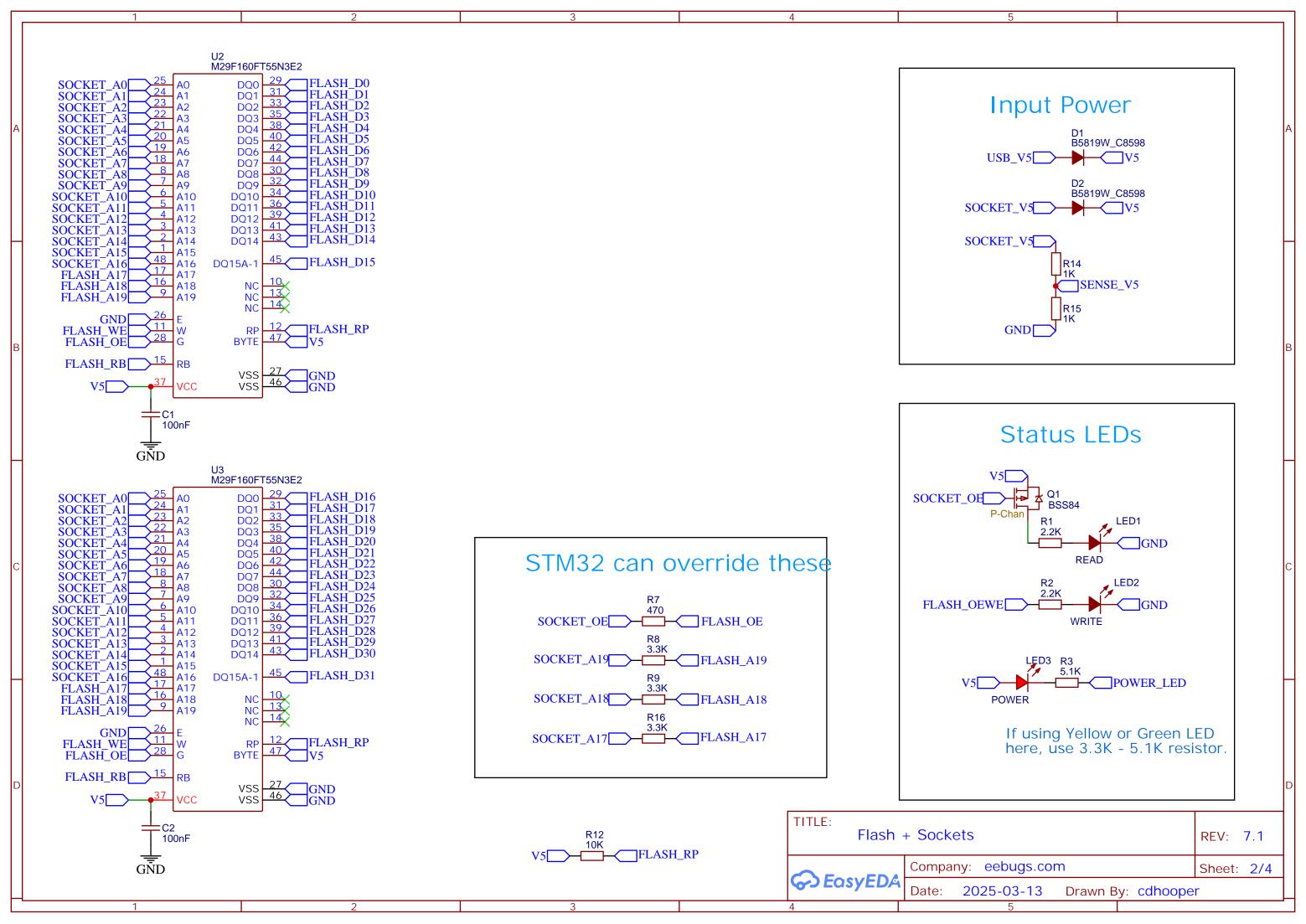
PB15 53 | FLASH\_RB PB14 52 | FLASH\_WE

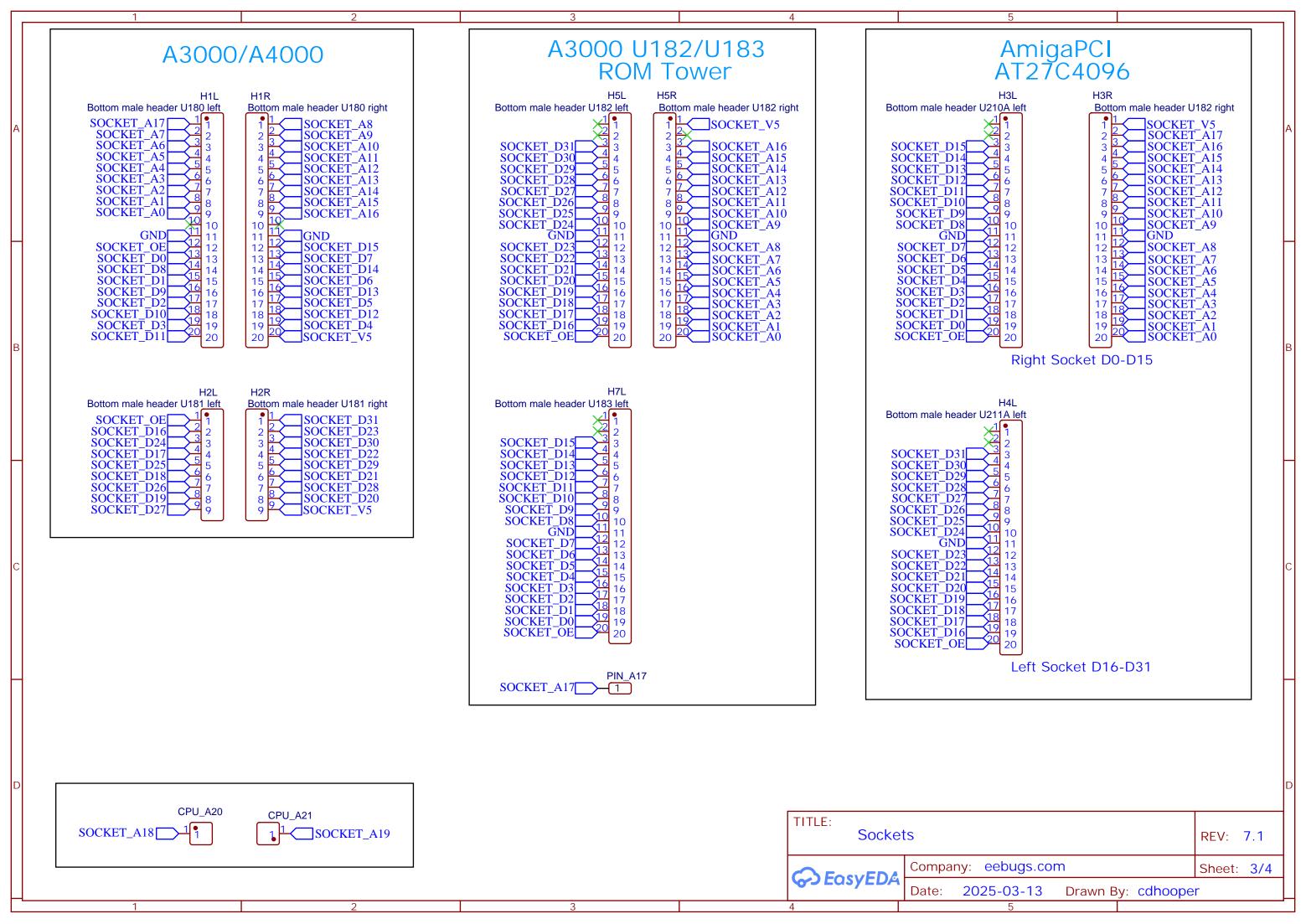
AMIGA\_KBRST 1 KBRST

PE14 1 46 PE15 2 47 PB10 2 48 PB11 49 VSS\_1 50 VDD 1

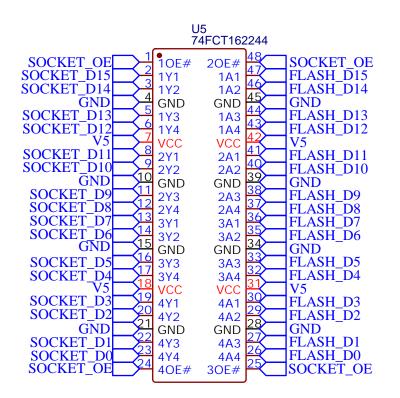
FLASH\_D31

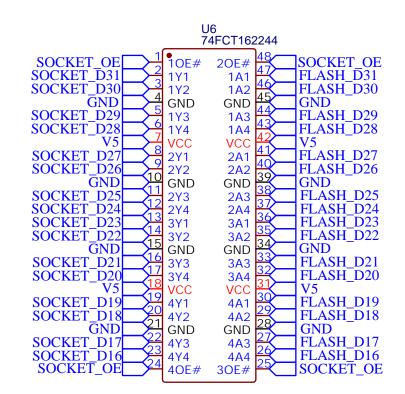
FLASH\_A18 FLASH\_A19





Bus drivers are used to decouple STM32 putting response and flash write data on the bus from when the Amiga CPU does a read.





TITLE: Buffer		REV: 7.1
€ FosvED&	Company: eebugs.com	Sheet: 4/4

CO EasyEDA Date

ate: 2025-03-13 Drawn B

Drawn By: cdhooper