Computer Architecture (501) - Part 1 Schedule of Lectures and Tutorials

Introduction + Boolean Algebra and Logic	Lecture	11 00 10 00 145
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Boolean Algebra and Logic	Tutorial	12:00 - 13:00, 145
Basic Circuits and Memory	Lecture	14:00 - 15:00, 311
Chip Design	Lecture	15:00 - 16:00, 311
Memory Organisation	Lecture	11:00 - 12:00, 145
Memory Organisation	Tutorial	12:00 - 13:00, 145
Data Representation + Binary Arithmetic	Lecture	11:00 - 12:00, 145
Data Representation + Binary Arithmetic	Tutorial	12:00 - 13:00, 145
Floating Point Numbers	Lecture	14:00 - 15:00, 311
Floating Point Numbers	Lecture	15:00 - 16:00, 311
Floating Point Numbers	Tutorial	14:00 - 15:00, 144
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	Memory Organisation Memory Organisation Data Representation + Binary Arithmetic Data Representation + Binary Arithmetic Floating Point Numbers Floating Point Numbers Floating Point Numbers	Memory Organisation Memory Organisation Data Representation + Binary Arithmetic Data Representation + Binary Arithmetic Tutorial Floating Point Numbers Floating Point Numbers Floating Point Numbers Tutorial Ploating Point Numbers Tutorial Ploating Point Numbers Tutorial Ploating Point Numbers Tutorial Pursework released - Deadling November 16th

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