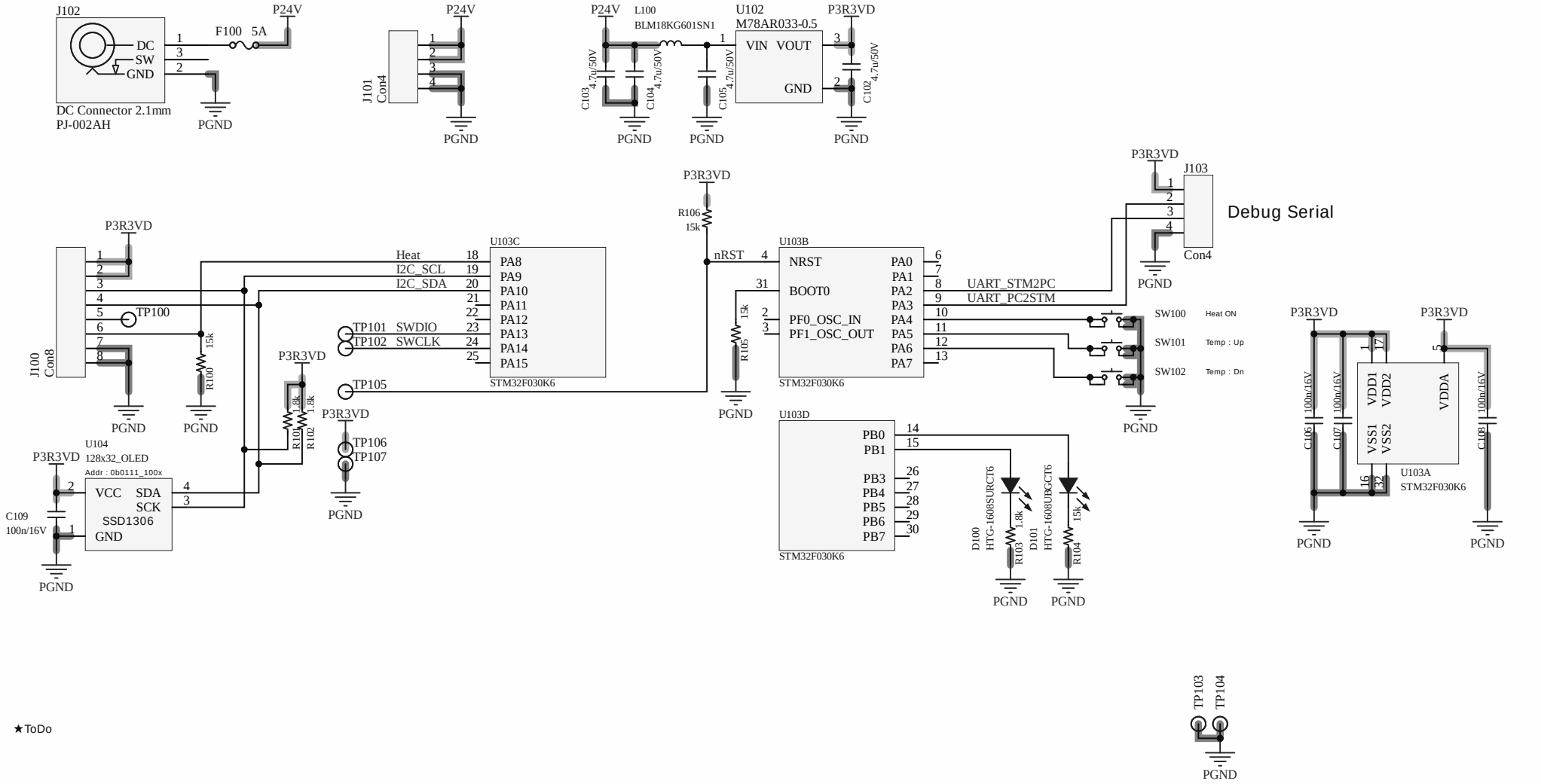
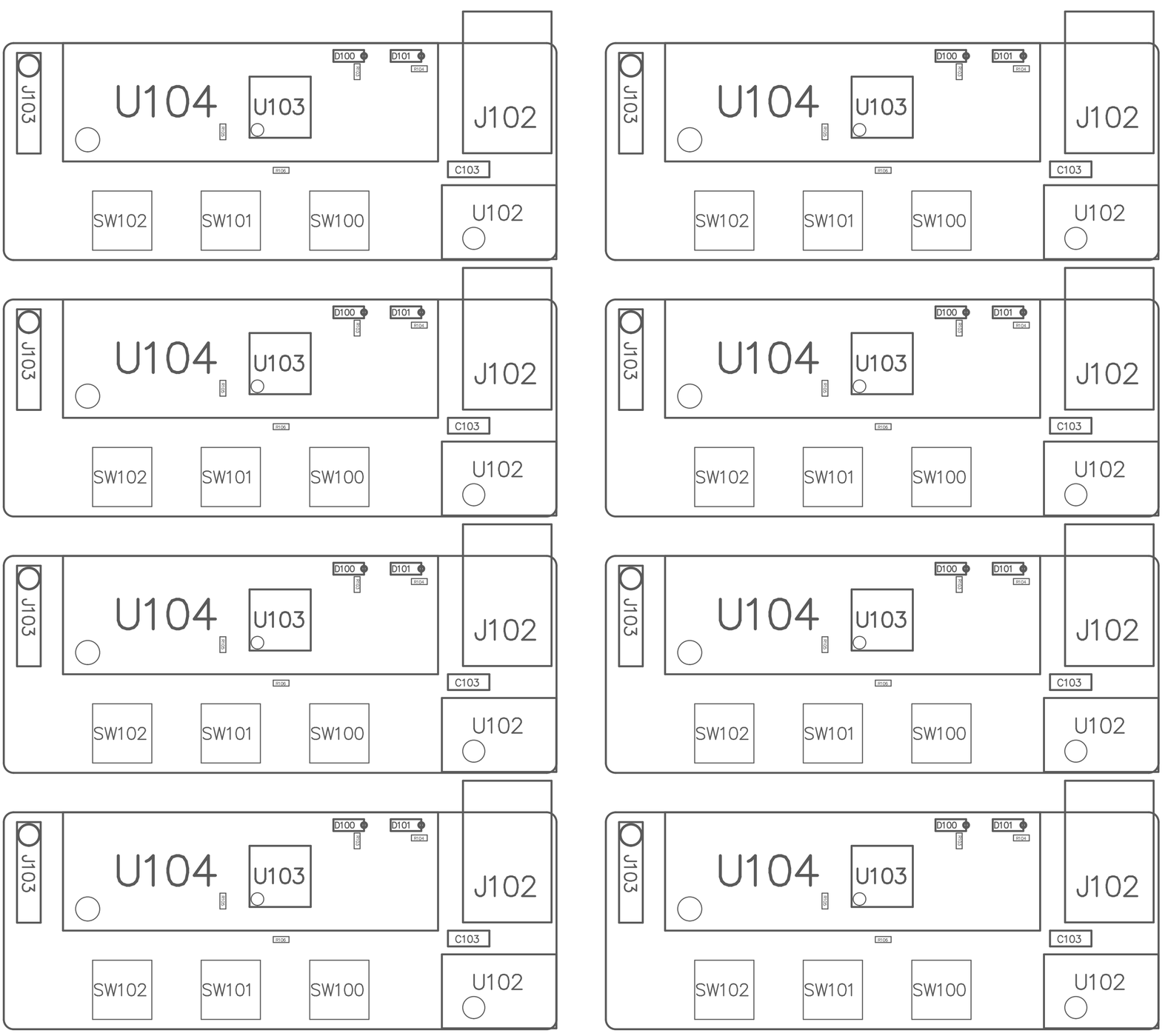


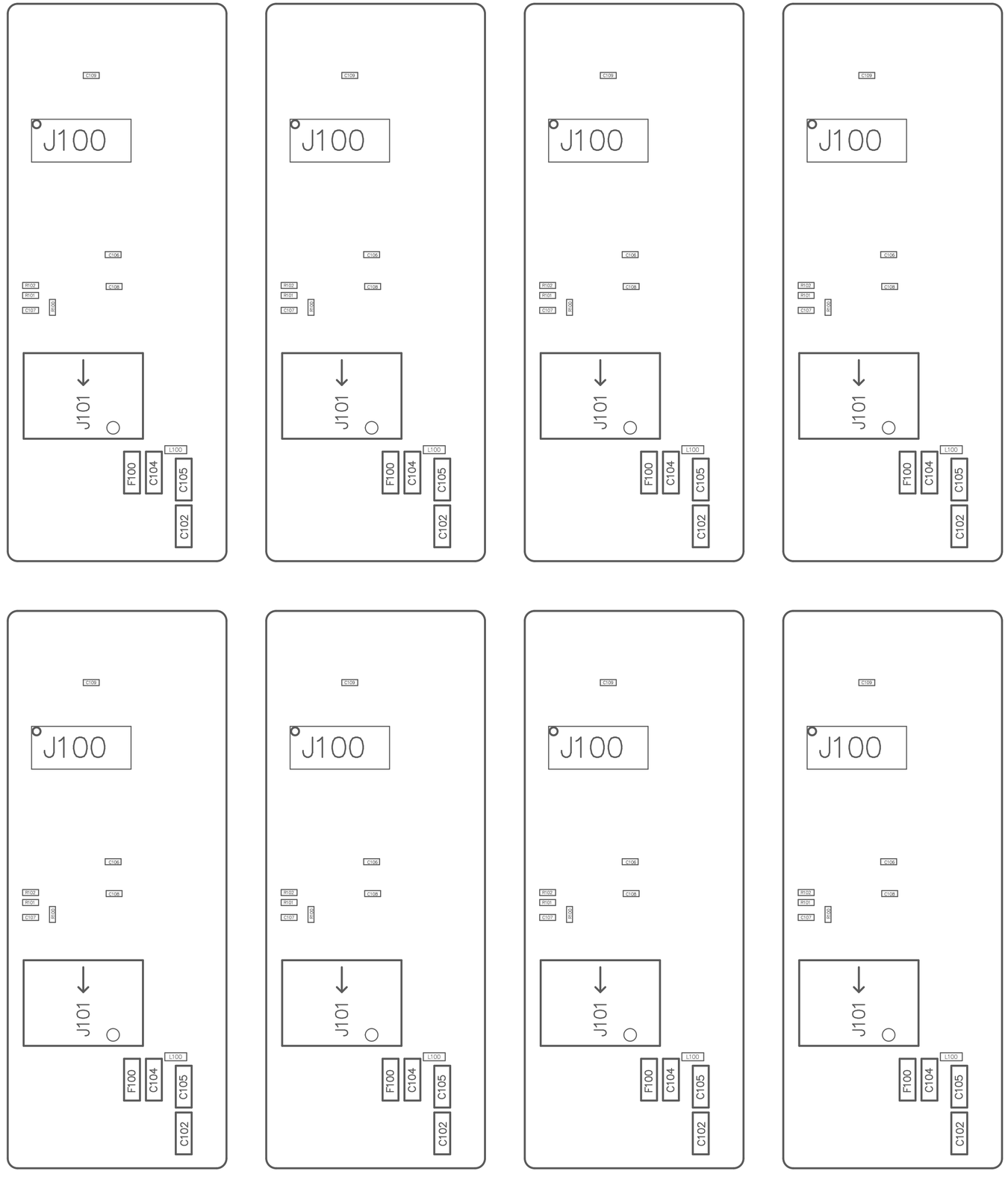
Title	PreHeater PowerStage		
Model	*	Rev.	*
Comment	*		
Filename	PreHeater_Control.SchDoc		
Size A4	Date 2020/04/17	CRONOS	
	Design *		
	Sheet * of *		


J103
U104
U103
J102
D100
D101
R105
R104
R106
C103
SW102
SW101
SW100
U102

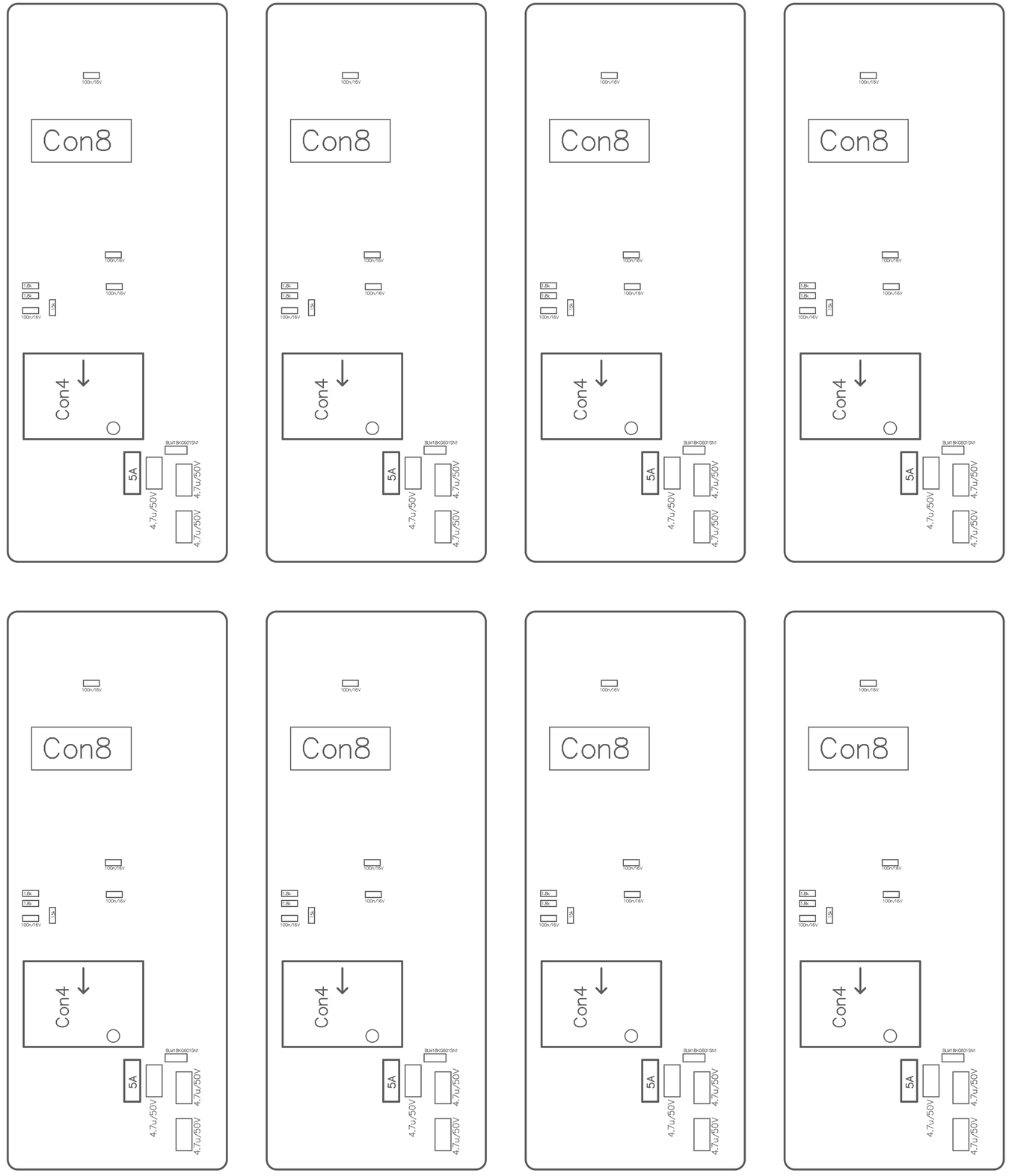
Outline (GML)

Con4
128x32_OLED
STM32F030K6
HTG-1608SURCT6
HTG-1608UBGCT6
DC Connector 2.1mm
4.7u/50V
1825910
1825910
1825910
M78AR033-0.5

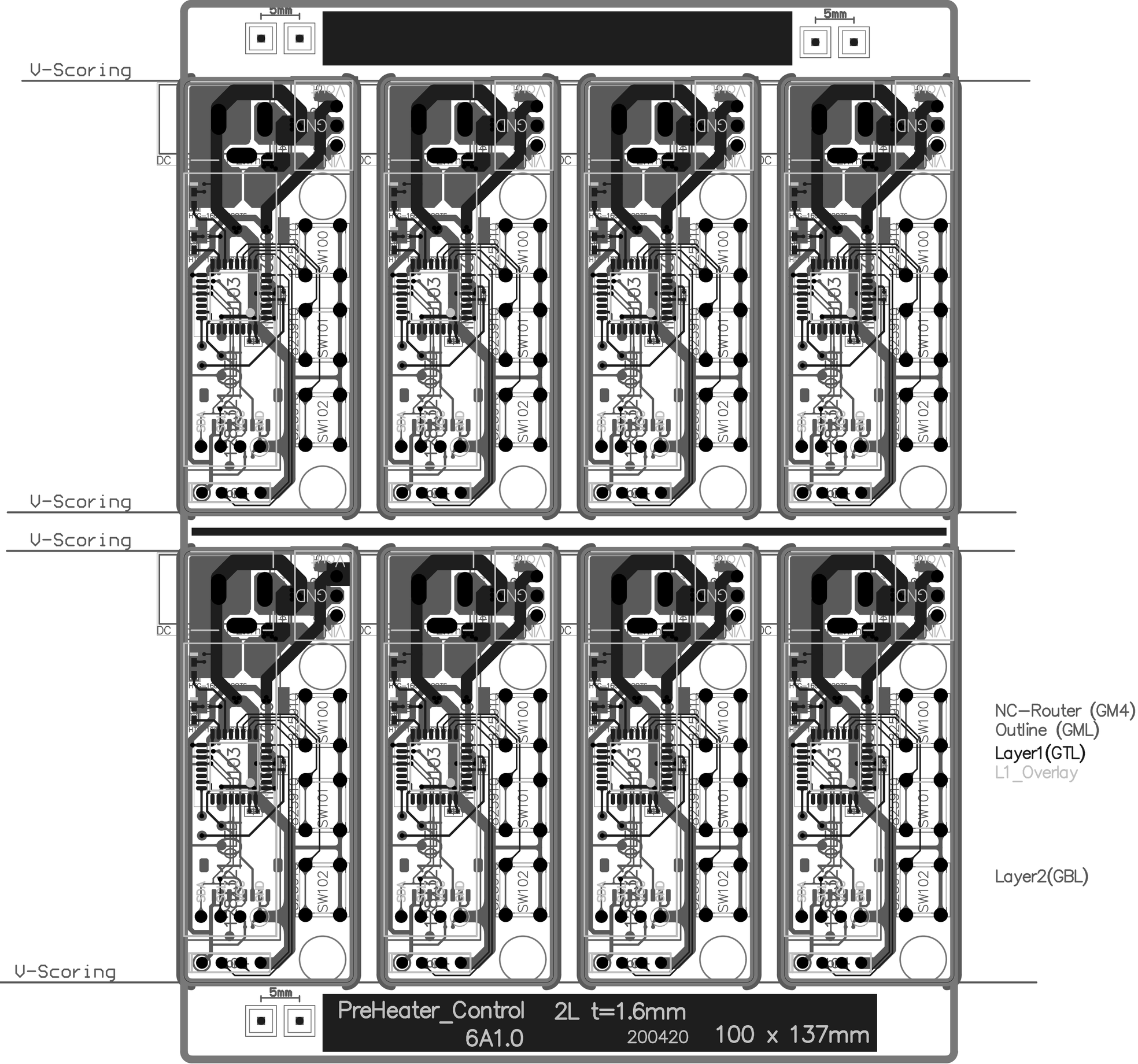
Outline (GML)

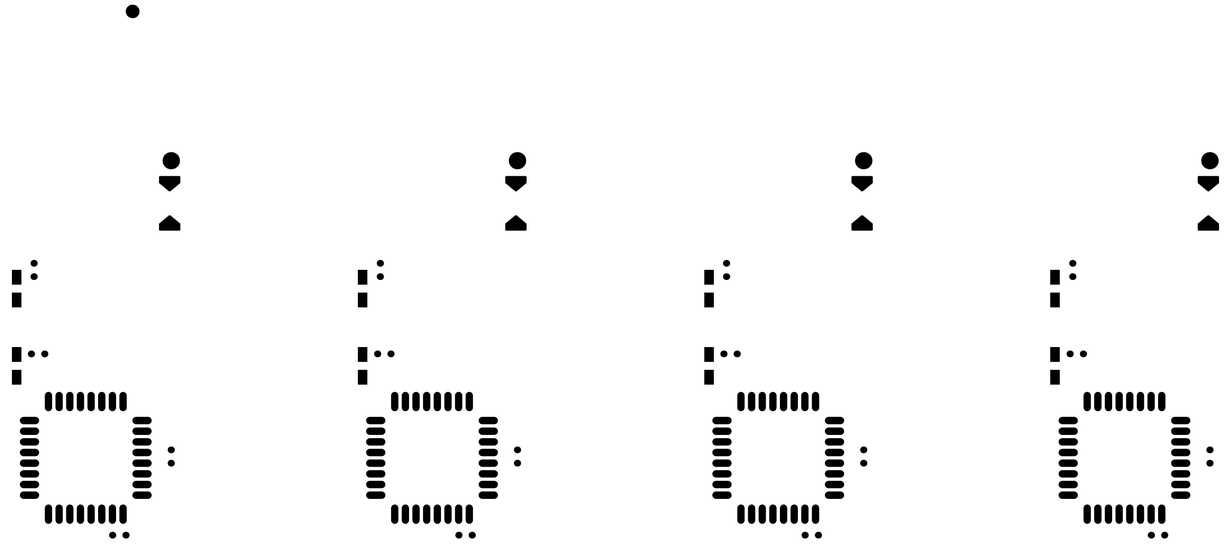

C109
J100
C106
C108
R102
R101
C107
R500
J101
F100
C104
L100
C105
C102

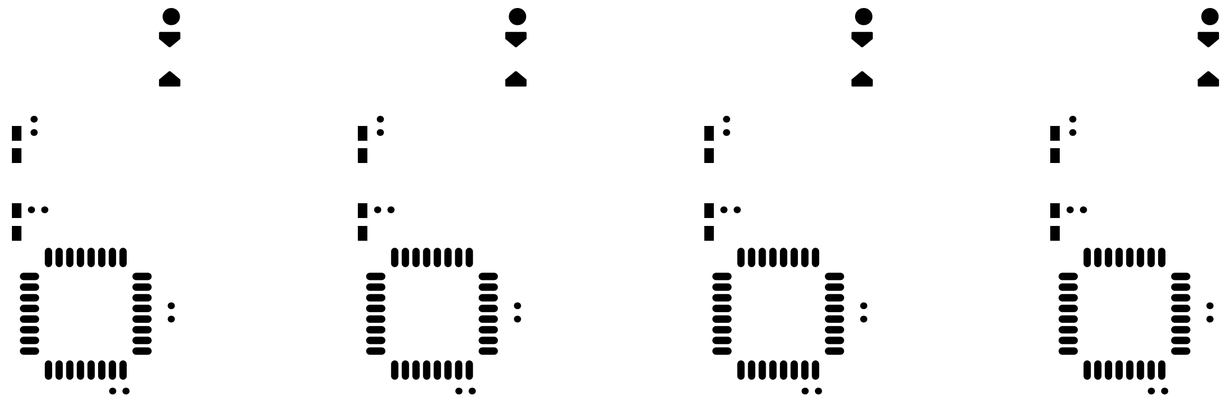
Outline (CAL)


100n/16V
Con8
1.8k
1.8k
100n/16V
1.5k
100n/16V
100n/16V
Con4
BLM18KG601SN1
5A
4.7u/50V
4.7u/50V
4.7u/50V
4.7u/50V

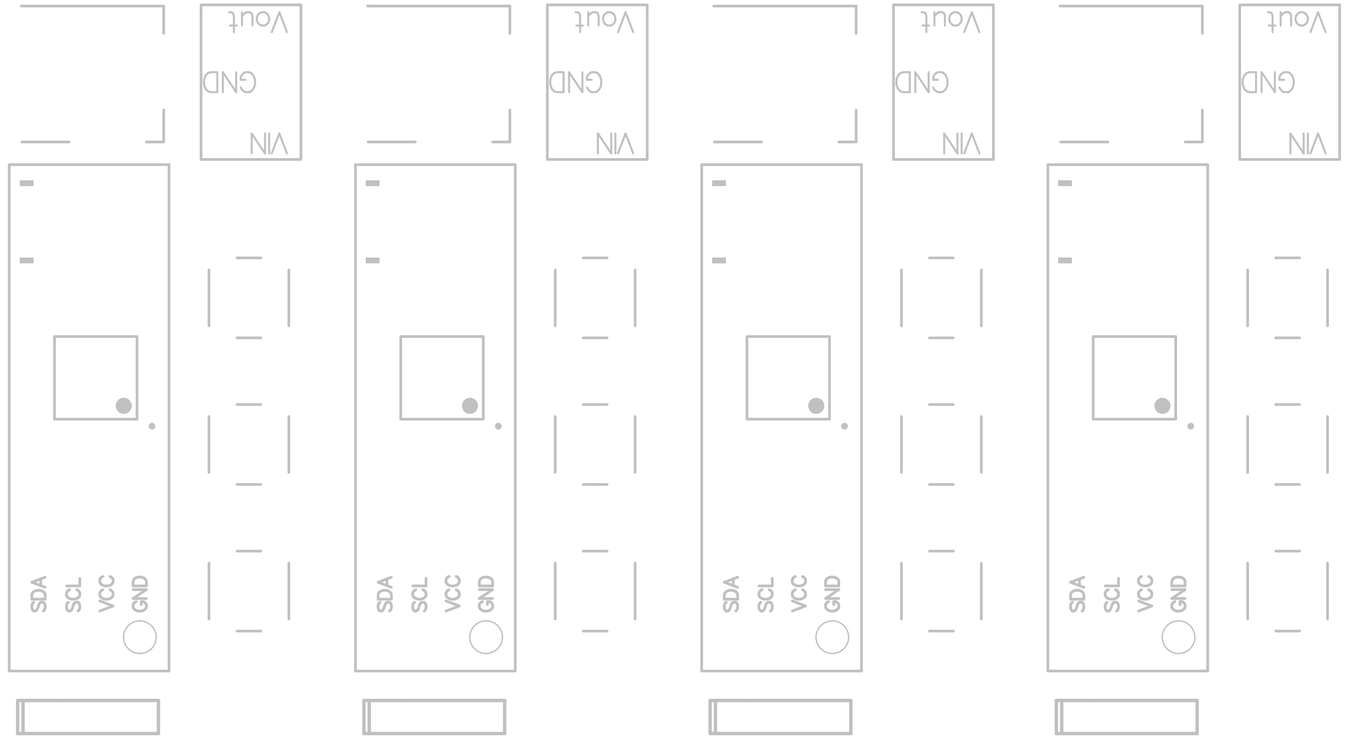
Outline (CAL)

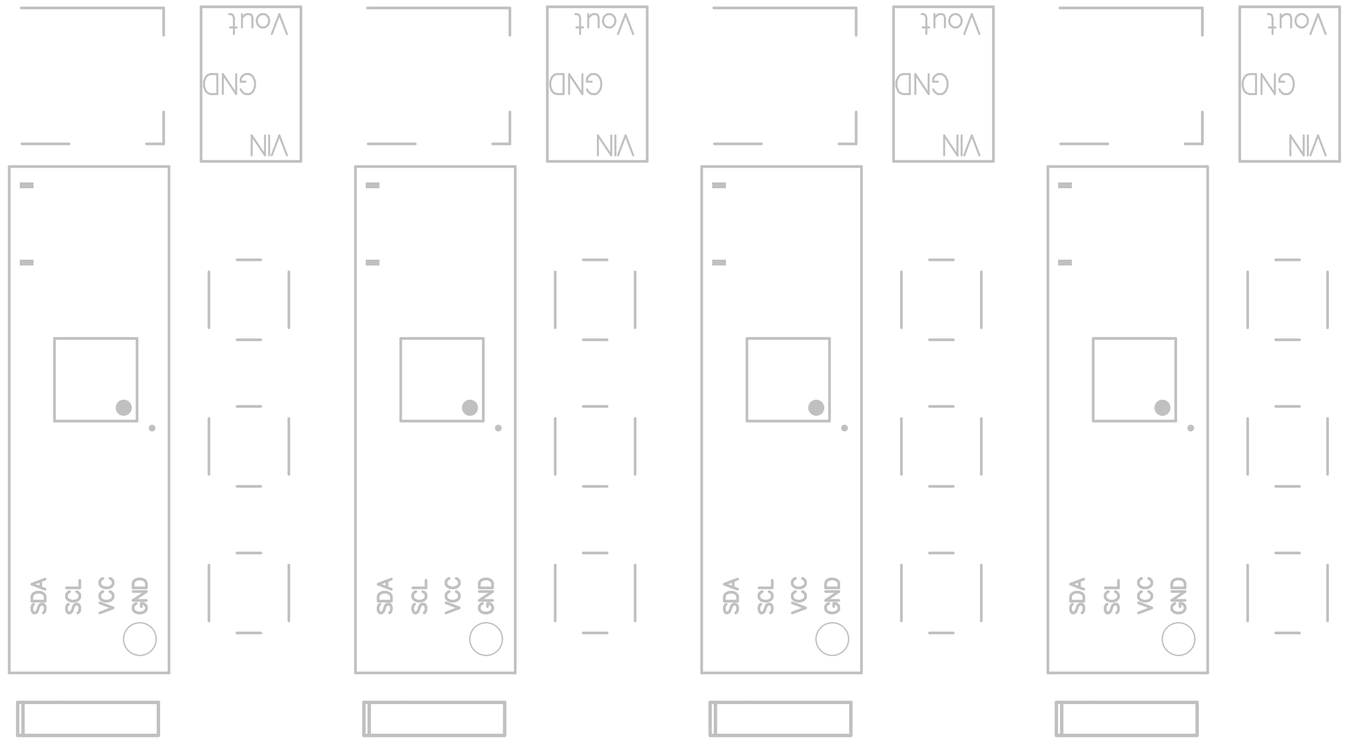

5mm
5mm
V-Scoring
V-Scoring
V-Scoring
V-Scoring
NC-Router (GM4)
Outline (GML)
Layer1(GTL)
L1_Overlay
Layer2(GBL)
5mm
PreHeater_Control 2L t=1.6mm
6A1.0
200420
100 x 137mm





Top_Paste (GTP)

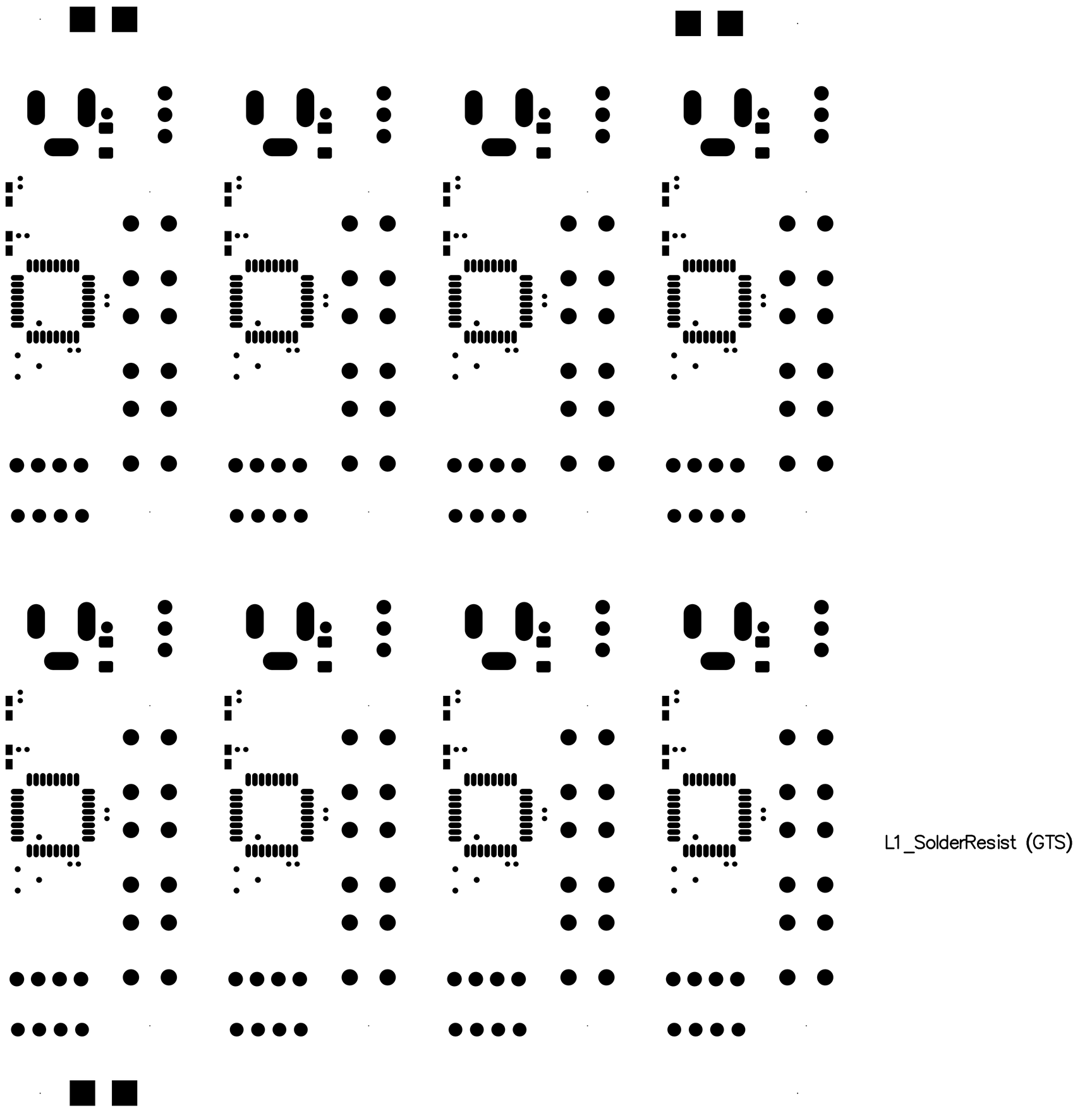




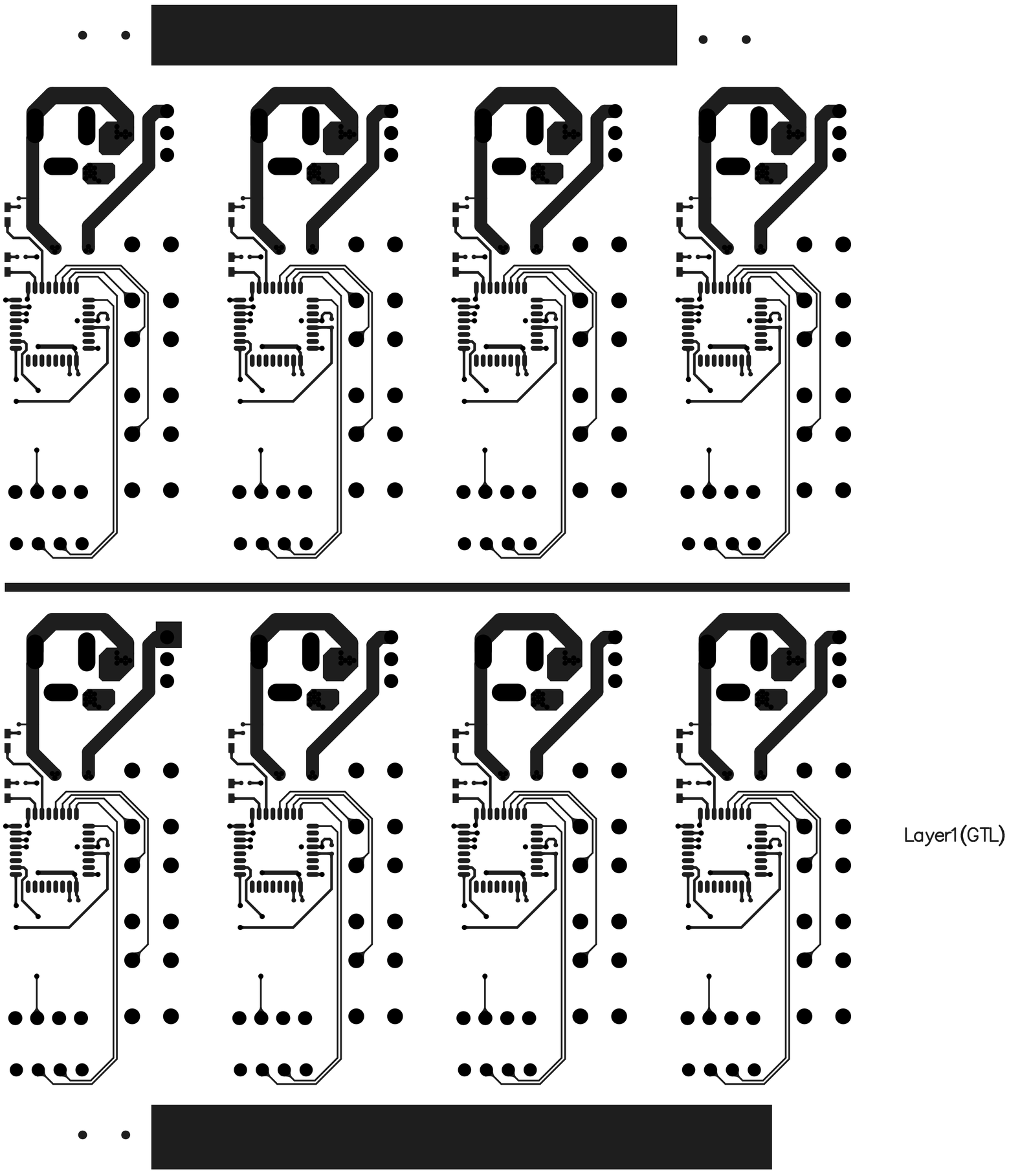
L1_Overlay

PreHeater_Control 2L t=1.6mm

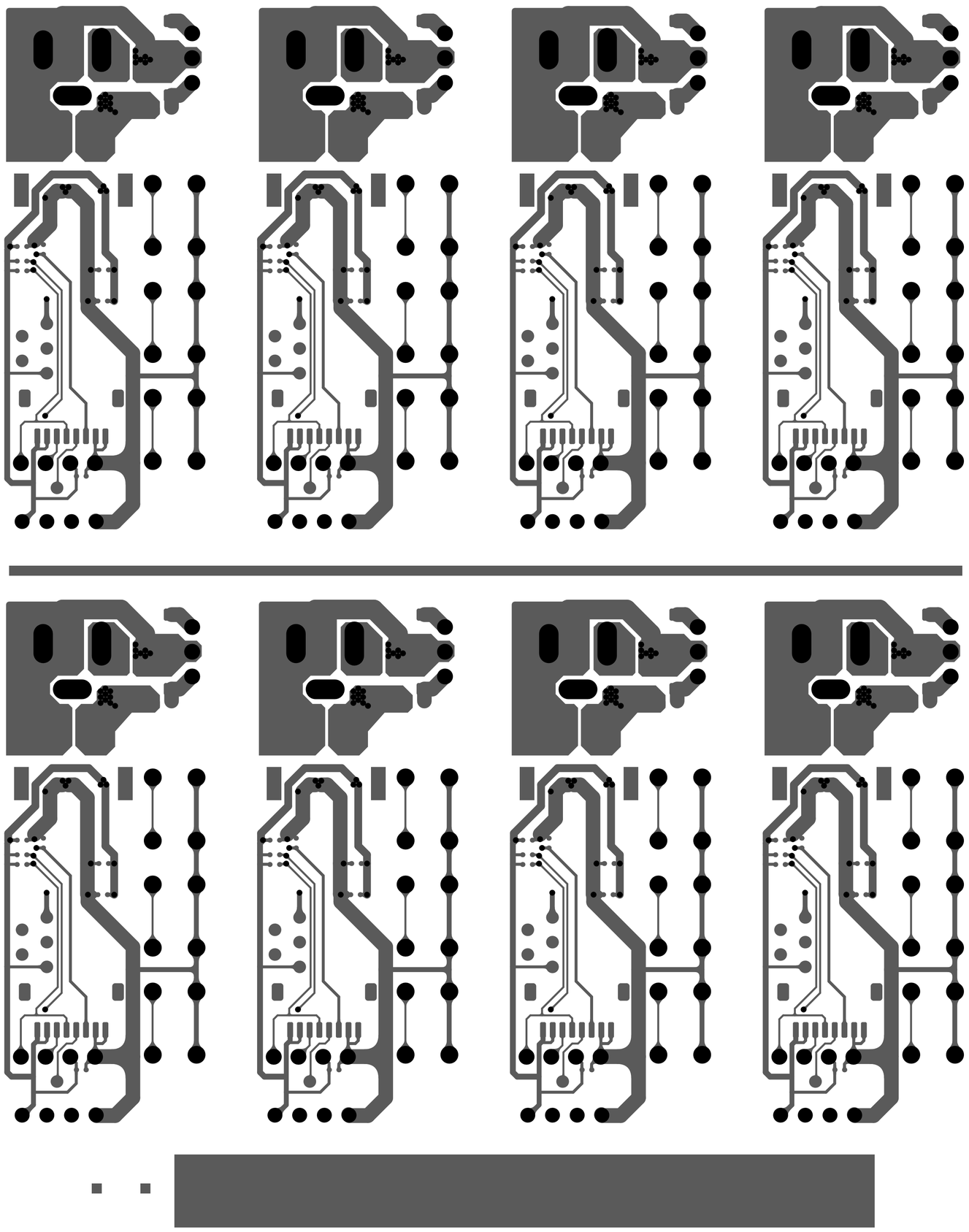
6A1.0 200420 100 x 137mm



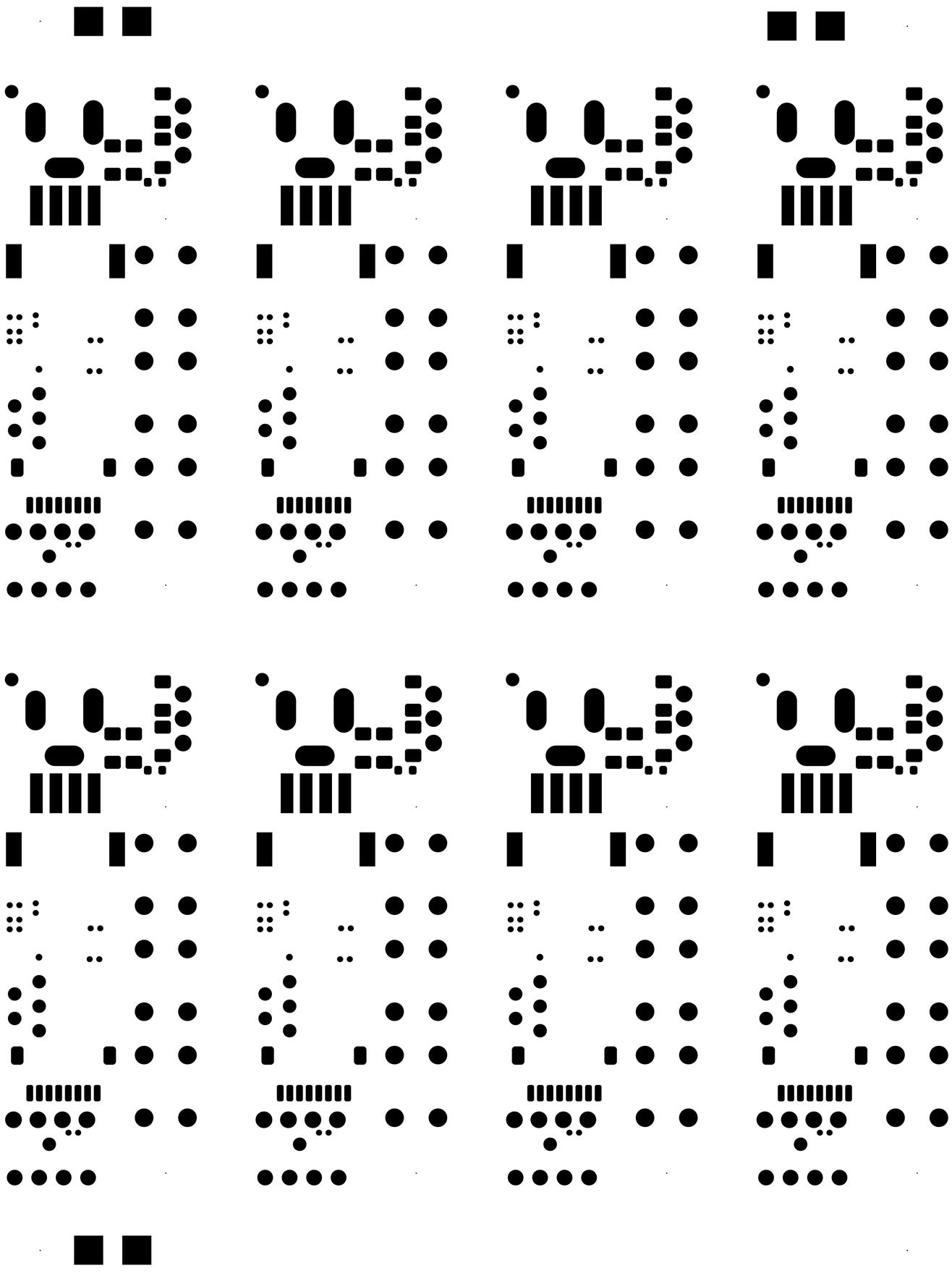
L1_SolderResist (GTS)



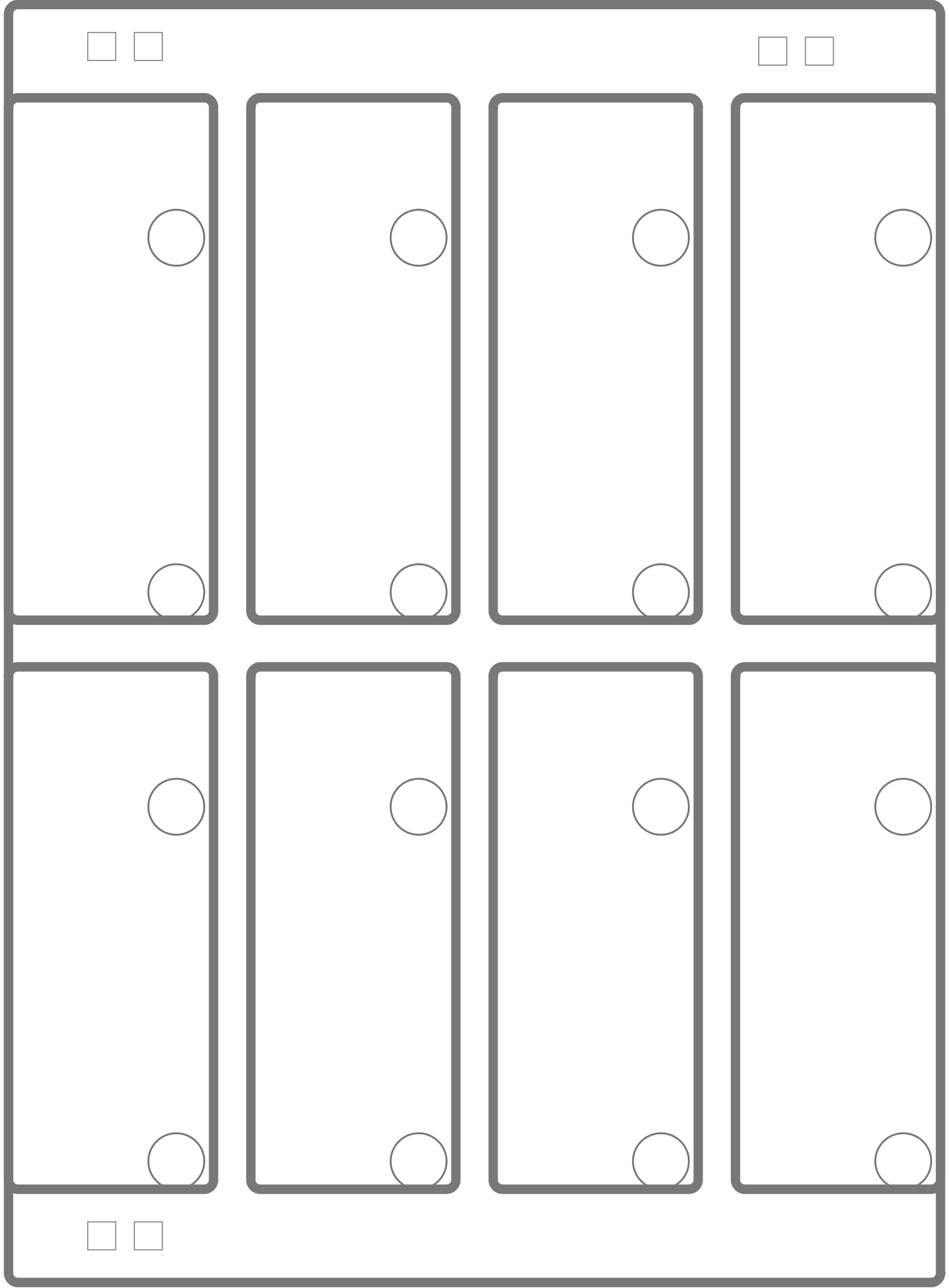
Layer1(GTL)

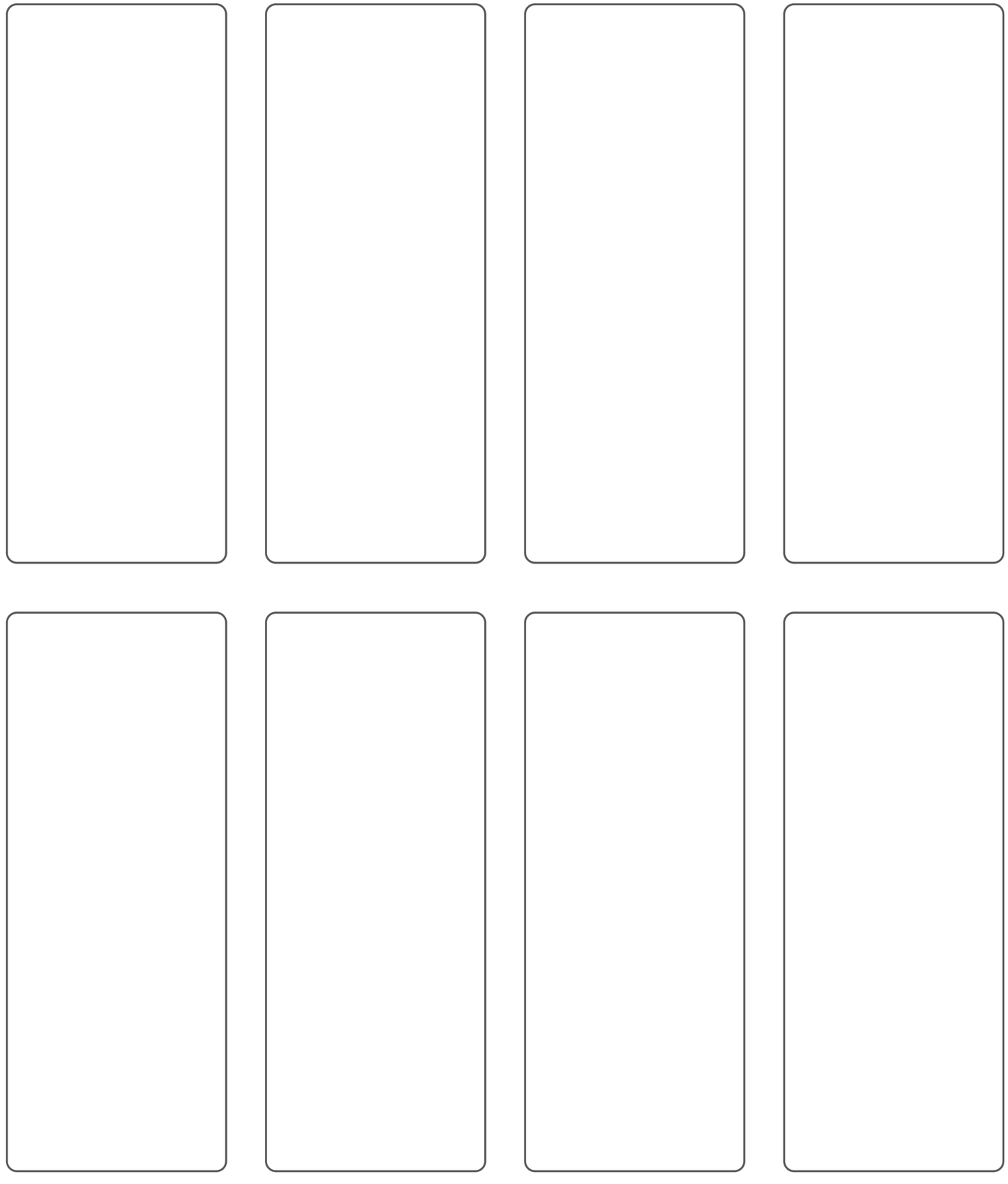


Layer2(GBL)



L2_SolderResist (GBS)



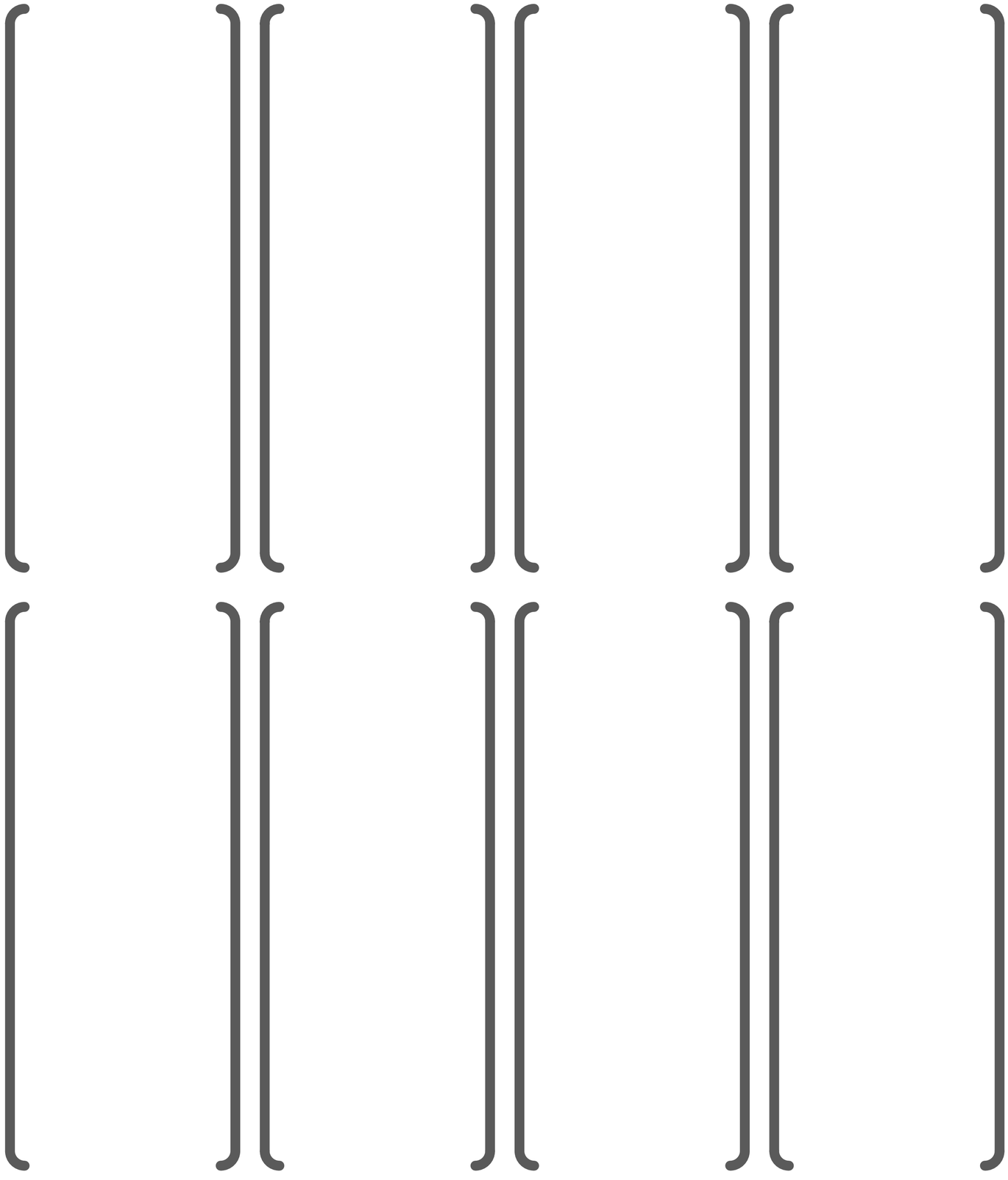


Outline (GML)

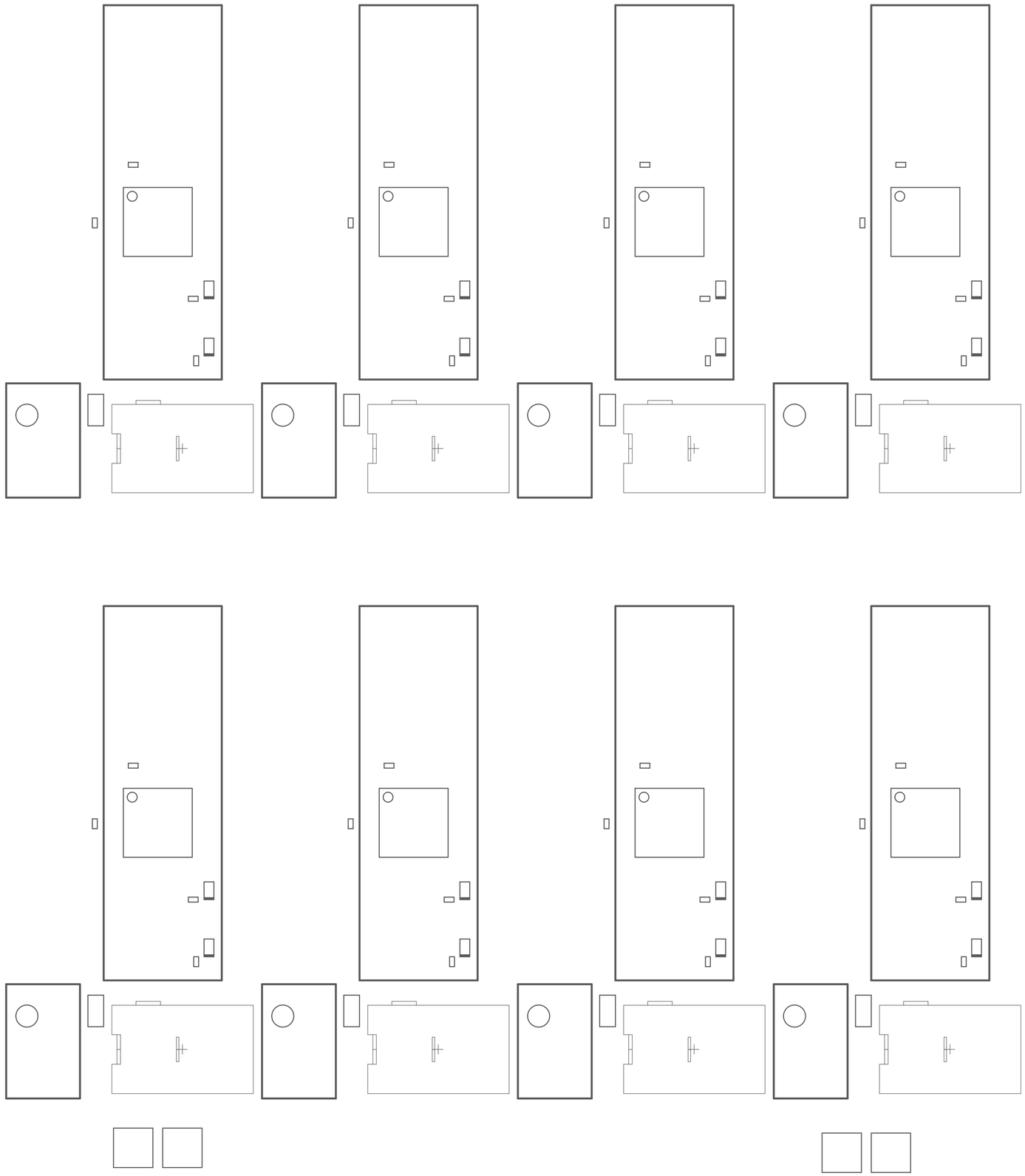
V-Scoring

V-Scoring

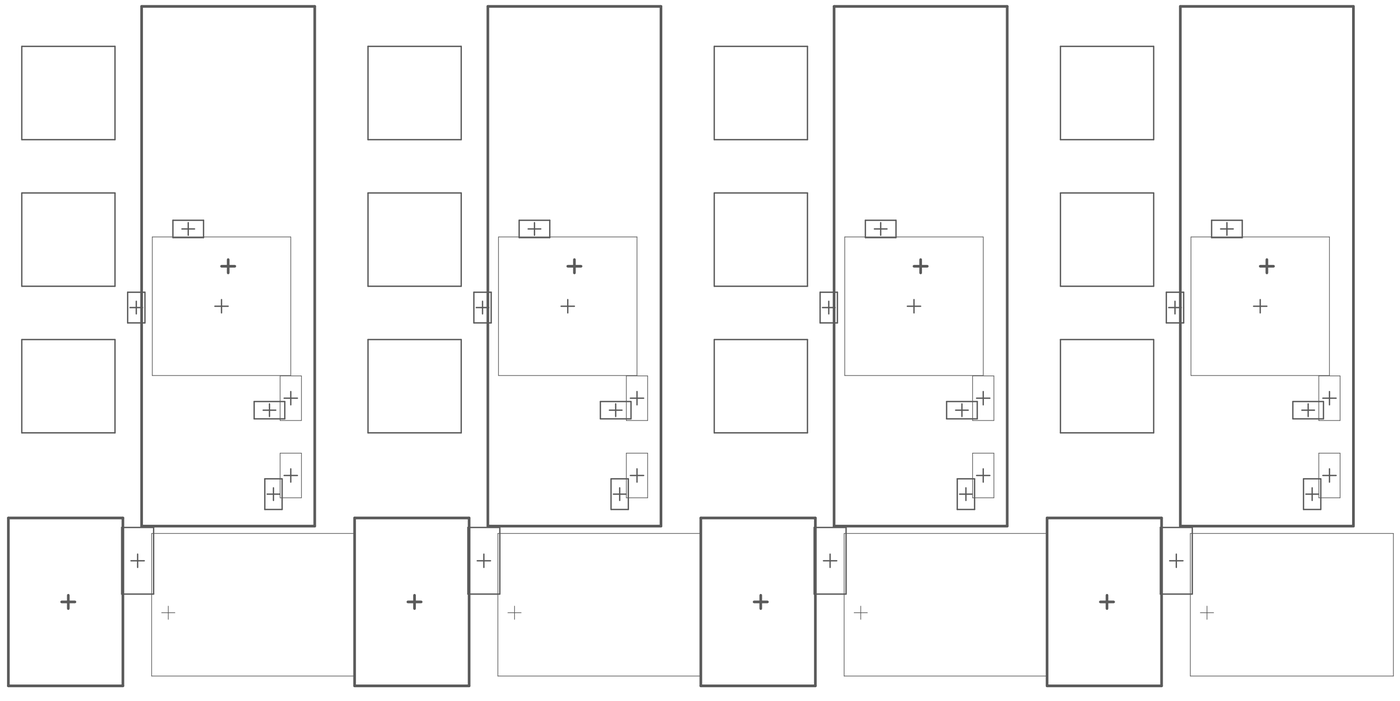
V-Scoring



NC-Router (GM4)

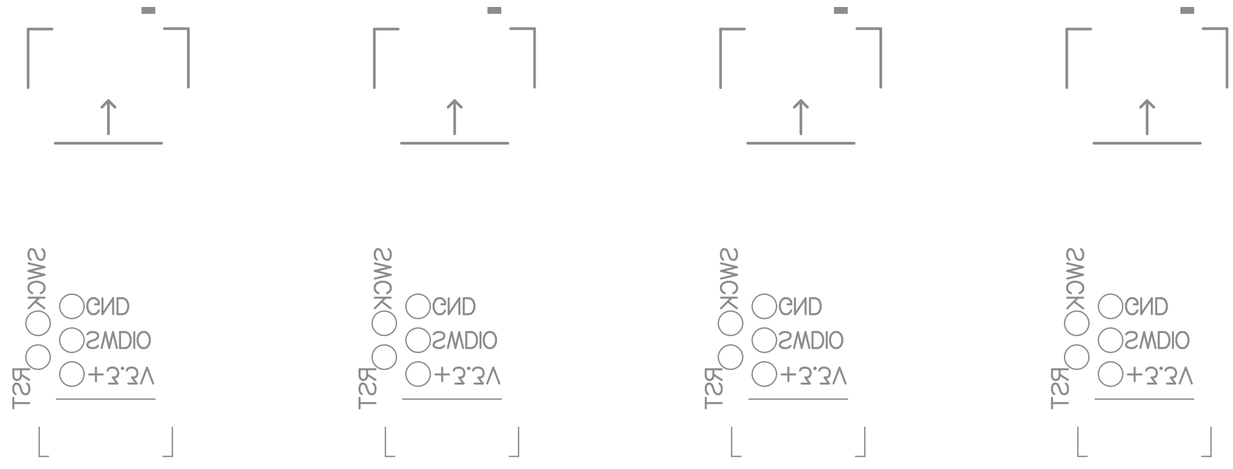


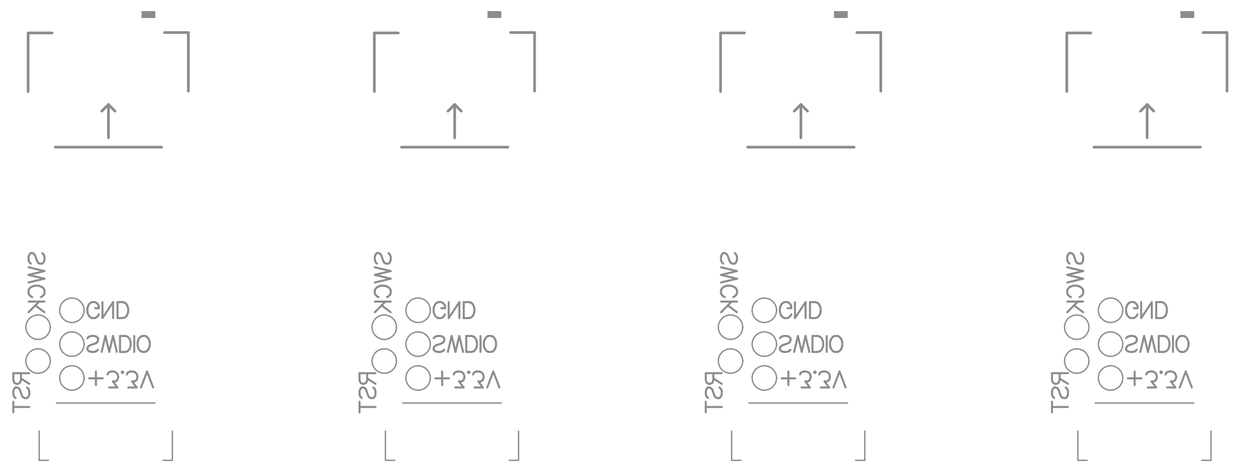
5mm



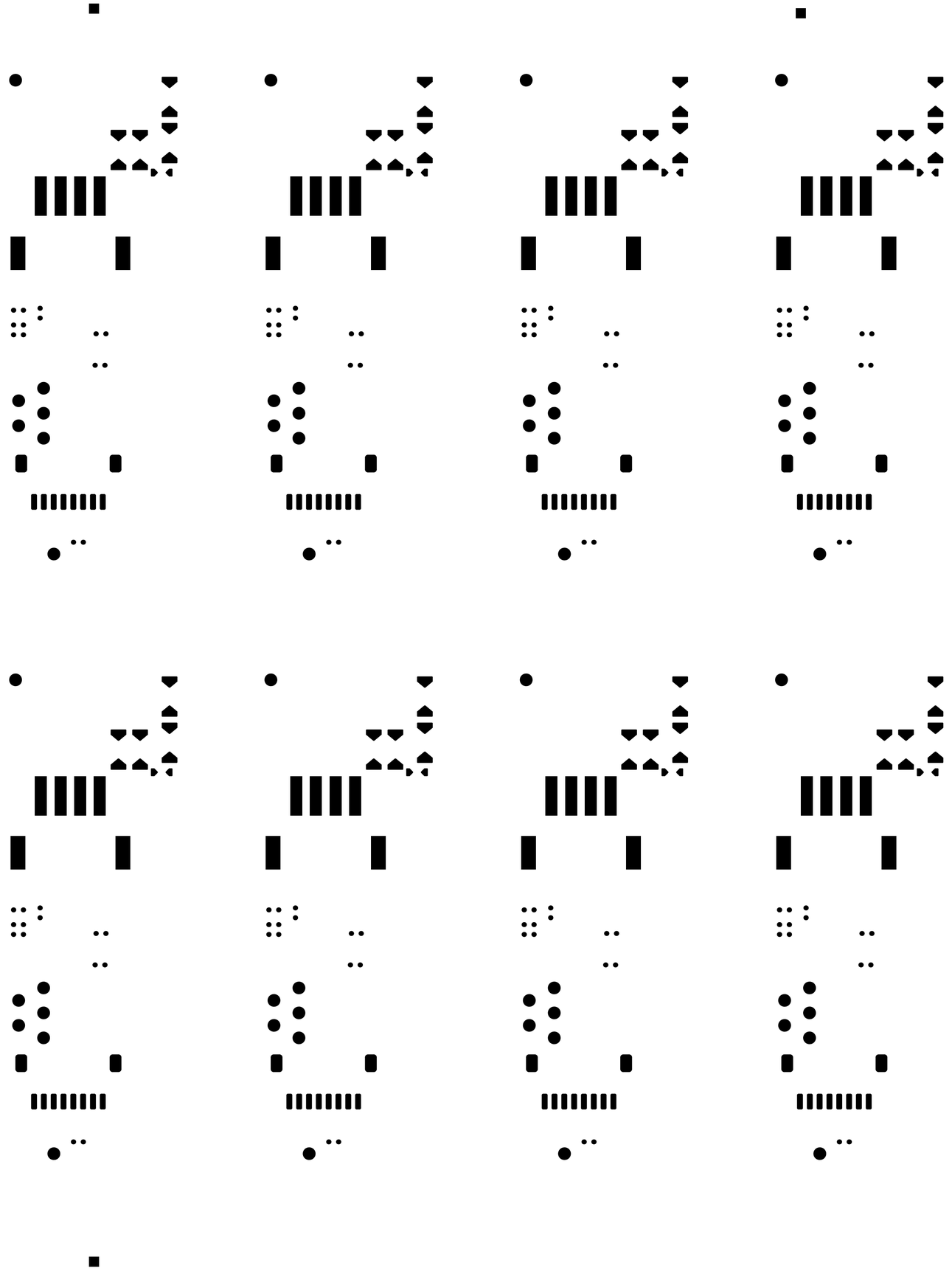
5mm

5mm


SWCK
GND
SWDIO
+3.3V
RST
SWCK
GND
SWDIO
+3.3V
RST
SWCK
GND
SWDIO
+3.3V
RST
SWCK
GND
SWDIO
+3.3V
RST


SWCK
GND
SWDIO
+3.3V
RST
SWCK
GND
SWDIO
+3.3V
RST
SWCK
GND
SWDIO
+3.3V
RST
SWCK
GND
SWDIO
+3.3V
RST

L2_Overlay



Bottom_Paste (GBP)