Junsang Cho

Contact Information:

Addr : 3 Comstock Circle Apt 101, Stanford, CA

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Interests:

Operating System, Database, Compiler, Network

Development experience:

Operating system (x86, ARM), Single User DBMS, Java Card virtual machine

Education:

2009-present – M.S candidate in Computer Science, Stanford University

2001-2004, 2008 – B.S in Electric Engineering, Seoul National University

Contest:

1999 – Seoul City Physics Olympiad – Bronze Medal

1998 - Korea Mathematic Olympiad – Silver Medal

Research:

Oct/2010 – Dec/2010 - Research Assistant at Stanford InfoLab

Development GUI for PANDA (A System for Provenance and Data)

Computer Skill:

C/C++, Java, Python, ARM programming

Employment:

2005-2007, 2010 - Solacia Inc, Seoul Korea

Development of supplemental OS for ARM TrustZone system in smart phone

Development of Java Card virtual machine & smart card OS

Class & Personal Project Experience:

2010 - "cs346: Database System Implementation"
- Design & implementation of single user DBMS
- Index, Query processor & optimizer

in Stanford University

2010 - Development of small x86 operating system
- Design & implementation of small x86 operating system
- Virtual memory management, Multi-process system, Dynamic linking, Logging style file system are implemented from scratch

Personal Project

Professional Project Experience:

Summer 2010 - Development of operating system for ARM TrustZone system
- Design & implementation of operating system on ARM Cortex A8 platform
- Supplemental operating system for security of smart phone

in Solacia Inc, Seoul Korea

2009 - Development of java card 3.0 virtual machine
- Design & implementation Java Card 3.0 virtual machine
- Design & implementation base operating system
- Anti-tearing memory management for RAM & NOR Flash & NAND Flash, multi threading, NAND flash file system, etc
- Target system: ARM core, 256KB NOR Flash, 32KB RAM, 16MB NAND Flash,

in Solacia Inc, Seoul Korea

2007 - Development of java card virtual machine
- Design & implementation of commercial Java Card 2.2.1 virtual machine
- Design & implementation of smart card operating system
- Anti-tearing memory management for RAM & NOR Flash, concurrent control of contact I/O (ISO 7816) and contactless I/O (ISO 14443)
- Target system: ARM core, 348KB NOR Flash, 8KB RAM, RF & serial I/O

in Solacia Inc, Seoul Korea