Property of Lite-on Only

FEATURES

- * Isolation voltage between input and output V_{iso} : 5,000 V_{rms}
- * 6pin DIP zero-cross optoisolators triac driver output
- * High repetitive peak off-state voltage V_{DRM}: Min. 600V
- * High critical rate of rise of off-state voltage

 $(dv/dt : MIN. 1000V / \mu s)$

* Dual-in-line package:

MOC3063

* Wide lead spacing package:

MOC3063M

* Surface mounting package:

MOC3063S

* Tape and reel packaging:

MOC3063S-TA1

- * UL approved (No. E113898)
- * CSA approved (No. CA91533-1)
- * FIMKO approved (No. 15469)
- * NEMKO approved (No. P00102123)
- * DEMKO approved (No. 309968-01)
- * SEMKO approved (No. 0032019/01-11)
- * VDE approved (No. 094722)
- * RoHS compliance

APPLICATIONS

- * AC Motor Drives
- * AC Motor Starters
- * E.M. Contactors
- * Lighting Controls
- * Solenoid/Valve Controls
- * Solid State Relays
- * Static Power Switches
- * Temperature Controls

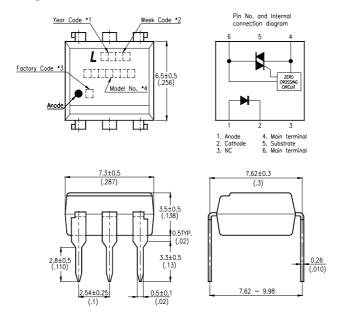
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BNS-OD-C131/A4

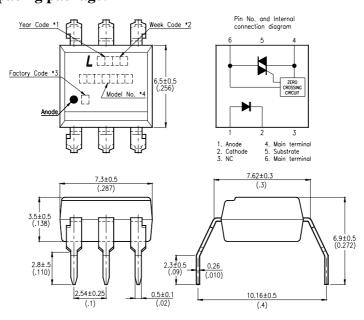
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OUTLINE DIMENSIONS

Dual-in-line package:



Wide lead spacing package:



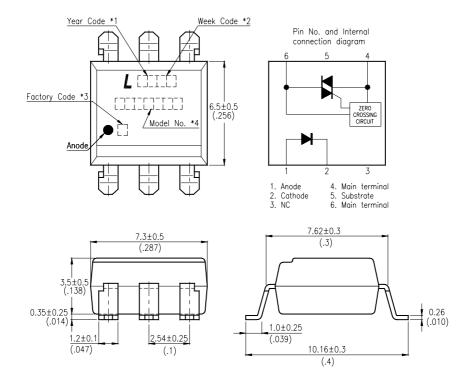
- *1. Year date code.
- *2. 2-digit work week.
- *3. Factory identification mark shall be marked (Z: Taiwan, Y: Thailand).
- *4. Model No.: MOC3063

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OUTLINE DIMENSIONS

Surface mounting package:



- *1. Year date code.
- *2. 2-digit work week.
- *3. Factory identification mark shall be marked (Z: Taiwan, Y: Thailand).
- *4. Model No.: MOC3063

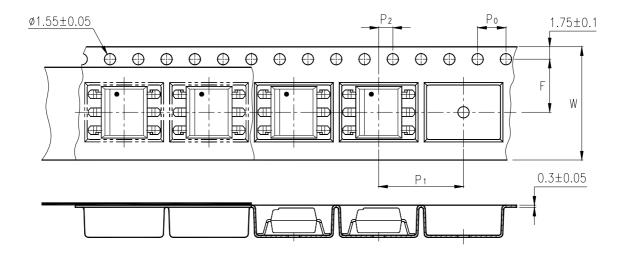
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TAPING DIMENSIONS

Tape and reel package:

MOC3063S-TA1



Description	Symbol	Dimensions in mm (inches)		
Tape wide	W	16 ± 0.3 (.63)		
Pitch of sprocket holes	P ₀	4 ± 0.1 (.15)		
Distance of compartment	F	$7.5 \pm 0.1 \; (.295)$		
Distance of compartment	P2	$2 \pm 0.1 \; (.079)$		
Distance of compartment to compartment	P ₁	$12 \pm 0.1 (.472)$		

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ABSOLUTE MAXIMUM RATING

 $(Ta = 25^{\circ}C)$

PARAMETER		SYMBOL	RATING	UNIT
	Forward Current	IF	50	mA
INPUT	Reverse Voltage	VR	6	V
	Power Dissipation	P_{D}	120	mW
Off-State Output Terminal Voltage		$V_{ m DRM}$	600	V
OUTPUT	Peak Repetitive Surge Current (PW=100μs, 120pps)	Itsm	1	A
	Collector Power Dissipation	Pc	150	mW
Total Power Dissipation		P _{tot}	250	mW
*1 Isolation Voltage		Viso	5,000	Vrms
Ambient Operating Temperature Range		T _A	T_A -40 ~ +100	
Storage Temperature Range		$T_{ m stg}$	-55 ~ +150	°C
*2 Soldering Temperature		T_{L}	260	°C

*1. AC For 1 Minute, R.H. = $40 \sim 60\%$

Isolation voltage shall be measured using the following method.

- (1) Short between anode and cathode on the primary side and between collector, emitter on the secondary side.
- (2) The isolation voltage tester with zero-cross circuit shall be used.
- (3) The waveform of applied voltage shall be a sine wave.
- *2. For 10 Seconds

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Property of Lite-on Only

ELECTRICAL - OPTICAL CHARACTERISTICS

 $(Ta = 25^{\circ}C)$

PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
INPUT	Forward Voltage	VF	_	1.2	1.4	V	I _F =20mA
	Reverse Current	IR	_	0.05	10	μА	V _R =6V
	*1 Peak Blocking Current, Either Direction	I_{DRM1}	_	_	500	nA	$V_{DRM} = 600V$
OUTPUT	Peak On-State Voltage, Either Direction	V_{TM}	_	_	3.0	V	I _{TM} =100 mA Peak
	*2 Critical rate of Rise of Off-Stat Voltage	dv/dt	1000	_	_	V/µs	
COUPLED	*3 Led Trigger Current, Current Required to Latch Output, Either Direction	B I _{FT}	_	_	5	mA	Main Terminal Voltage = 3V
	Holding Current, Either Direction	I_{H}	_	400	_	μΑ	
	Turn-On Time	T _{ON}	_	8	20	μs	$V_P=9V$, $I_F=20mA$ $R_L=100\Omega$
ZERO CROSSING	Inhibit Voltage	V _{INH}	_	5	20	Volts	I _F =Rated I _{FT} , MT1-MT2 Voltage above which device will not trigger.
	Leakage in Inhibited State	I_{DRM2}	_	_	500	μА	$\begin{split} &I_F = Rated \ I_{FT}, \ Rated \\ &V_{DRM}, \ Off \ State \end{split}$

^{*1} Test voltage must be applied within dv/dt rating.

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^{*2} This is static dv/dt. Commutating dv/dt is a function of the load-driving thyristor(s) only.

^{*3} All devices are guaranteed to trigger at an I_F value less than or equal to max I_{FT} . Therefore, recommended operating I_F lies between max I_{FT} 5mA for MOC3063 and absolute max I_F (50mA)

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CHARACTERISTICS CURVES

Fig.1 Forward Current vs.

Ambient Temperature

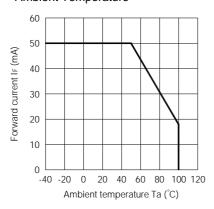


Fig.3 Minimum Trigger Current vs. Ambient Temperature

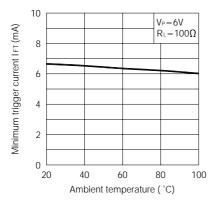


Fig.5 On-state Voltage vs. Ambient Temperature

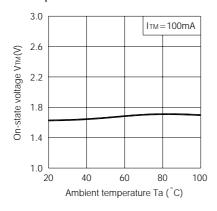


Fig.2 On-state Current vs. Ambient Temperature

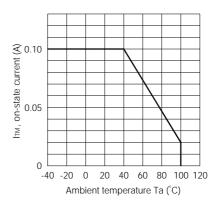


Fig.4 Forward Current vs. Forward Voltage

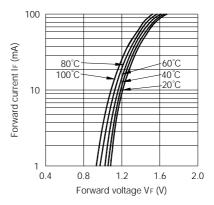
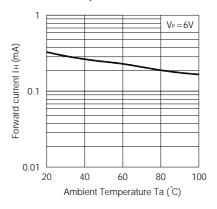


Fig.6 Holding Current vs.

Ambient Temperature



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Property of Lite-on Only

CHARACTERISTICS CURVES

Fig.7 Turn-on Time vs. Forward Current

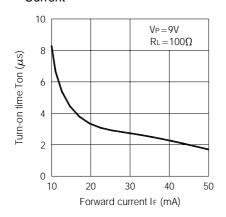


Fig.8 Repetitive Peak Off-state Current vs. Temperature

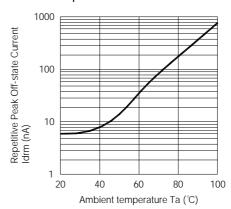
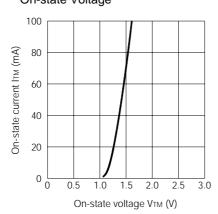
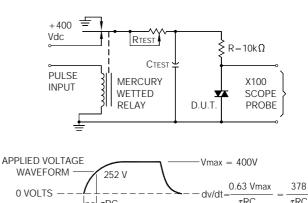


Fig.9 On-state Current vs. On-state Voltage



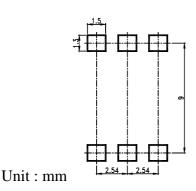
Static dv/dt Test Circuit

L- τRC





0 VOLTS -



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