

**F3** 

Faculty of Electrical Engineering Department of Measurement

**Bachelor's Thesis** 

# On-board computer for PC104 format CubeSats

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**Cybernetics and Robotics** 

January 2021

https://github.com/visionspacetec/VST104

Supervisor: Ing. Vojtěch Petrucha, Ph.D.

Draft: 15. 1. 2021

## **Acknowledgement / Declaration**

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In Prague on January 13, 2013

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#### Abstrakt / Abstract

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**Kľúčové slová:** CubeSat; PC104; OBC; hardvér; PCB dizajn; schémy.

**Preklad titulu:** Palubný počítač pre CubeSaty formátu PC104

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**Keywords:** CubeSat; PC104; OBC; hardware; PCB design; schematics.

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## **Requests for correction**

1 Draft: 15. 1. 2021