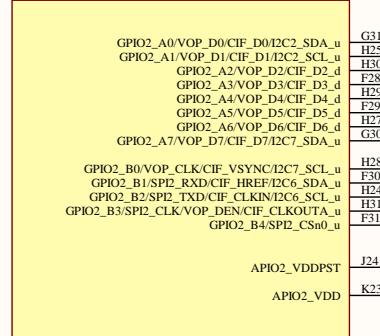
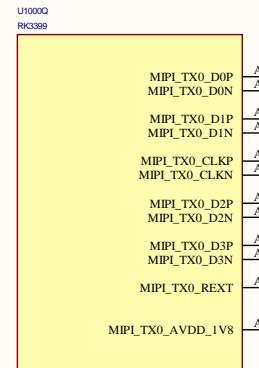
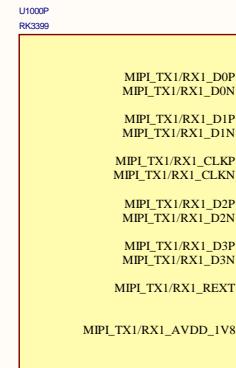
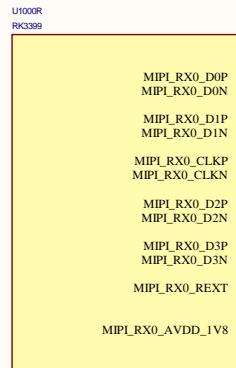
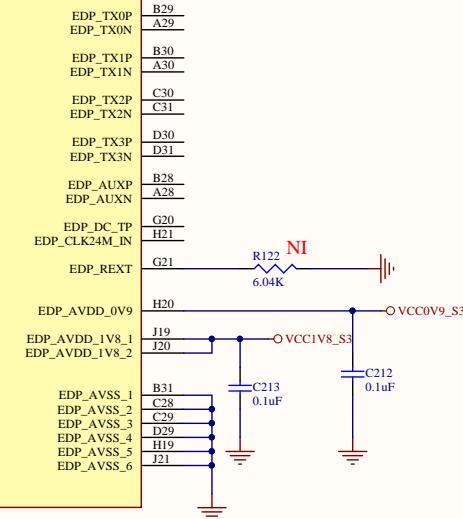
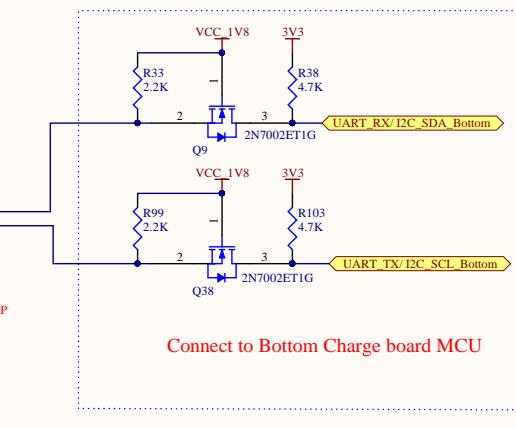
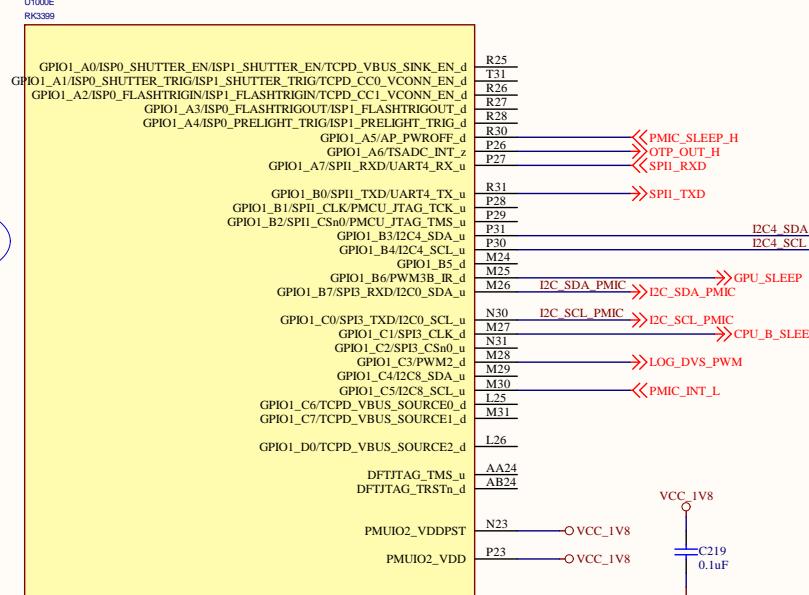
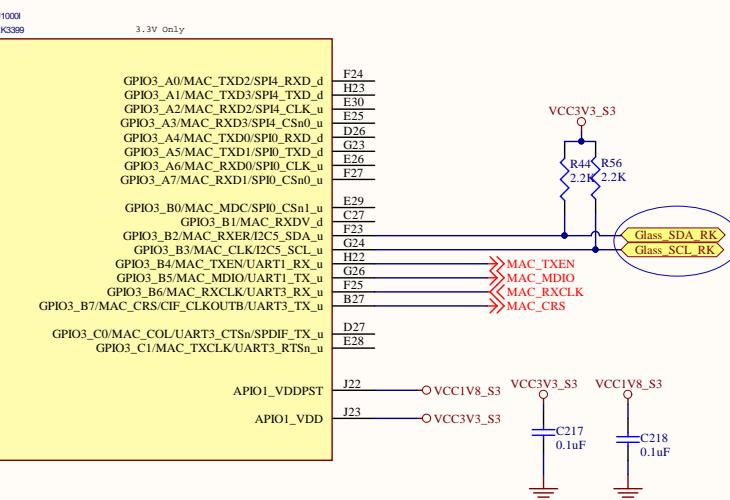


U1000L  
RK3399

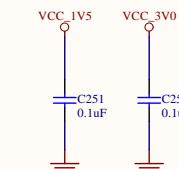
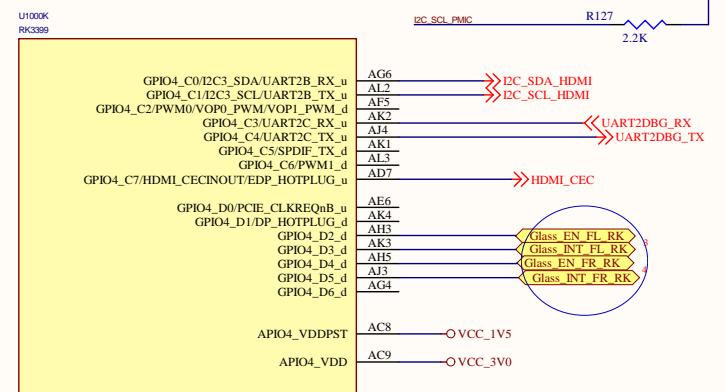
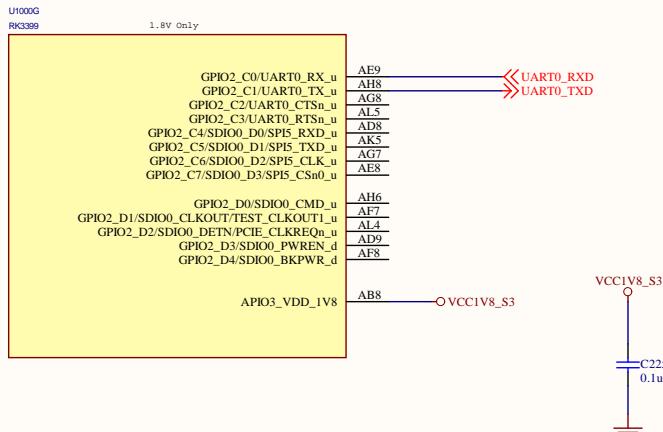
func1	func2	func3
GPIO2_A0/CIF_D0	I2C2_SDA	
GPIO2_A1/CIF_D1	I2C2_SCL	
GPIO2_A7/CIF_D7	I2C7_SDA	
GPIO2_B0/CIF_VSYNC	I2C7_SCL	
GPIO2_B1/CIF_HREF	SP12_RXD	I2C6_SDA
GPIO2_B2/CIF_CLKI	SP12_TXD	I2C6_SCL
GPIO2_B3/CIF_CLKO	SP12_CLK	
GPIO2_B4/DVP_PDN0_H	SP12_CSN	

U1000M  
RK3399VCC1V8\_S3  
C215 0.1uFVCC1V8\_S3  
C216 0.1uF

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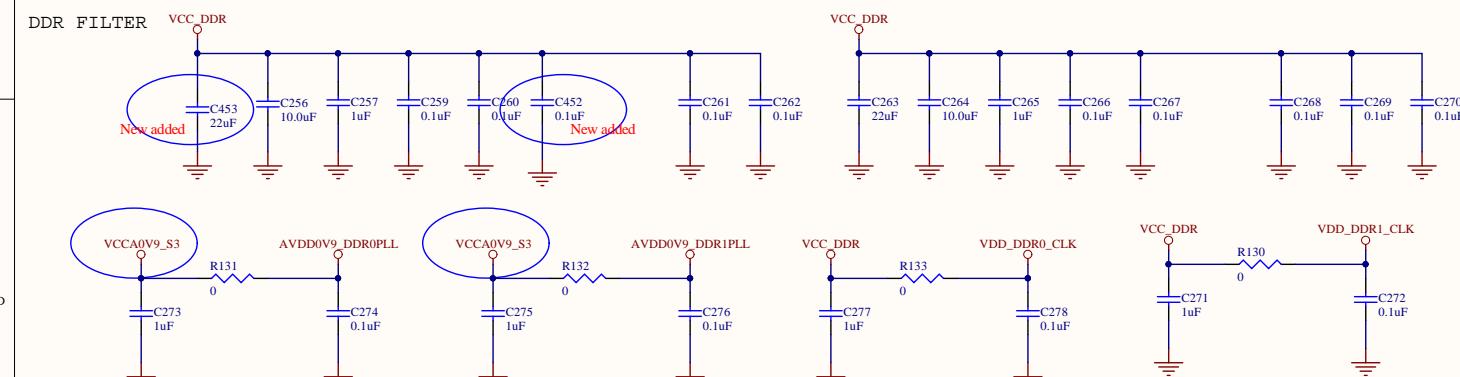
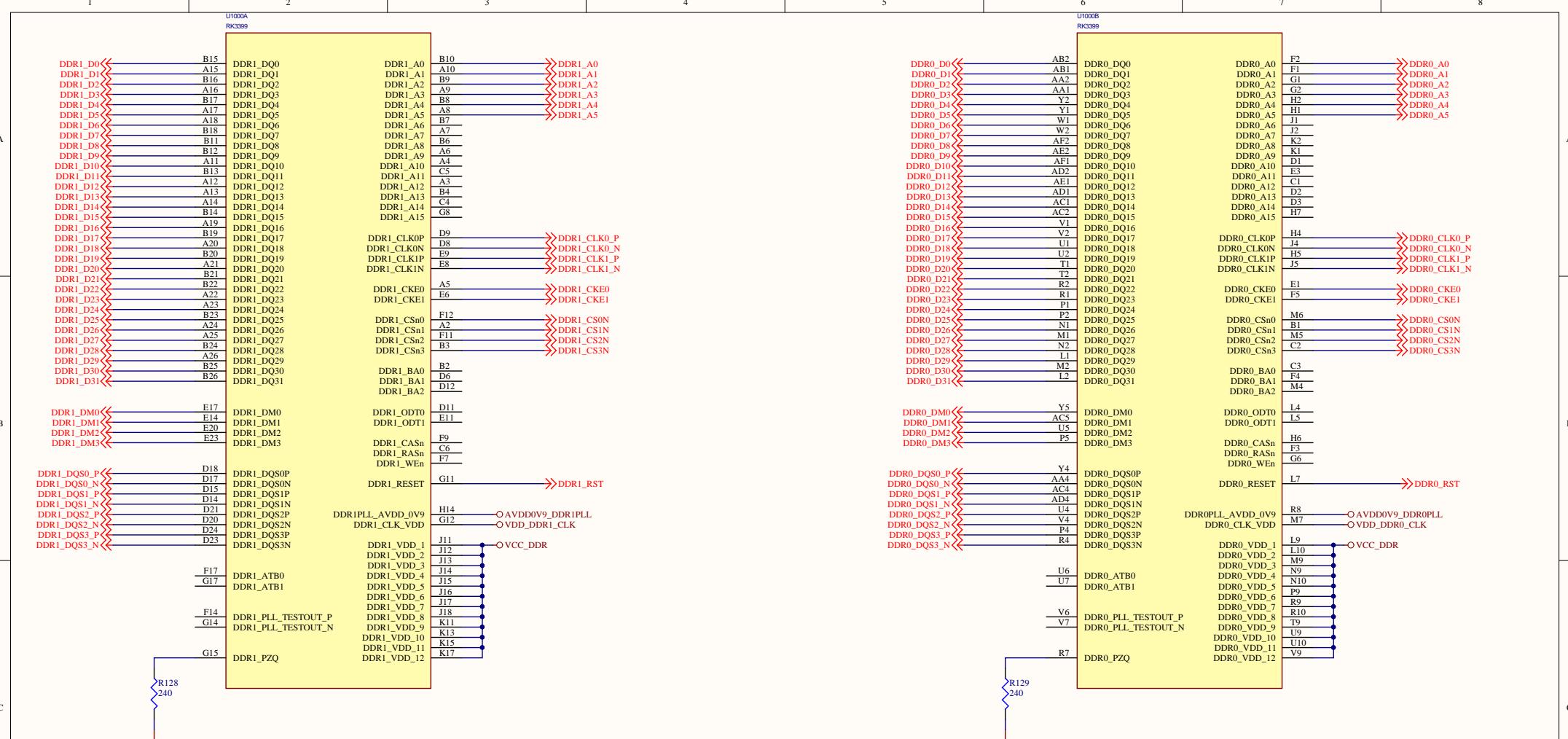


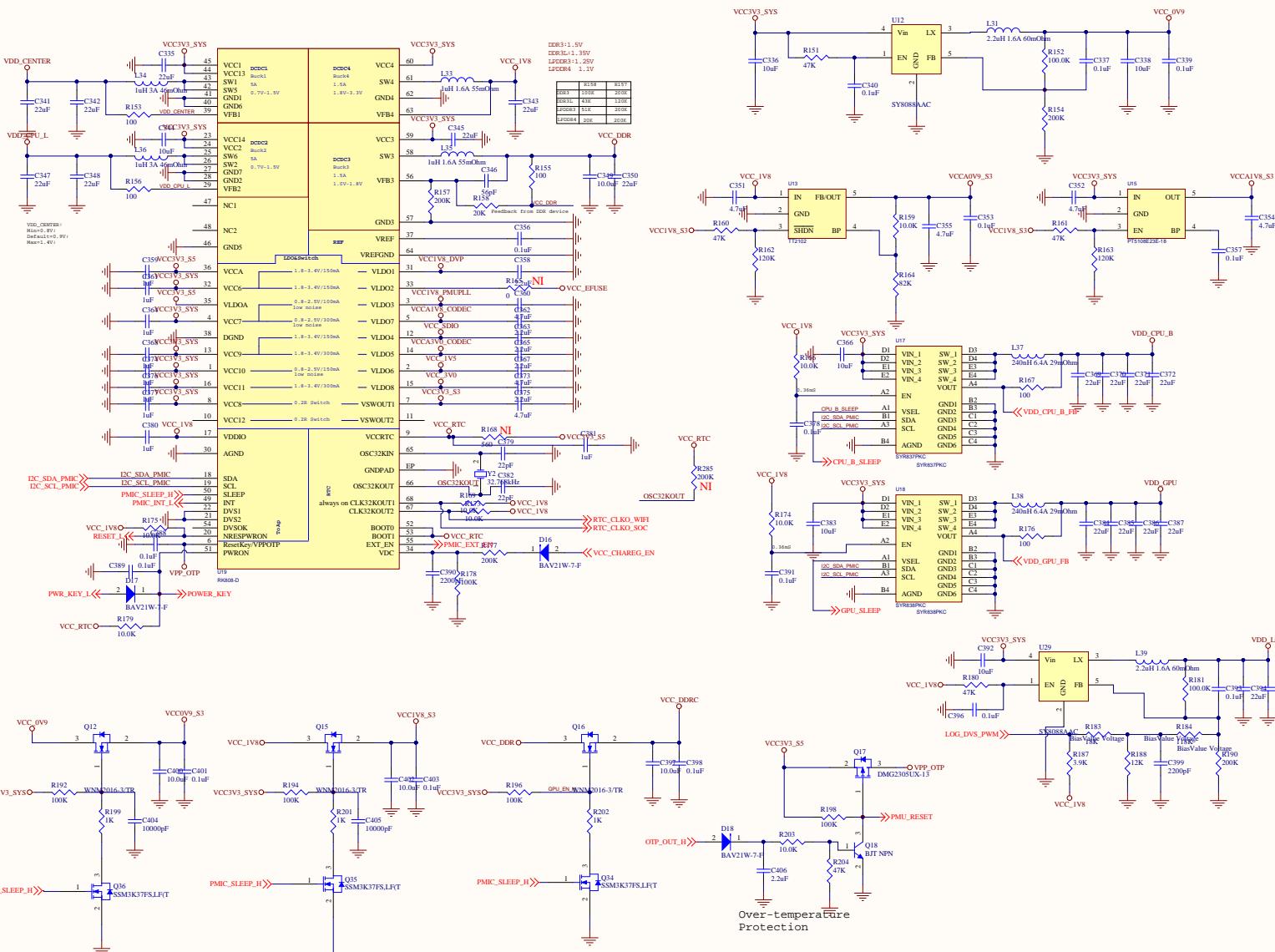
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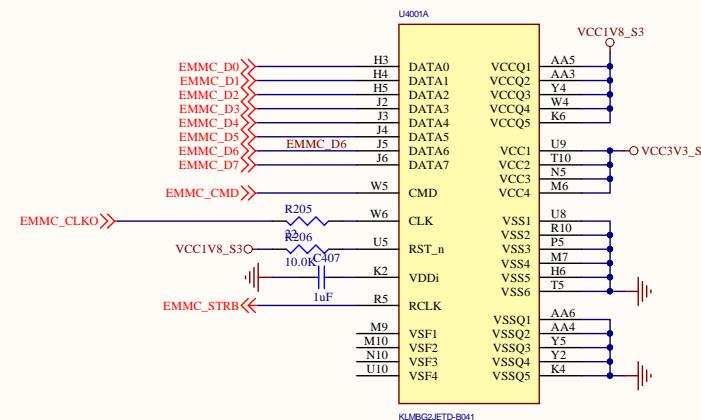
1.8V Only	VDDPST=VDDIO=1.8V
3.3V Only	VDDPST=1.8V, VDDIO=3.3V
other	3.3V mode: VDDPST=1.8V, VDDIO=3.3V 1.8V mode: VDDPST=1.8V, VDDIO=1.8V

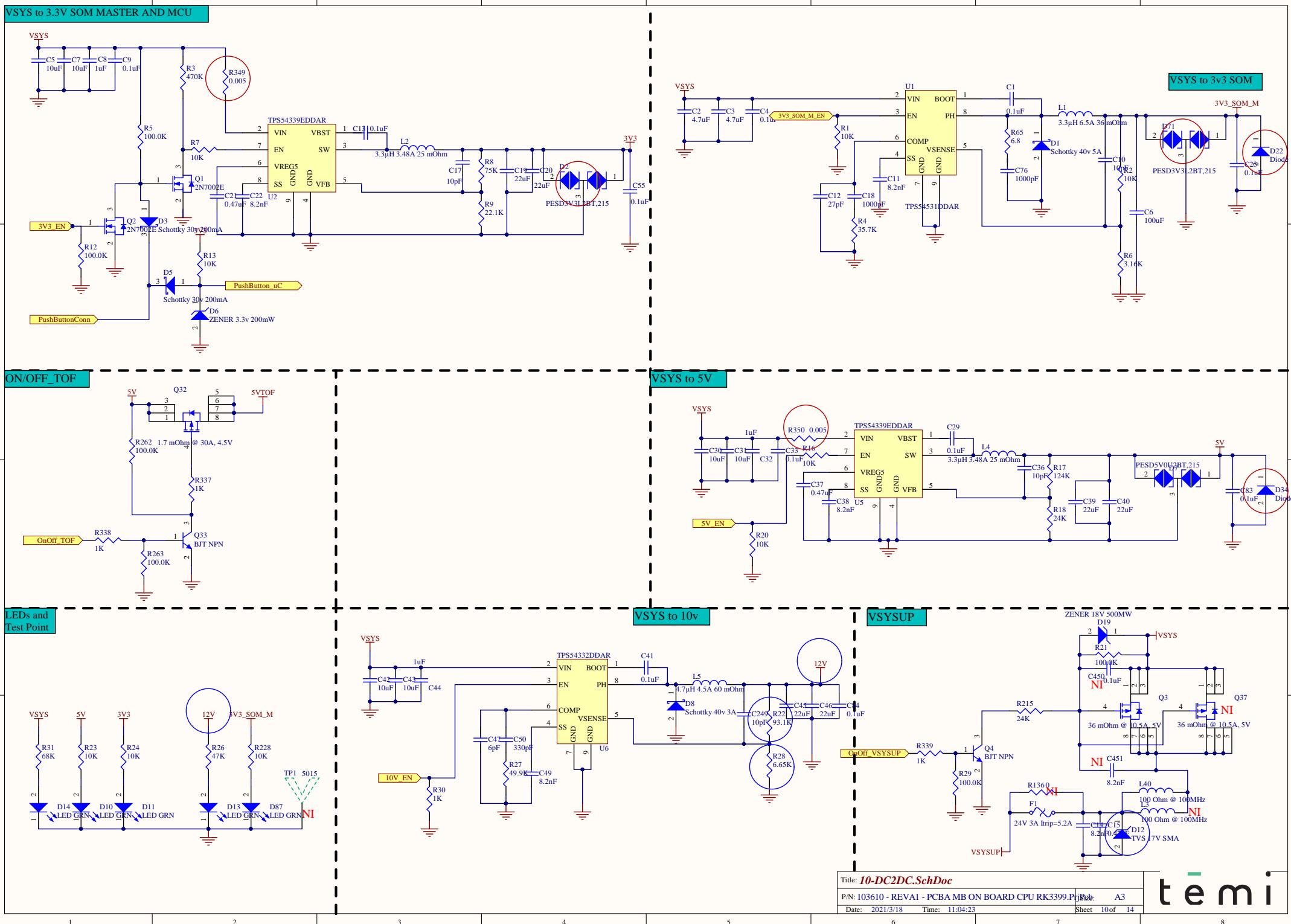


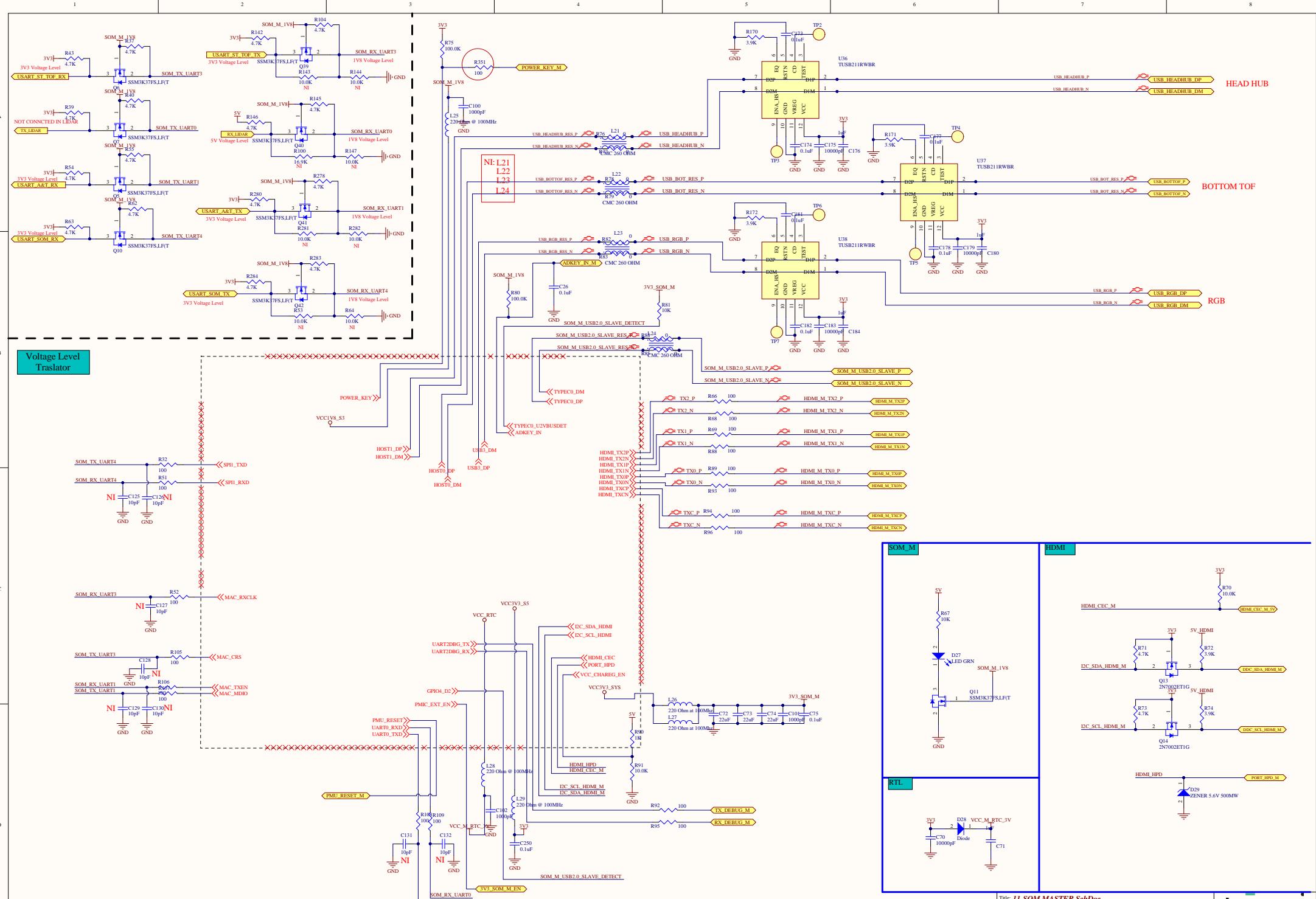


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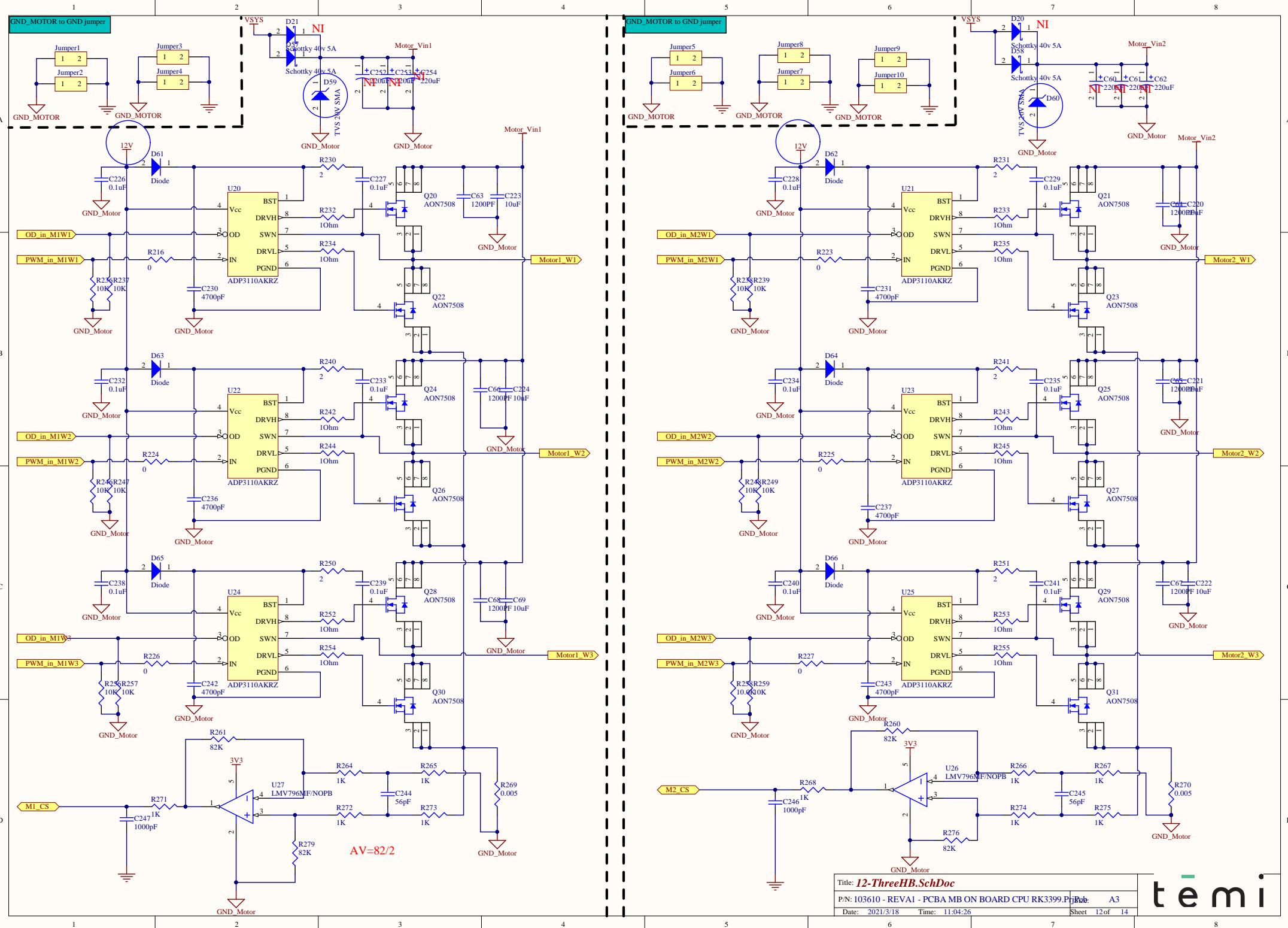


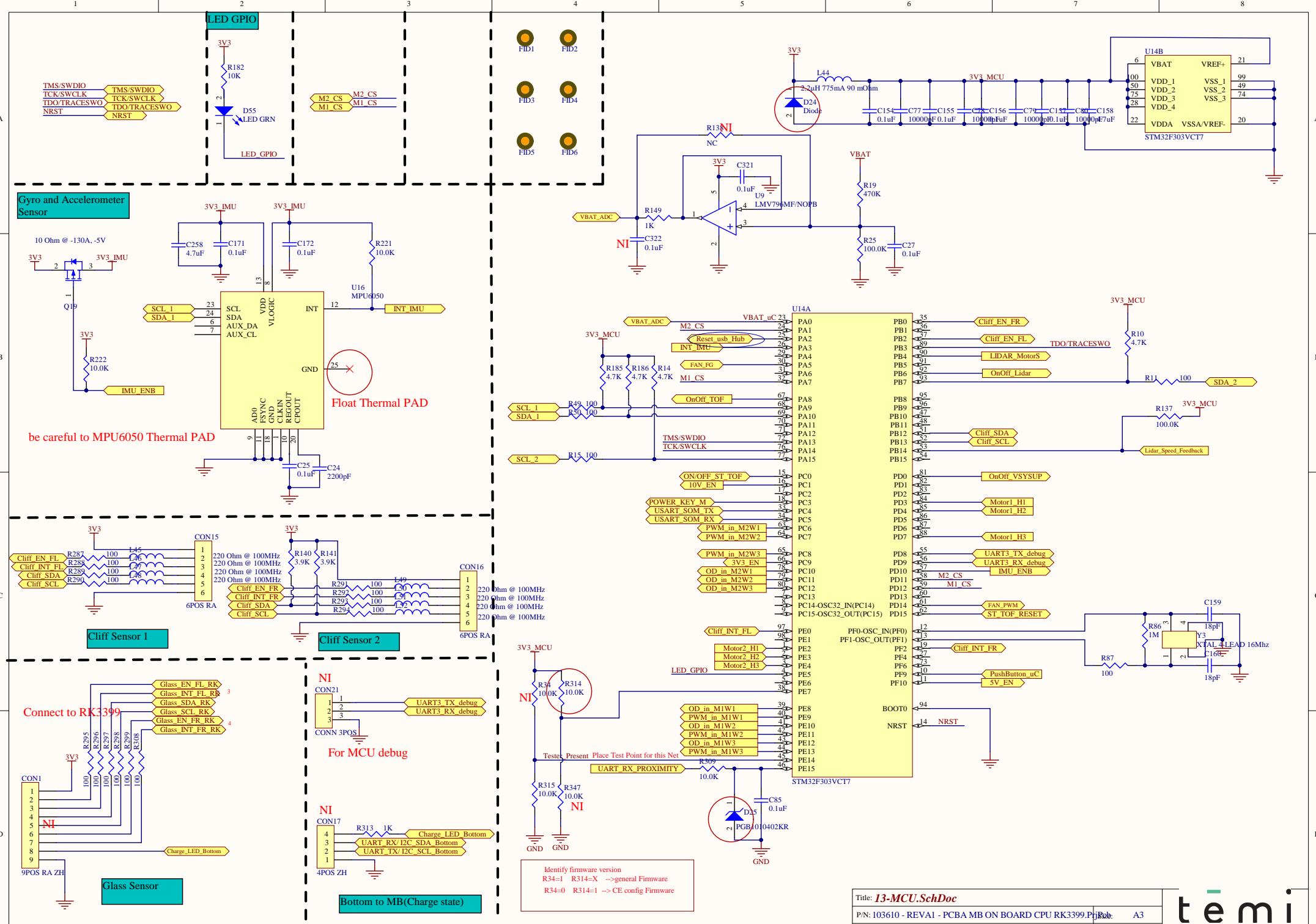


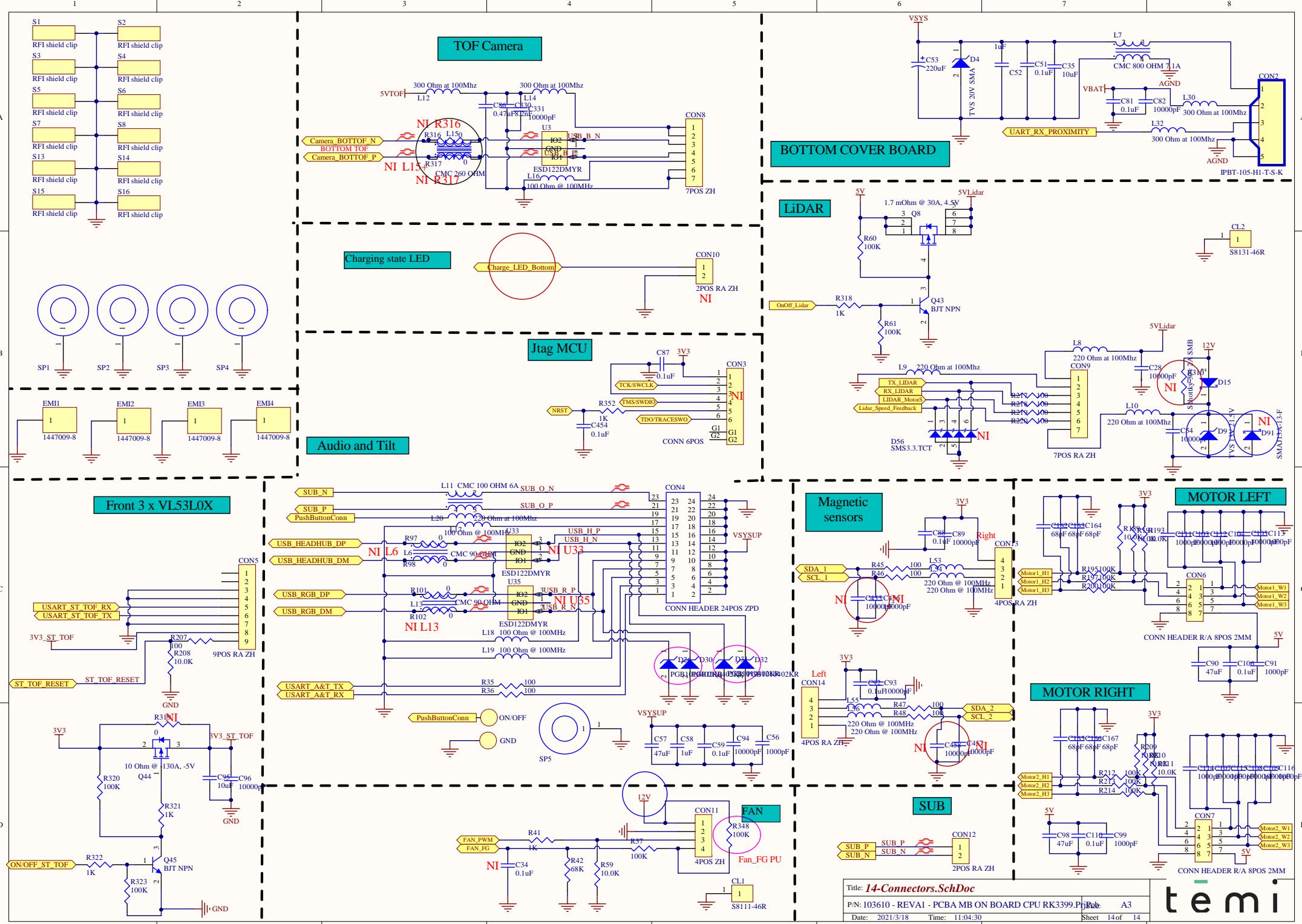
Title: **U-SOM MASTER SchDec**

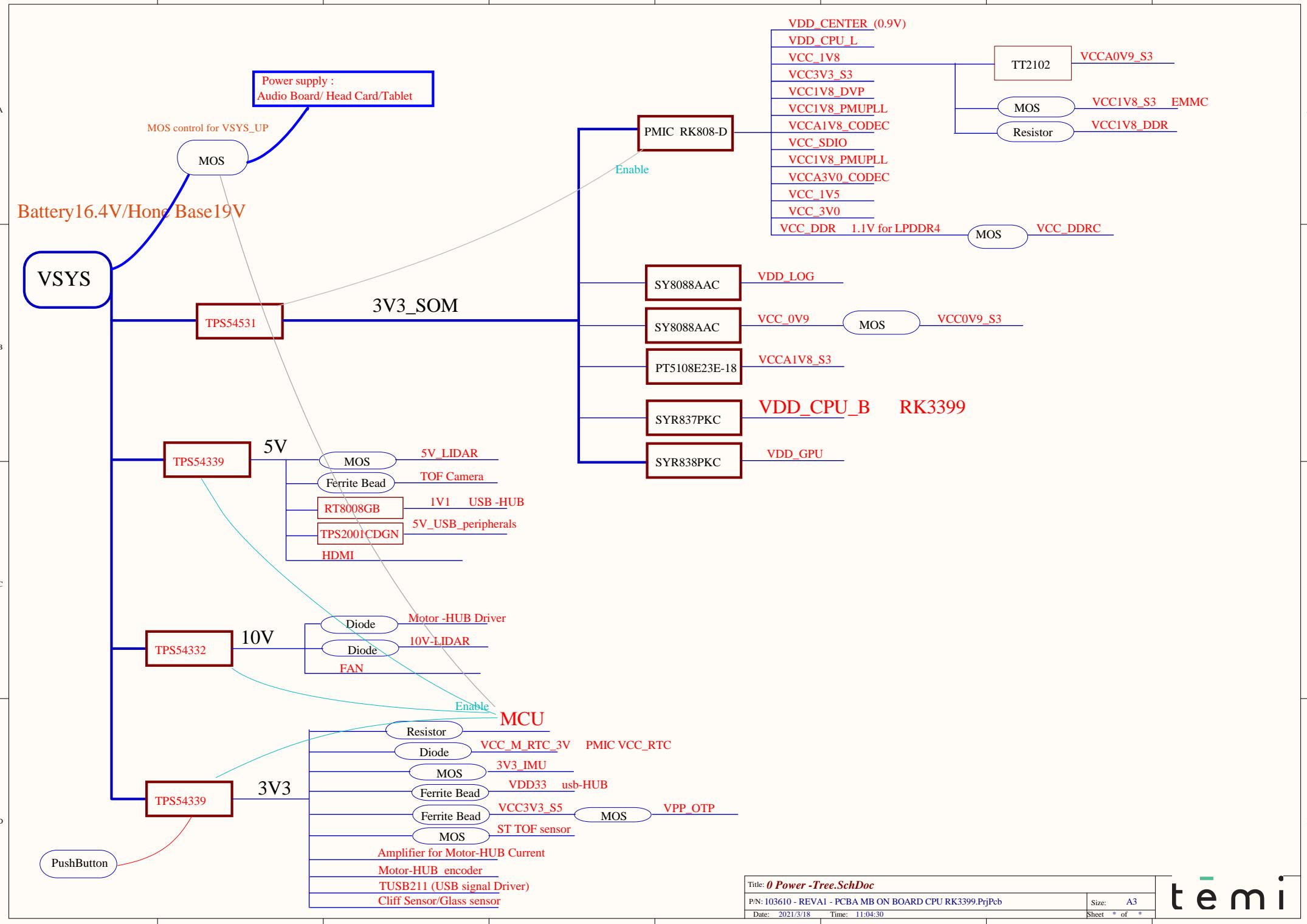
P/N: 103610 - REV A1 - PCBA MB ON BOARD CPU RK3399. P/N: P/N: A2  
Date: 2021/2/18 Time: 11:04:25 Element: 11-6-1

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<b>REV</b>	<b>DESCRIPTION</b>	<b>DATE</b>	<b>AUTHOR</b>
REV00	Add a Resettable Fuse to protect VSYSUP and add a resistor to protect the VSYSUP switch Q3's Vgs<12V C321 C305 NC in DDR_CLK_P,DDR_CLK_N Add one 0 resistor for all motor PWM control signals	5/5/2019	Charles wang
REV01	Change RAM to LPDDR4 ,configure VCC_DDR to 1.1V add a PU resistor in Lidar_Speed_Feedback	13/5/2019	Charles wang
	Add connector for Cliff dection/Battery charge Control	5/6/2019	Charles wang
REV02	Add Cliff sensor connector, USB-HUB HDMI connector and DEBUG ports,	8/11/2019	Charles wang
		18/03/2020	Charles wang
REV03	Reference Layer GND Copper Compensate for DDR	19/05/2020	Morris
REV04	Add Charging LED, add PU resistor to indicate firmware version add PU resistor for FAN FG signal	4/06/2020	Charles

B

B

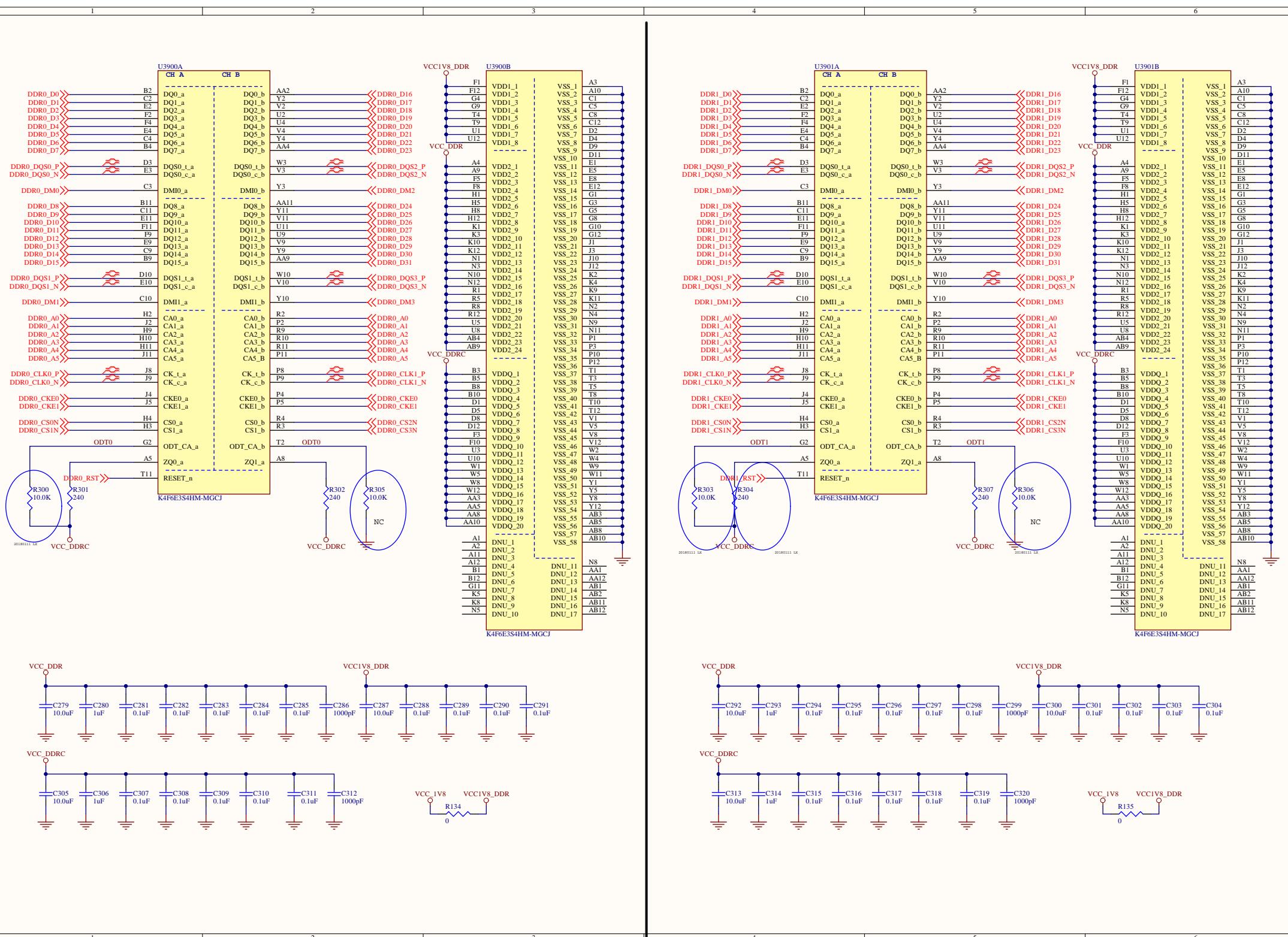
C

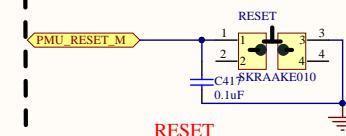
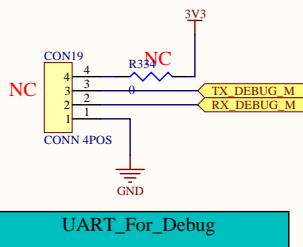
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Title		
Size A4	Number	Revision
Date: 2021/3/18		Sheet of
File: C:\Users\..00 - Change list.SchDoc		Drawn By:





**Buttons**

