Χ





gymk.tech@gmail.com >

NPTEL (https://swayam.gov.in/explorer?ncCode=NPTEL) » Cloud Computing (course)

Announcements (announcements) About the Course (https://swayam.gov.in/nd1_noc19_cs64/preview) Ask a Question (forum)

Progress (student/home) Mentor (student/mentor)

Unit 2 - Week 0 Assignment 0



| Week 1 | (c) (d) | |
|--------------------|--|---------|
| Text Transcription | 2) The IP address is/are used by hosts when they are being booted. (a) 1.1.1.1 (b) 1.0.0.0 (c) 0.0.0.0 (d) 255.255.255.255 | 1 point |
| | (a) (b) (c) (d) | |
| | Which of the following layer(s) of OSI Reference model is/are also called end-to-end layer? (a) Physical layer (b) Datalink layer (c) Network layer (d) Transport layer | 1 point |
| | (a) (b) (c) (d) | |
| | 4) Atomic operation(s) permissible on Semaphores is/are - (a) stop (b) wait (c) hold (d) signal | 1 point |
| | (a)✓ (b)(c) | |

(d)

| Suppose we want to download text documents at the rate of 100 pages per second. Assume that a page consists of an average of 25 lines with 95 characters in each line. What is the required bit rate of the channel? (a) 237.5 Mbps (b) 237.5 Kbps (c) 1.9 Mbps (d) 1.9 Kbps |
|--|
| (a) (b) ✓ (c) (d) |
| 6) |
| Which of the following is/are correct statement? (a) In memory - mapped I/O, the CPU can manipulate I/O data residing in interface registers that are not used to manipulate memory words. (b) The isolated I/O method isolates memory and I/O addresses so that memory address range is not affected by interface address assignment. (c) In asynchronous serial transfer of data the two units share a common clock. (d) In synchronous serial transmission of data the two units have different clocks. |
| □ (a)☑ (b)□ (c)□ (d) |
| 7) Which of the following addressing mode is/are best suited to access elements of an array of contiguous memory locations? (a) Indexed addressing mode (b) Base Register addressing mode (c) Relative address mode (d) Displacement mode |

| ✓ (a)(b)(c)(d) | |
|---|---------|
| 8) Match the following: List – I 1. Data link layer 2. Network layer 3. Transport layer 4. Presentation layer List – II i. Encryption ii. Connection control iii. Routing iv. Framing | 1 point |
| (a) 1. (iv), 2. (iii), 3. (i), 4. (ii) (b) 1. (iii), 2. (iv), 3. (ii), 4. (i) (c) 1. (iv), 2. (ii), 3. (iii), 4. (i) (d) 1. (iv), 2. (iii), 3. (ii), 4. (i) | |
| (a)(b)(c)✓ (d) | |

The address of a class B host is to be split into subnets with a 6 - bit subnet number. What is the maximum number of subnets and maximum number of hosts in each subnet?

- (a) 64 subnets and 1024 hosts
- (b) 64 subnets and 256 hosts
- (c) 62 subnets and 1022 hosts
- (d) 62 subnets and 254 hosts

| (a)(b)✓ (c)(d) |
|---|
| 10) Which module gives control of the CPU to the process selected by the short-term scheduler? 1 point (a) Dispatcher (b) Interrupt (c) Scheduler (d) Threading |
| ✓ (a)□ (b) |
| (c) (d) |
| You may submit any number of times before the due date. The final submission will be considered for grading. Check Answers |
| Submit Answers |