

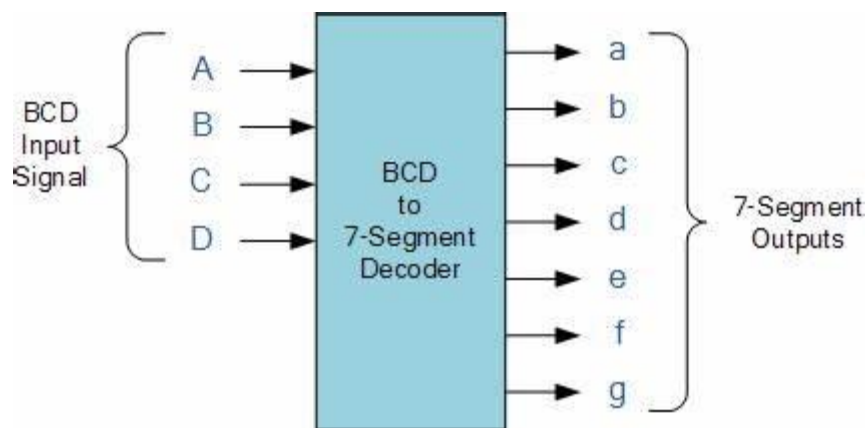
ASSIGNMENT WEEK 7

Problem 1. Use **Behavioral model** to design circuits. Simulate your designs on ModelSim. Create code coverage report, and waveform. Screenshot your results.

1. Encoder 8 – 3



2. Design Majority 4 bit circuit.
3. Design BCD to 7 – segment Decoder



4. Design flip flops: D – FF, T – FF, SR – FF, JK – FF
 - a. Use Synchronous reset.
 - b. Use Asynchronous reset.

How does these synthesis (Lecture 4).

(Construct truth table for flip flops).

Problem 2. Answer the questions:

Describe the **initial statement** and **always statement** in Behavioral Verilog