

Spirit Level Reaction Time Tester

Group 52

Asmita Limaye

Athreya Krishnan

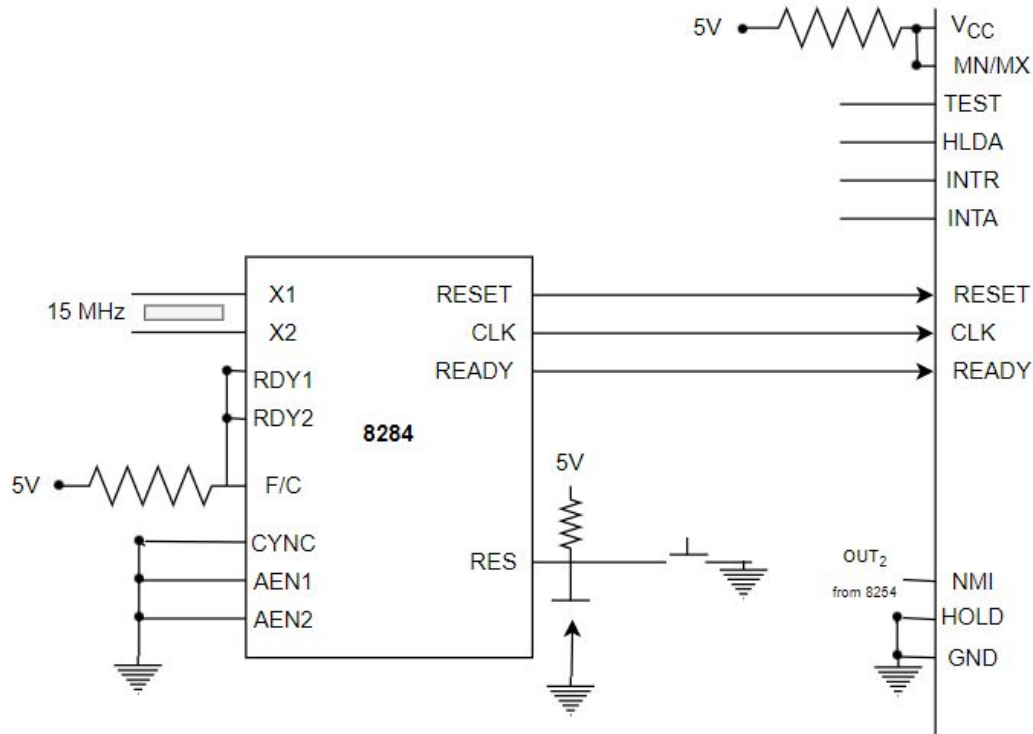
S. K. Anusri

Prabodh Kant Rai

Saurav Shukla

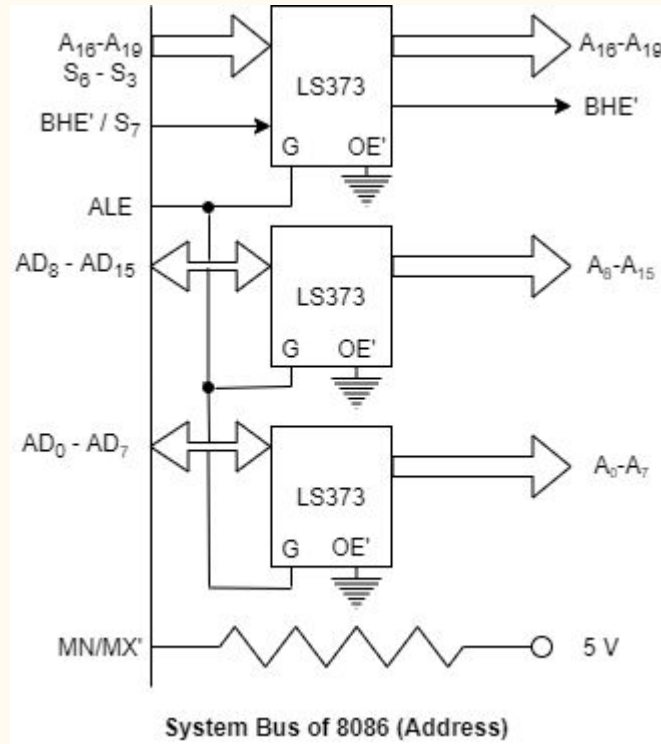
Varun Singh

8086 Inputs

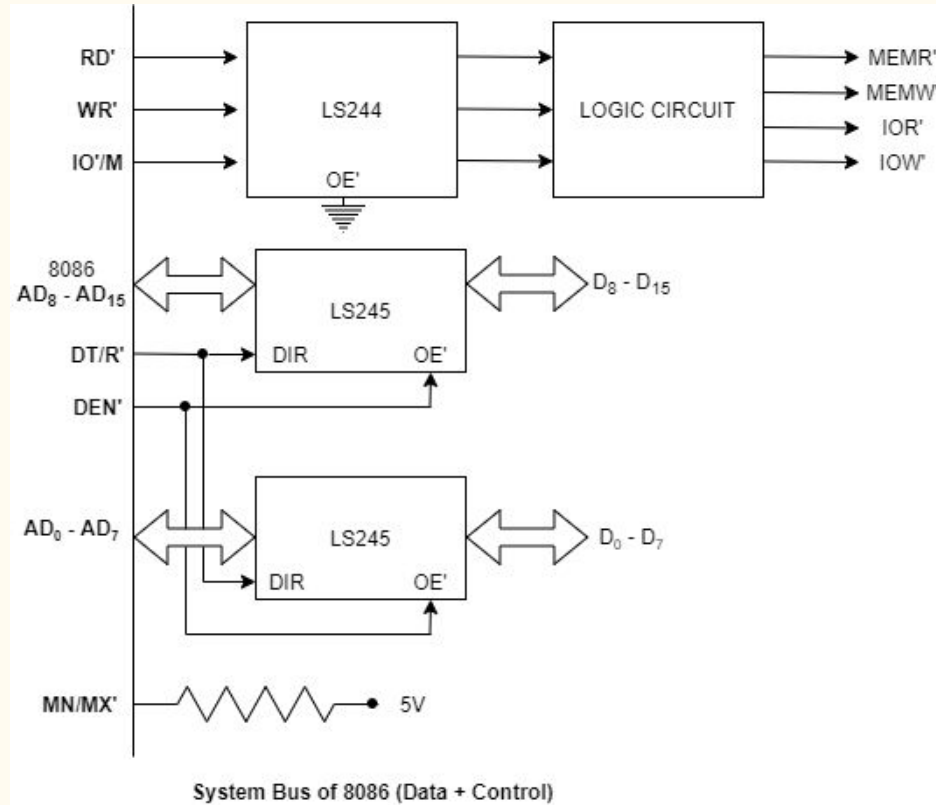


8086 Inputs

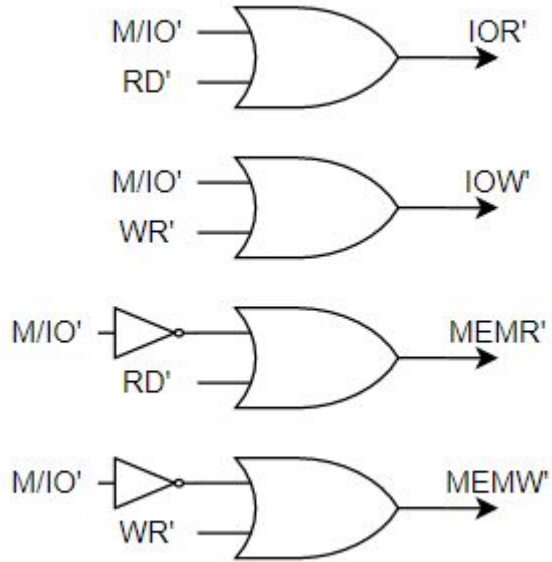
System Bus of 8086 (Address)



System Bus of 8086 (Data + Control)

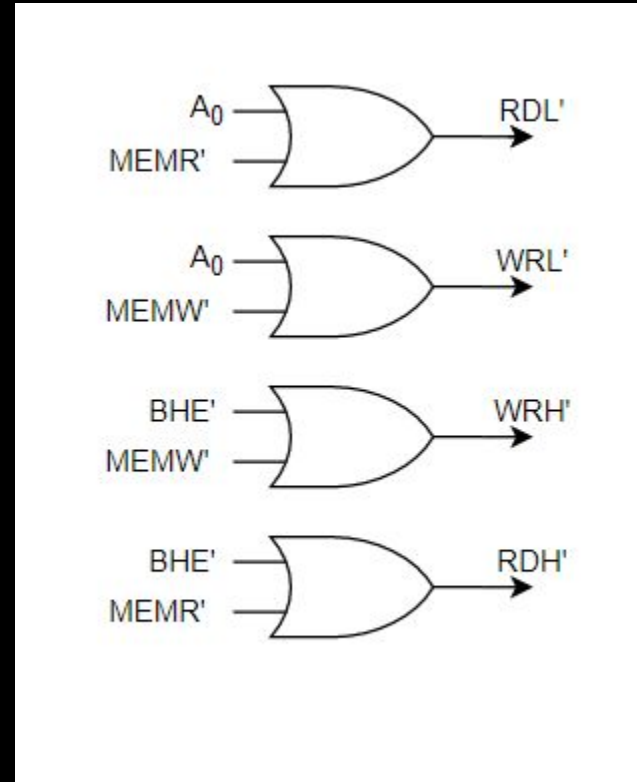
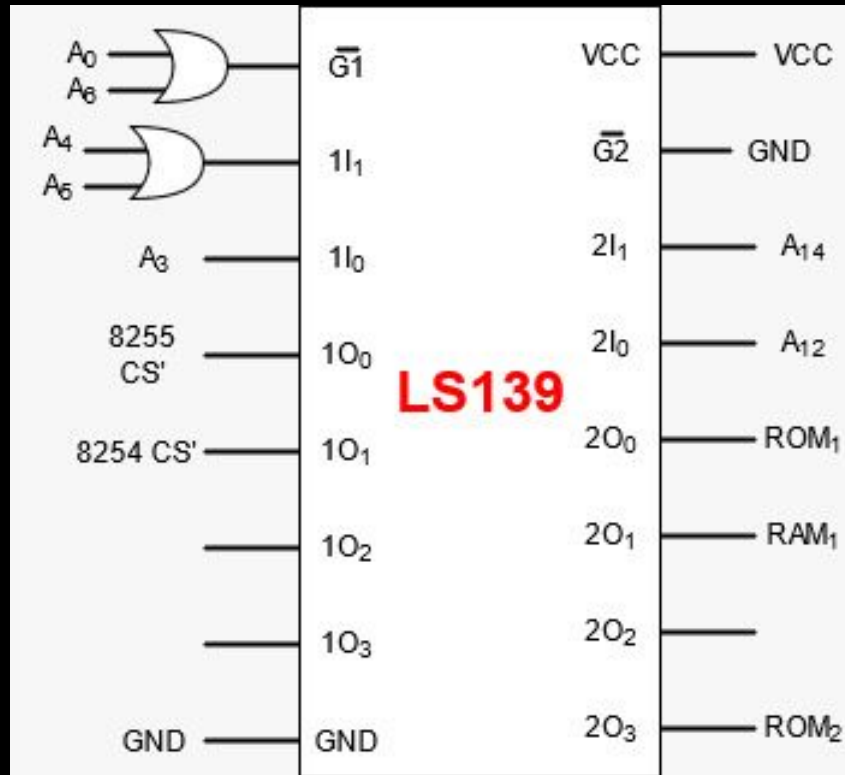


Logic Circuit

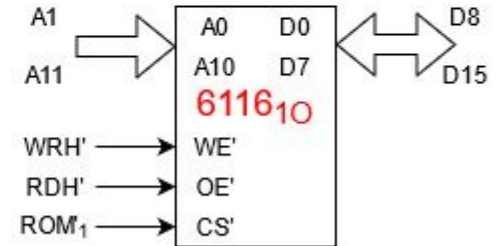
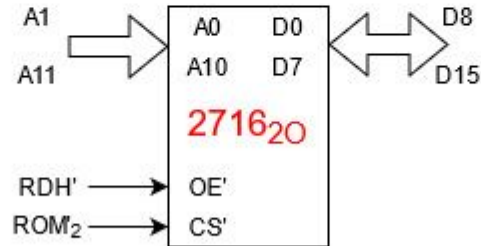
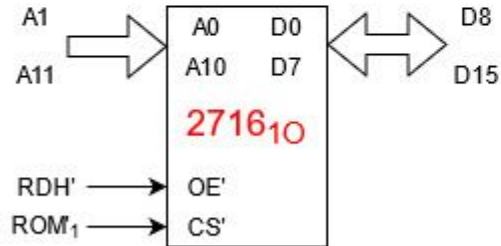
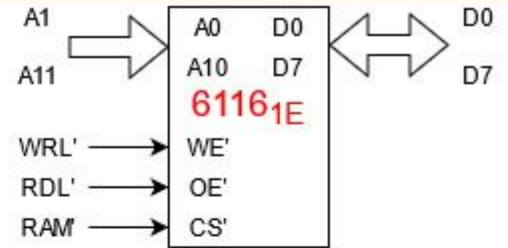
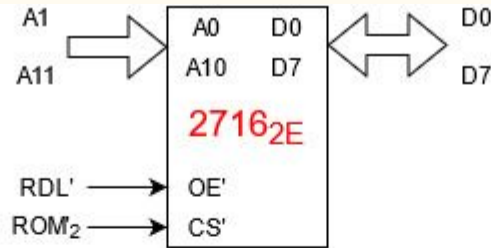
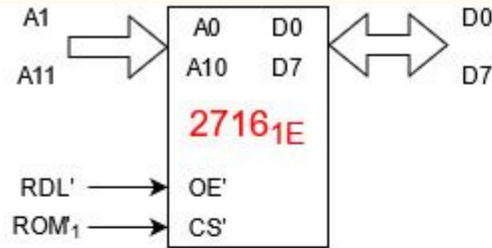


M/IO'	RD'	WR'	Bus Cycle
1	0	1	MEMR'
1	1	0	MEMW'
0	0	1	IOR'
0	1	0	IOW'

Memory and I/O Decoder (Dual 2x4 decoder)

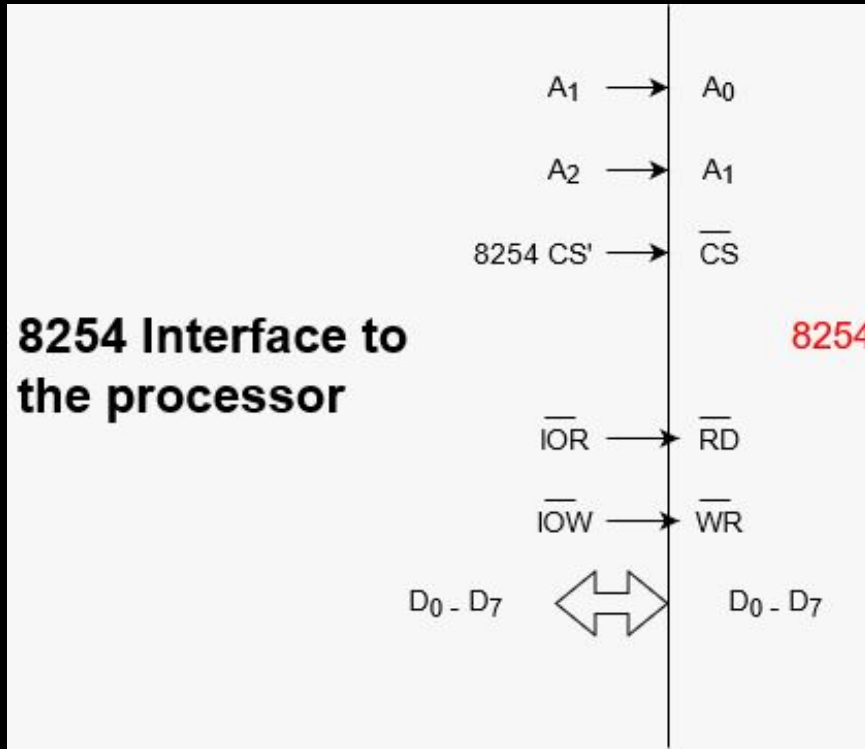


Memory Interfacing

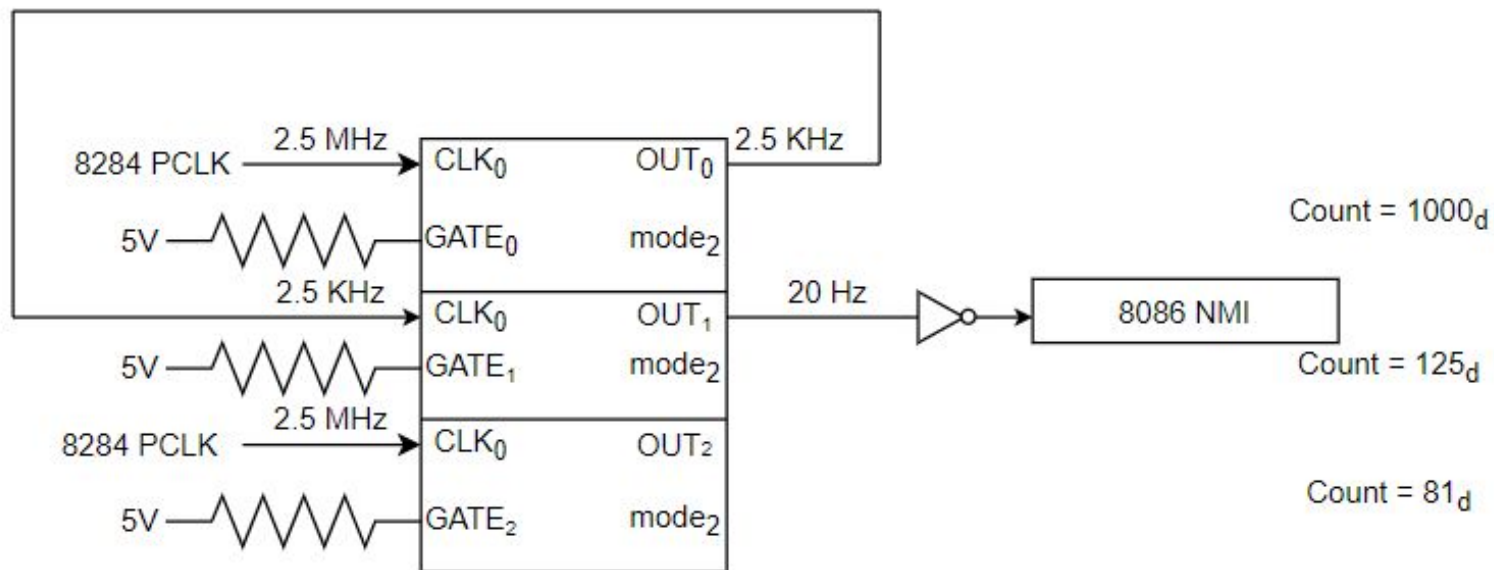


Memory Interfacing

8254 Interface to the Processor

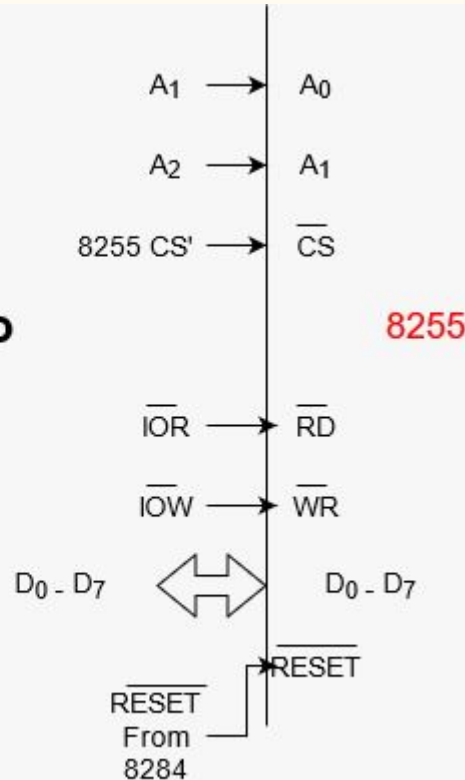


8254

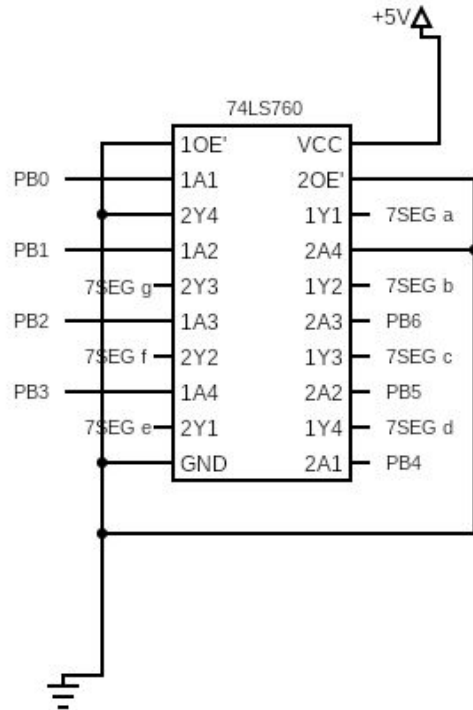


8255 Interface to the Processor

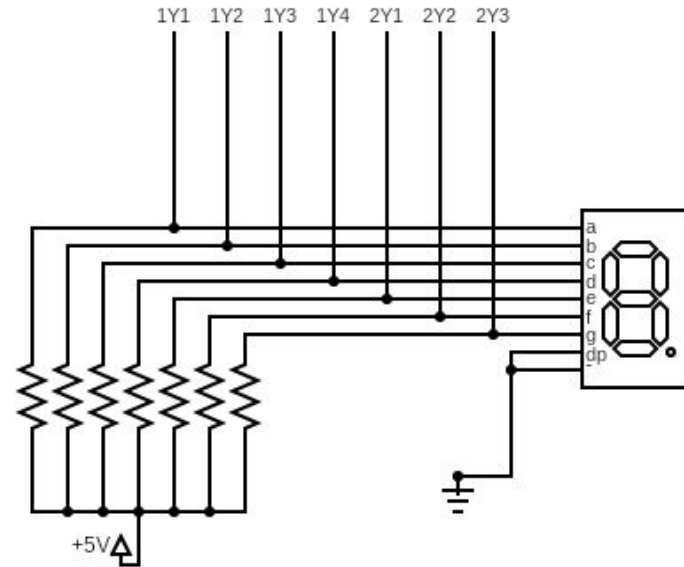
8255 Interface to the processor



7 Segment Display Interfacing



7 Segment Display Interfacing



8255, LED, Button Interfacing

