

#### 2 U-boot

Youtube: <a href="https://youtu.be/lq6CfYajaSw">https://youtu.be/lq6CfYajaSw</a>



# References

[1]:u-boot sources: <a href="http://www.denx.de/wiki/U-Boot">http://www.denx.de/wiki/U-Boot</a>

[2]: wiki.friendlyarm.com/wiki/index.php/NanoPi\_NEO\_Plus2

[4]: https://github.com/u-boot/u-boot

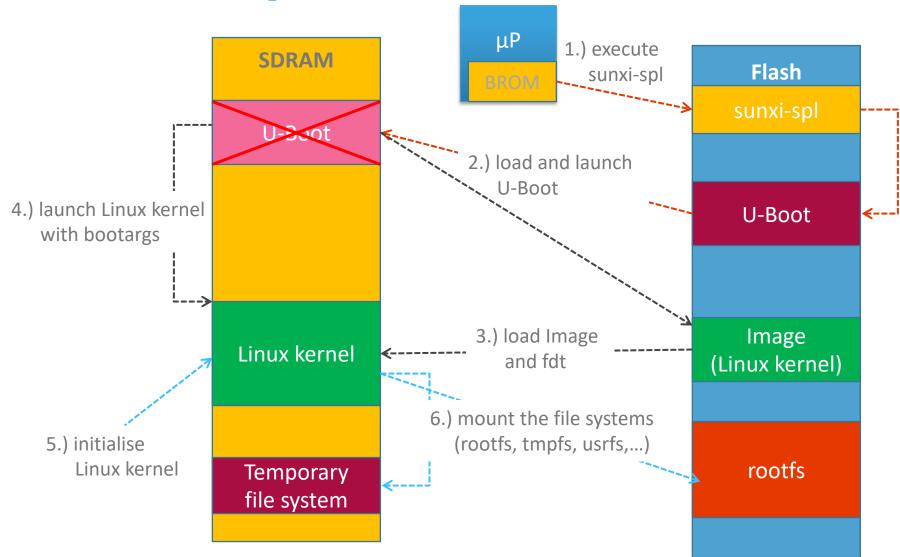


# Boot sequence [Cours CSEL, D. Gachet]

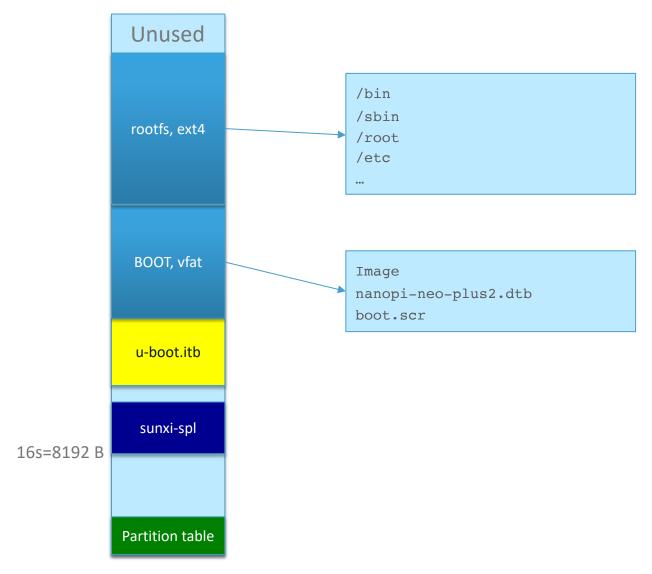
#### ▶ Le démarrage du NanoPi NEO Plus2 se décompose en 6 phases:

- Lorsque le μP est mis sous tension, le code stocké dans son BROM va charger dans ses 32KiB de SRAM interne le firmware « sunxi-spl » stocké dans le secteur nº 16 de la carte SD / eMMC et l'exécuter.
- Le firmware « sunxi-spl » (Secondary Program Loader) initialise les couches basses du μP, puis charge l'U-Boot dans la RAM du μP avant de le lancer.
- L'U-Boot va effectuer les initialisations hardware nécessaires (horloges, contrôleurs, ...) avant de charger l'image non compressées du noyau Linux dans la RAM, le fichier «Image», ainsi que le fichier de configuration FDT (flattened device tree).
- □ L'U-Boot lancera le noyau Linux en lui passant les arguments de boot (bootargs).
- □ Le noyau Linux procédera à son initialisation sur la base des bootargs et des éléments de configuration contenus dans le fichier FDT (sun50i-h5-nanopi-neoplus2.dtb).
- □ Le noyau Linux attachera les systèmes de fichiers (rootfs, tmpfs, usrfs, ...) et poursuivra son exécution.

# Boot sequence [Cours CSEL, D. Gachet]



## **BOOT** and rootfs content



## **U-boot commands**

If a key is pressed quickly during NanoPi boot, you can enter to the u-boot mode

```
U-Boot SPL 2019.01 (Aug 29 2019 - 12:49:32 +0200)

DRAM: 1024 MiB

Trying to boot from MMC1

U-Boot 2019.01 (Aug 29 2019 - 12:49:32 +0200) Allwinner Technology

CPU: Allwinner H5 (SUN50I)

Model: FriendlyARM NanoPi NEO Plus2

DRAM: 1 GiB

MMC: SUNXI SD/MMC: 0, SUNXI SD/MMC: 1

=> // prompt u-boot
```



## **U-boot commands**

Type "help" or "?" for a complete listing of available commands.

```
=> ?
 ?
        - alias for 'help'
        - print or set address offset
base
bdinfo - print Board Info structure
        - boot default, i.e., run 'bootcmd'
boot
        - boot default, i.e., run 'bootcmd'
bootd
bootefi - Boots an EFI payload from memory
bootelf - Boot from an ELF image in memory
        - boot arm64 Linux Image image from memory
booti
bootm - boot application image from memory
        - boot image via network using BOOTP/TFTP protocol
bootp
bootvx - Boot vxWorks from an ELF image
ext2load- load binary file from a Ext2 filesystem
ext2ls - list files in a directory (default /)
ext4load- load binary file from a Ext4 filesystem
ext4ls - list files in a directory (default /)
ext4size- determine a file's size
fatinfo - print information about filesystem
fatload - load binary file from a dos filesystem
fatls - list files in a directory (default /)
 fatmkdir- create a directory
fatrm - delete a file
fatsize - determine a file's size
fatwrite- write file into a dos filesystem
```

## **U-boot commands**

```
md
        - memory display
mdio - MDIO utility commands
mii
        - MII utility commands
        - memory modify (auto-incrementing address)
mm
       - MMC sub system
mmc
mmcinfo - display MMC info
        - memory write (fill)
mw
        - boot image via network using NFS protocol
nfs
       - send ICMP ECHO REQUEST to network host
printenv- print environment variables
        - run commands in an environment variable
run
       - save file to a filesystem
save
Examples:
ext2ls mmc 0:1 // show SDCard 1st partition
ext21s mmc 0:2
               // show SDCard 2<sup>nd</sup> partition
fatls mmc 1:1 // show eMMC 1st partition
```

## **Load kernel**

The following U-Boot commands load the Linux kernel, Image file, the FDT (Flattened Device Tree) and start Linux

=> run bootcmd

// load and start Image

This command searches and executes the boot.scr file in the 1st partition.

#### Create boot.scr file:

cd ~/workspace/nano/buildroot

mkimage -C none -A arm64 -T script -d board/friendlyarm/nanopi-neoplus2/boot.cmd /home/schuler/workspace/nano/buildroot/output/images/boot.scr



## **Load kernel**

```
Show boot.cmd file: cat boot.cmd

setenv bootargs console=ttyS0,115200 earlyprintk root=/dev/mmcblk0p2 rootwait

fatload mmc 0 $kernel_addr_r Image
fatload mmc 0 $fdt_addr_r nanopi-neo-plus2.dtb
booti $kernel_addr_r - $fdt_addr_r

Load FDT Start Linux

Linux kernel boot parameters

mmc 0: SDCard 1st partition (mmc 0 = mmc 0:1)
```



## **Load kernel**

```
SDCard 1st partition
                                                                                         RAM
                                                             ≯|mage
                                                              _nanopi neo plus2.dfb
                                                              boot.scr
fatload mmc 0 $kernel addr r Image
fatload mmc 0 $fdt addr r nanopi-neó-plus2.dtb
                                                                                         nanopi-neo-
                                                                                           plus.dtb
fatls mmc 0
                                                                             0x4FA00000
   30210560 Image
   20426
             nanopi-neo-plus2.dtb
   271
             boot.scr
printenv kernel_addr_r
                                                                                          Image
   kernel addr r=0x40080000
                                                                             0x40080000
printenv fdt_addr_r
   fdt addr r=0x4FA00000
```

### **Load kernel: start Linux**

booti \$kernel\_addr\_r - \$fdt\_addr\_r
\$(kernel\_addr\_r) = 0x40080000

This command starts the Linux kernel

```
$(fdts_addr_r)
    Kernel address
                                       FDT address
                        No initrd
help booti
booti - boot arm64 Linux Image image from memory
Usage:
booti [addr [initrd[:size]] [fdt]]
    - boot arm64 Linux Image stored in memory
        The argument 'initrd' is optional and specifies the address
        of an initrd in memory. The optional parameter ':size' allows
        specifying the size of a RAW initrd.
        Since booting a Linux kernel requires a flat device-tree, a
        third argument providing the address of the device-tree blob
        is required. To boot a kernel with a device-tree blob but
```

without an initrd image, use a '-' for the initrd argument.

## **U-boot configuration**

U-boot configuration looks like Linux kernel configuration

```
Configure:
 cd ~/workspace/nano/buildroot
 make uboot-menuconfig
Compile: 2 possibilities:
 1)
 cd ~/workspace/nano/buildroot
 make uboot-rebuild
 2)
 cd ~/workspace/nano/buildroot/
 rm output/build/uboot-2020.07/.stamp-built
 make
After the make command, two files are created:
  ~/workspace/nano/buildroot/output/images/u-boot.itb
  ~/workspace/nano/buildroot/output/images/boot.scr
```

## **U-boot configuration**

cd ~/workspace/nano/buildroot

make uboot-menuconfig

The configuration file is saved to the ~/workspace/nano/buildroot/output/build/uboot-2020.07/.config file

```
.config - U-Boot 2019.01 Configuration
                            U-Boot 2019.01 Configuration
   Arrow keys navigate the menu. <Enter> selects submenus ---> (or empty submenus
   ----). Highlighted letters are hotkeys. Pressing <Y> includes, <N> excludes,
   <M> modularizes features. Press <Esc><Esc> to exit, <?> for Help, </>>
   Search. Legend: [*] built-in [ ] excluded <M> module < > module capable
            Architecture select (ARM architecture)
            ARM architecture --->
            General setup --->
            Boot images --->
            API --->
            Boot timing --->
            Boot media --->
        (2) delay in seconds before automatically booting
        1(+)
               <Select>
                          < Exit > < Help > < Save > < Load >
```

## **U-boot configuration**

It is possible to show the .config file. Don't modify this file with an editor

```
#
# Automatically generated file; DO NOT EDIT.
# U-Boot 2019.01 Configuration
#

CONFIG_CREATE_ARCH_SYMLINK=y
# CONFIG_ARC is not set
CONFIG_ARM=y
...
# CONFIG_SH is not set
# CONFIG_X86 is not set
# CONFIG_X7ENSA is not set
CONFIG_SYS_ARCH="arm"
CONFIG_SYS_CPU="armv8"
```

## Directories installation [2]

```
~/workspace
                                  → working space
  /nano
                                  → working space for NanoPi
    /buildroot
                                  → space for tools, kernel, rootfs generation
          /board/friendlyarm/nanopi-neo-plus2→ genimage.cfg, boot.cmd, sunxi-spl.bin
          /dl
                                  → downloaded « tared » packets: e.g. busybox-1.30.1.tar.bz2
                                  → Rootfs skeleton
          /system/skeleton
          /output
                  /build
                                  → source codes and compiled packets, e.g.: linux-5.1.16
                  /images
                                  → Image, nanopi-neo-plus2.dtb, rootfs.ext4, u-boot.itb,
                                     boot.scr, sunxi-spl.bin
                                  → rootfs not "tared"
                  /target
                  /host/usr/bin
                                  → cross-compiler: aarch64-linux-gnu-gcc, ...
```

Files u-boot.itb, sunxi-spl.bin, Image, sun50i-h5-nanopi-neo-plus2.dtb, rootfs.ext4, boot.scr will be copied to the uSD card.

In order to cross-compile, link, ..., add the / PATH=\$PATH:~/workspace/nano/buildroot/output/host/usr/bin



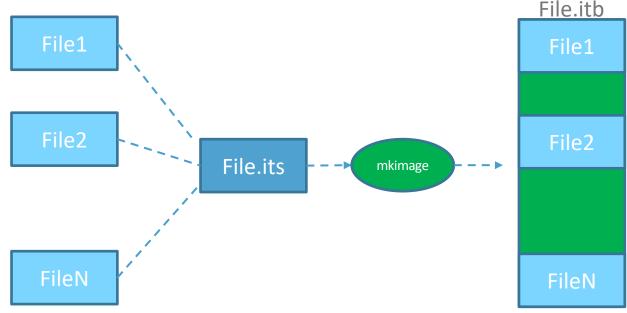
- The Flattened Device-Tree (FDT) was introduced in kernel 2.6. It is a file which contains the hardware description. Linux uses it for its configuration
- FDT has two files:
  - .dts: Device Tree Source, it is an ascii file
  - .dtb: Device Tree Blob, it is a binary file
- dtc command transforms .dts file to .dtb file: dtc board.dts —o board.dtb



- U-boot uses a FTD file in order to describe the hardware.
- The sun50i-h5-nanopi-neo-plus2.dts file describes the hardware used by u-boot for the NanoPi (workspace/nano/buildroot/output/build/uboot-2020.07/arch/arm/dts/)

```
cat sun50i-h5-nanopi-neo-plus2.dts
```

- After the introduction of FTD with the kernel 2.6, a new binary file format was created: FIT (Flattened Image Tree). This format allows to insert different files into a single file
- FIT has two files:
  - its: image source file, it is a text file
  - itb: Image Tree Blob, it is a binary file
- .its file describes which files will be inserted to the .itb file



- FIT format allows more flexibility in handling images of various types and also enhances integrity protection of images with hash functions like rsa signature, sha256, sha1, md5, crc32, ...
- The mkimage command reads a .its file and creates a .itb file:

```
mkimage -f file.its -E file.itb
```



### **U-boot and FIT**

 During startup, the Secondary Program Loader (sunxi-spl) loads the u-boot.itb file.

U-boot.itb is built with these commands:

```
cd workspace/nano/buildroot/output/build/uboot-2020.07 mkimage -f u-boot.its -E u-boot.itb
```

### **U-boot and FIT**

#### Show u-boot.its file

```
cat u-boot.its
  /dts-v1/;
  / {
     description = "Configuration to load ATF before U-Boot";
     #address-cells = <1>;
     images {
           uboot {
              description = "U-Boot (64-bit)";
              data = /incbin/("u-boot-nodtb.bin");
              type = "standalone";
              arch = "arm64";
              compression = "none";
              load = <0x4a0000000>;
           };
           atf {
              description = "ARM Trusted Firmware";
              data =
  /incbin/("/home/schuler/workspace/nano/buildroot/output/images/bl31.bin");
              type = "firmware";
              arch = "arm64";
              compression = "none";
              load = <0x44000>;
              entry = <0x44000>;
           };
```

### **U-boot and FIT**

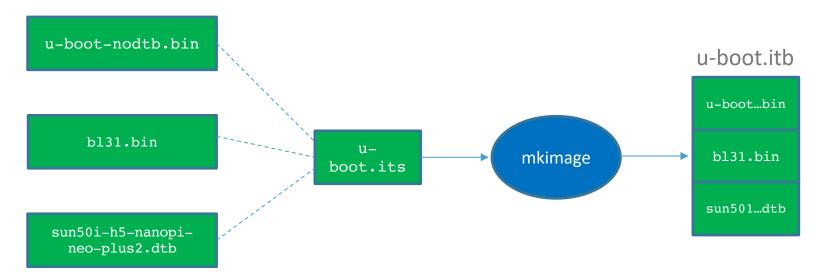
```
fdt_1 {
    description = "sun50i-h5-nanopi-neo-plus2";
    data = /incbin/("arch/arm/dts/sun50i-h5-nanopi-neo-plus2.dtb");
    type = "flat_dt";
    compression = "none";
}

};

configurations {
    default = "config_1";

    config_1 {
        description = "sun50i-h5-nanopi-neo-plus2";
        firmware = "uboot";
        loadables = "atf";
        fdt = "fdt_1";
};
```

- mkimage reads u-boot.its and builds u-boot.itb.
- u-boot.itb contains
  - u-boot-nodtb.bin: u-boot code
  - bl31.bin: trust zone
  - sun501-h5 ... .dtb: Flattened device tree (device Tree Blob)

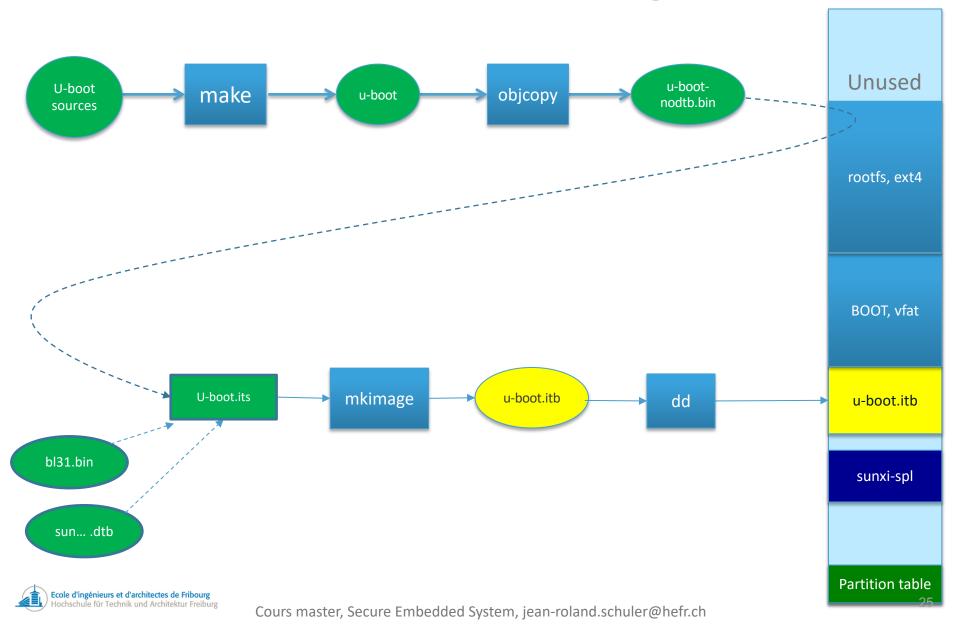


#### Where are these files:

- workspace/nano/buildroot/output/build/u-boot.2020.07/u-boot.its
- workspace/nano/buildroot/output/build/u-boot.2020.07/u-boot-nodtb.bin
- workspace/nano/buildroot/output/build/output/image/bl31.bin
- workspace/nano/buildroot/output/build/u-boot.2020.07/arch/arm/dts/sun50i-h5-nanopi-neoplus2.dtb



# Create u-boot.itb image



# Create u-boot.itb image

During the make, u-boot-nodtb.bin image is created, by the arm-linux-gnueabihf-objcopy command

aarch64-linux-gnu-objcopy --gap-fill=0xff -0 binary u-boot u-boot-nodtb.bin

u-boot is created by the linker and has the ELF format u-boot-nodtb.bin is a raw file with only the loadable sections of the u-boot file, other sections (symbols, debug, relocation, ...) are not copied to u-boot-nodtb.bin

- aarch64-linux-gnu-objcopy generates a raw binary file, it will essentially produce a memory dump of the contents of the input object file.
- All symbols and relocation information will be discarded. The memory dump will start at the load address of the lowest section copied into the output file.

# u-boot compilation options

Important: cd ~/workspace/nano/buildroot/output/build/u-boot-2020.07

In order to change compilation options, it is necessary to change config.mk or directly Makefile

#### config.mk:

PLATFORM\_CPPFLAGS, LDFLAGS\_FINAL

#### Makefile:

KBUILD\_CLAGS

These flags modify compilation options

#### Less Makefile

c\_flags := \$(KBUILD\_CFLAGS) \$(cpp\_flags)



## Analyze the u-boot compilation

By default during the make, the followed gcc options are used

Compilation command: cd ~/workspace/nano/buildroot

make uboot-rebuild V=1

```
aarch64-linux-qnu-qcc -Wp,-MD, arch/arm/cpu/armv8/.fwcall.o.d -nostdinc -isystem
/home/schuler/workspace/nano/buildroot/output/host/opt/ext-toolchain/bin/../lib/gcc/aarch64-
linux-qnu/8.2.1/include - Iinclude - I./arch/arm/include - include ./include/linux/kconfig.h -
D KERNEL -D UBOOT -Wall -Wstrict-prototypes -Wno-format-security -fno-builtin -
ffreestanding -std=gnull -fshort-wchar -fno-strict-aliasing -fno-PIE -Os -fno-stack-protector
-fno-delete-null-pointer-checks -fmacro-prefix-map=./= -q -fstack-usage -Wno-format-nonliteral
-Werror=date-time -D ARM -fno-pic -mstrict-align -ffunction-sections -fdata-sections -fno-
common -ffixed-r9 -fno-common -ffixed-x18 -pipe -march=armv8-a -D LINUX ARM ARCH =8 -
                                   -D"KBUILD STR(s)=#s" -D"KBUILD BASENAME=KBUILD STR(fwcall)"
I./arch/arm/mach-sunxi/include
-D"KBUILD MODNAME=KBUILD STR(fwcall)" -c -o arch/arm/cpu/armv8/fwcall.o
arch/arm/cpu/armv8/fwcall.c
-nostdinc: don't use the standard include directories, use only the -I directories
-Os: optimized for size
-fno-stack-protector: no protection against stack smashing
-fno-delete-null-pointer-checks: don't check null pointer
-q: debug option
-fstack-usage
-fno-common: check multiple-definition of global variables
-Dxxx: same as #define xxx
-I: include directories
```



## Improve the u-boot compilation

In order to improve the u-boot code security, it is possible to add these options:

- suppress the -g option: delete the debug information
- Add the -fstack-protector-all option

-fstack-protector-all option adds extra code to check buffer overflows, such as stack smashing attacks. This is done by adding a guard variable to functions. This variable is called Canary

Example: file.c has a buffer overflow error, the —fstack-protector-all detects that



## -Fstack-protection-all gcc option, Intel uP

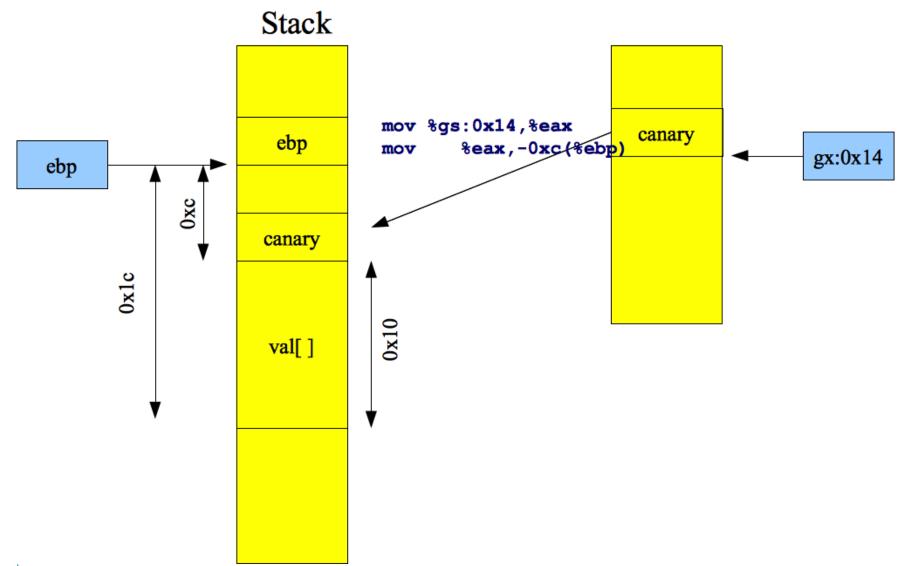
Principe: localBufferOverflow assembler code (Intel code)

```
08048453 < ZL19localBufferOverflowv>:
8048453:
                                                %ebp
                55
                                         push
                                                %esp,%ebp
 8048454:
                89 e5
                                         mov
8048456:
          83 ec 28
                                         sub
                                                $0x28,%esp
 8048459:
             65 a1 14 00 00 00
                                                %qs:0x14,%eax
                                         mov
                                                ext{eax}, -0xc(ext{ebp})
 804845f:
          89 45 f4
                                         mov
8048462:
                31 c0
                                                %eax, %eax
                                         xor
                8b 55 f4
                                                -0xc(%ebp),%edx
 804848b:
                                         mov
                65 33 15 14 00 00 00
                                                %qs:0x14,%edx
 804848e:
                                         xor
8048495:
                74 05
                                                804849c
                                         iе
                e8 54 fe ff ff
                                         call
8048497:
                                                80482f0 < stack chk fail@plt>
 804849c:
                c9
                                         leave
```

If the edx register is not zero, the function \_\_stack\_chk\_fail@plt is called



## -Fstack-protection-all gcc option, Intel uP



### -Fstack-protection-all gcc option, ARM uP

```
08048453 <testCanary>:
                     e58de004
                                              lr, [sp, #4]
        1049c:
                                      str
        104a0:
                     e28db004
                                              fp, sp, #4
                                      add
        104a4:
                    e24dd010
                                      sub
                                              sp, sp, #16
        104a8:
                    e3003f08
                                             r3, #3848
                                                               : 0xf08
                                      movw
        104ac:
                                              r3, #2
                     e3403002
                                      movt
        104b0:
                     e5933000
                                      ldr
                                              r3, [r3]
                 e50b3008
                                              r3, [fp, #-8]
        104b4:
                                      str
        104b8:
                     e3a03000
                     e3003f08
                                              r3, #3848
                                                               ; 0xf08
        104f8:
                                      movw
        104fc:
                                              r3, #2
                     e3403002
                                      movt
        10500:
                                              r2, [fp, #-8]
                     e51b2008
                                      ldr
        10504:
                                              r3, [r3]
                     e5933000
                                      ldr
        10508:
                                              r2, r3
                     e1520003
                                      cmp
        1050c:
                                              10514 <testCanary+0x7c>
                     0a000000
                                      beq
        10510:
                     ebffff8e
                                              10350 < stack chk fail@plt>
                                      bl
        10514:
                     e24bd004
                                              sp, fp, #4
                                      sub
        10518:
                     e59db000
                                      ldr
                                              fp, [sp]
        1051c:
                     e28dd004
                                              sp, sp, #4
                                      add
        10520:
                     e49df004
                                              {pc}
                                                               ; (ldr pc, [sp],
                                      pop
```

If r2 != r3 --> \_\_stack\_chk\_fail function is called



# **U-boot: debug and symbols**

In order to improve the security of an ELF executable file, it is necessary to remove debug and symbols information. The strip command deletes this information.

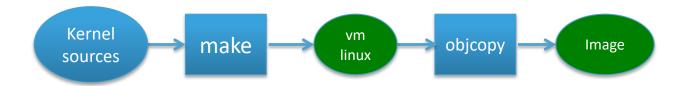
aarch64-linux-gnu-strip u-boot

```
Diassemble with symbols
arm-linux-gnueabihf-objdump -d u-boot
  43e00058 <reset>:
                                        43e01440 <save boot params default>
  43e00058:
                 eb0004f8
                                bl
  43e0005c:
                 e10f0000
                                        r0, CPSR
                                mrs
  43e00060:
                 e3c0001f
                                        r0, r0, #31
                                bic
                                        r0, r0, #211
  43e00064:
                 e38000d3
                                orr
                                                        ; 0xd3
                                        CPSR fc, r0
  43e00068:
                 e129f000
                                msr
```

```
Diassemble without symbols
arm-linux-qnueabihf-objdump -d u-boot
  43e00058:
                 eb0004f8
                                 .word
                                         0xeb0004f8
  43e0005c:
                 e10f0000
                                 .word
                                         0xe10f0000
  43e00060:
                 e3c0001f
                                 .word
                                         0xe3c0001f
  43e00064:
                 e38000d3
                                         0xe38000d3
                                 .word
  43e00068:
                                         0xe129f000
                 e129f000
                                 .word
```

The command below automatically removes debug and symbols information aarch64-linux-gnu-objcopy --gap-fill=0xff -0 binary u-boot u-boot-nodtb.bin





- vmlinux: Linux kernel not stripped, elf format
- Image: Linux kernel stripped without some sections (.note, .comment, ...)

#### Where are these files:

- Kernel sources: workspace/nano/buildroot/output/build/linux-xx
- vmlinux: workspace/nano/buildroot/output/build/linux-xx/vmlinux
- Image: workspace/nano/buildroot/output/build/linux-xx/arch/arm64/boot/Image

#### zlmage: Compressed Linux

- cd workspace/nano/buildroot/output/build/linux-xx
- cat arch/arm64/boot/Image | gzip -n -f -9 > arch/arm64/boot/zImage

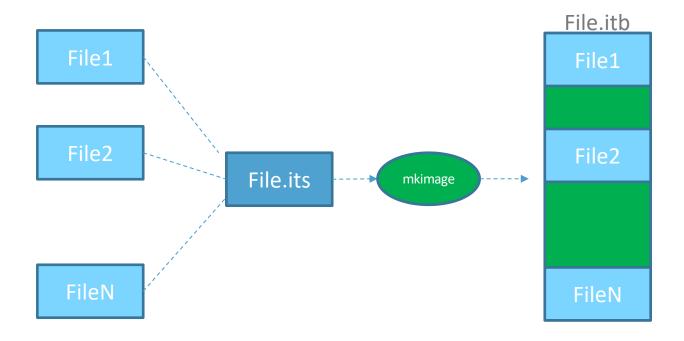


#### ulmage: Compressed Linux with a u-boot header

- cd workspace/nano/buildroot/output/build/linux-xx
- mkimage -A arm64 -O linux -C none -T kernel -a 0x40080000 -e 0x40080000 -n
   'Linux-XX' -d arch/arm64/boot/zImage arch/arm64/boot/uImage



**FIT**: It is the new format supported by u-boot. A .its file describes information (the structure, the check, ..) about different files inserted into the .itb file



Example: FIT configuration. During linux startup, u-boot can check the integrity of the different files

Source: https://xilinx-wiki.atlassian.net/wiki/spaces/A/pages/18842374/U-Boot+Images

```
/dts-v1/;
   description = "Simple image with single Linux kernel, FDT blob and ramdisk";
   #address-cells = <0x1>;
   images {
       kernel@1 {
                                                                                 ramdisk@1 {
           description = "Zyng Linux kernel";
                                                                                     description = "Ramdisk Image";
           data = /incbin/("./vmlinux.bin.gz");
                                                                                     data = /incbin/("./ramdisk.image.gz");
           type = "kernel";
                                                                                     type = "ramdisk";
           arch = "arm";
                                                                                     arch = "arm";
           os = "linux";
                                                                                     os = "linux":
           compression = "gzip";
                                                                                     compression = "gzip";
           load = <0x8000>;
                                                                                     load = <0 \times 008000000>;
           entry = <0x8000>;
                                                                                     entry = <0\times008000000>;
           hash@1 {
                                                                                     hash@1 {
               algo = md5;
                                                                                     algo = "md5":
           };
                                                                                     };
           hash@2 {
                                                                                     hash@2 {
               algo = "sha1";
                                                                                         algo = "sha1":
           };
                                                                                };
       fdt@1 {
           description = "ZED board Flattened Device Tree blob";
                                                                             configurations {
           data = /incbin/("./zynq-microzed.dtb");
                                                                                 default = "conf@1":
           type = "flat dt";
                                                                                 conf@1 {
           arch = "arm";
                                                                                     description = "Boot Linux kernel, FDT blob and ramdisk";
           compression = "none";
                                                                                     kernel = "kernel@1";
           hash@1 {
                                                                                     fdt = "fdt@1";
               algo = "md5";
                                                                                     ramdisk = "ramdisk@1"
           };
                                                                                 };
           hash@2 {
                                                                            };
               algo = "sha1"
                                                                         };
       };
```

#### **U-boot loads Linux kernel**

U-boot has different commands in order to load the Linux kernel

- booti loads an Image format file
- bootz loads an zlmage format file
- bootm loads an ulmage format file
- bootm load a fit format file

#### Example:

```
setenv bootargs console=ttyS0,115200 earlyprintk root=/dev/mmcblk0p2 rootwait fatload mmc 0 0x40080000 Image fatload mmc 0 0x4fa00000 sun50i-h5-nanopi-neo-plus2.dtb booti 0x40080000 - 0x4fa00000
```

