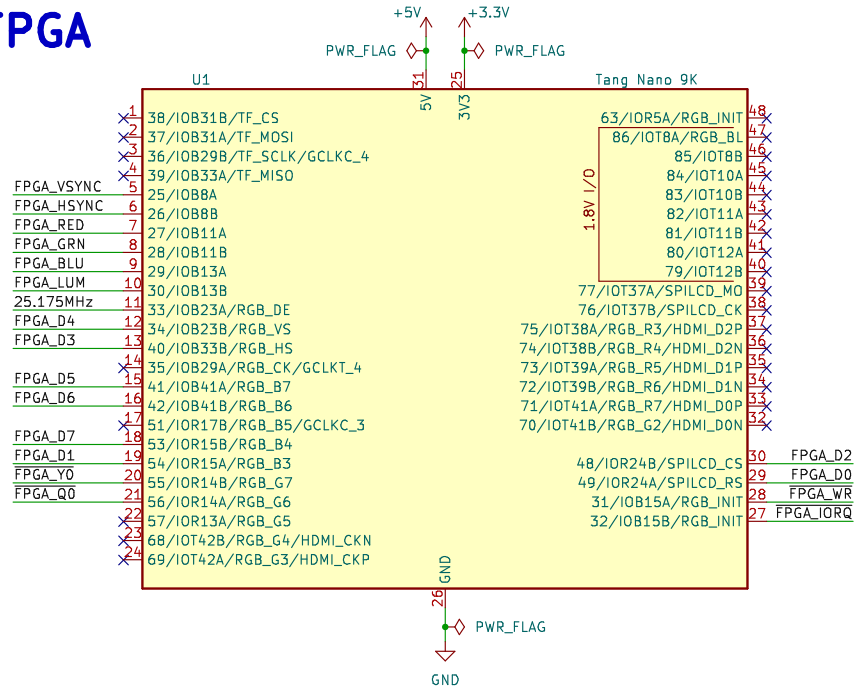
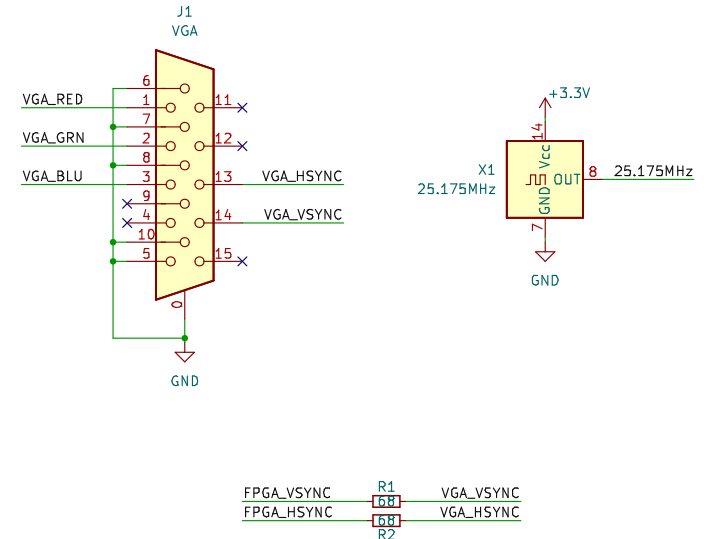


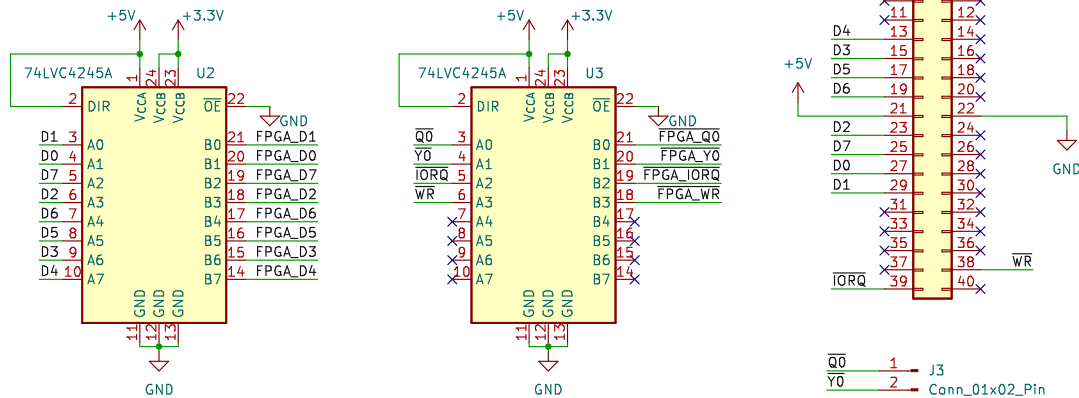
FPGA



VGA Interface



JML-8-Mini Interface



Created by: Jacob Long (<https://github.com/jac-oblong>)
Released under MIT License

FPGA based VGA board for JML-8-Mini
<https://github.com/jac-oblong/JML>

Sheet: /
File: jml-8-mini-vga.kicad_sch

Title: JML-8-Mini VGA Daughterboard

Size: A4 Date: 2024-01-20

KiCad E.D.A. kicad 7.0.10

Rev: A

Id: 1/1