

#### **EEE Lab Report**

Course: Electronic Devices and Circuits & Pulse Techniques Lab Course Code: EEE 204

Experiment No: 04

Experiment Name: Non-Inverting Amplifier

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Experiment No: 04

Experiment Name: Non-Inventing Amplifient Aim: To design and setup a non-inventing amplifient circuit - with OP AMP IC 741C for a gain of 11, plot the waveform, observe the Phase reversal, measure the gain.

Objectives: After completion of this experiment, we will be able to design and setup a non-inventing amplifient using OP AMP. We will acquire skill to design and implement OP AMP non-inventing amplifier circuit.

### Equipments/Components:

SL. M.	Non I Specification	Quantity
	Name and Specification	0
01.	Dual Power Supply +/-15V	1
02.	Function generatore	1
	(0-1 MHZ)	
0.3.	Oscilloscope	1
04.	Bread board	1
05.	IC 741C	1
06.	Resistors	2
07.	Probes and connecting	As
	Twines	required

Principle: It is a linear closed loop mode application of op-amp and employs negative teedback. The Rt and R; are the teedback and input resistance of the circuit respectively. There will be no Phase difference between the output and input. Hence it is called non-inventing

amplifier.

$$A_{V} = \frac{V_{o}}{V_{im}}$$

$$= 1 + \frac{R_{f}}{R_{i}}$$

Herre the +Ve sign indicates the output will be an amplified wave in phase with the input. By varying the Rt on Ri, the gain of the amplifier can be varied to any desired value.

### Procedure:

- 1. Check the components.
- 2. Setup the circuit on the breadboard and check the connections.
- 3. Switch on the power supply.
- 4. Grive 1 VPP/1KHZ sine wave as input.
- 5. Observe input and output on the two channels of the CRO simultaneously.

- 6. Note down and draw the input and output waveforms on the graph.
- 7. Verify the input and output wave forms are in phase.
- 8. Verify the obtained gain is same as designed value.

## Circuit Diagram:

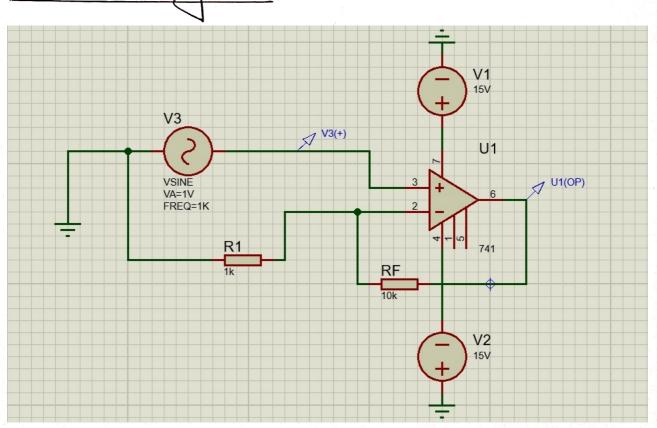


Figure: Non-Inverting Amplifier(Proteus)

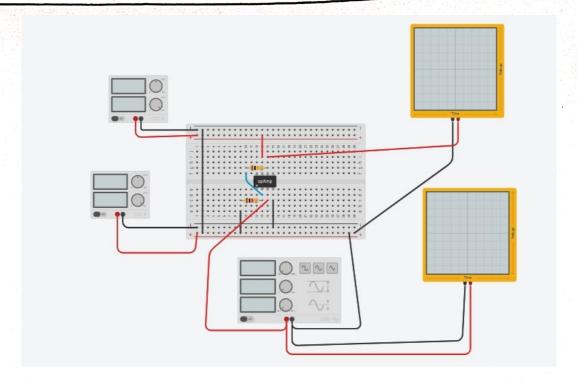
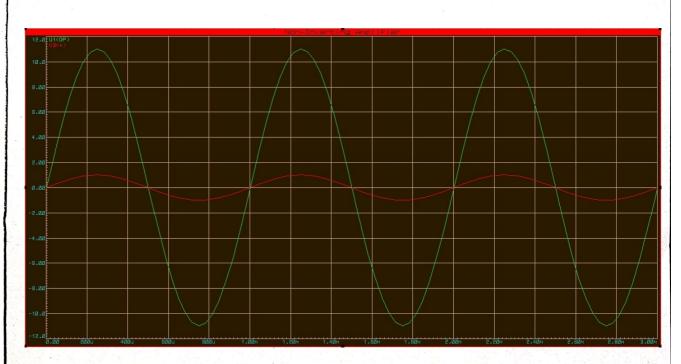


Figure: Non-Inverting Amplifier (TinkerCAD)

# Greaph:



Graph: Non-Inverting Amplifier

### Result:

We know that, Gain of a non-inventing amplifier,  $A_{V} = \frac{V_{0}}{V_{in}}$ = 1+ R<sub>b</sub>

Hene,

We let, Rt = 10 KD and R: = 1K1

There force,

$$A_{V} = 1 + \frac{10}{1}$$

$$= 1 + 10$$

$$= 11$$

So, the ultimate gain is 11.

#### Discussion:

A non-inverting amplifier is an op-amp circuit configuration that Produces an amplified output signal. This output signal of the non-inverting op-amp is in-Phase -with the input signal applied. In other words, a non-inventing amplifier behaves like a voltage follower circuit. A non-inverting amplifier also uses a negative teedback connection, but instead It feeding the entire output signal to the input, only a part of the output signal voltage is ted back as input to the inverting input terminal of the op-amp. The high input impedance and low output impedance of the non-inventing amplifier make the

cincuit ideal for impedance buffering applications.

During the experiment, when we tried to get the output sine waveform, we have encountered some problems. The Sine waveform didn't show up at the OSCIlloscope. After some trial and error, we found out that the source of the Problems are due to incorrect grounding. The connection of ground from bias Voltage cannot be connected to the ground of the supply voltage. Thus, we have to make a new ground for each of bias voltages and supply voltages. Next, at DP bias voltage also have V+ and Vand both of it has been connected to positive supply, hence the flat sine wave form shown on the oscilloscope. To

combat this, we made sure that the DC bias voltage V+ and V- are connected to the connect terminals which are the Positive and negative supply voltages.

Finally -we can say that, this experiment is more effective to gain knowledge about a non-inverting amplifier and thom this experiment -we can realize that, how non-inverting amplifier works. So at last -we can say that, this experiment is more important for us.

References:

[1] Lab Manual for EEE 204 Course

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