Makefiles

A sample Makefile:

```
all: GoL

GoL: GoL.cc
    g++ -o GoL GoL.cc

clean:
    rm -f GoL
```

The build/clean process:

```
jgourd@latech:~/_git/school/teaching/Fall2008/csc220/2_arrays$ make
jgourd@latech:~/_git/school/teaching/Fall2008/csc220/2_arrays$ make clean
```