

DATA MINING

FOR ACS LOGs FILES

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Until now

- FSM and software design understanding.
 - Defining partition criteria.
 - Communication interface.
 - Hierarchical structure.
- Comparison of SCXML implementations.
- Actual documentation understanding.

FSM software design understanding.

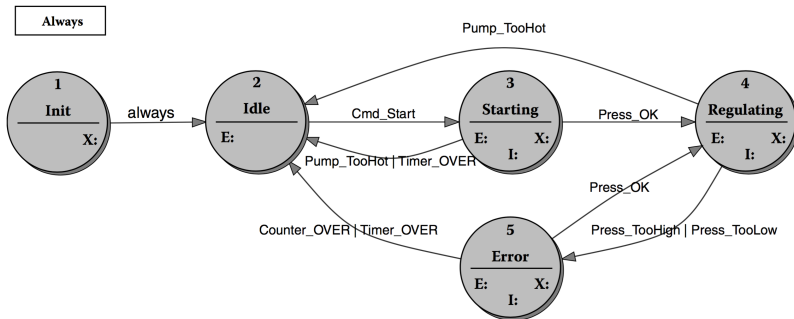


Figure 8.17 Pressure supervision state machine: the state transition diagram.

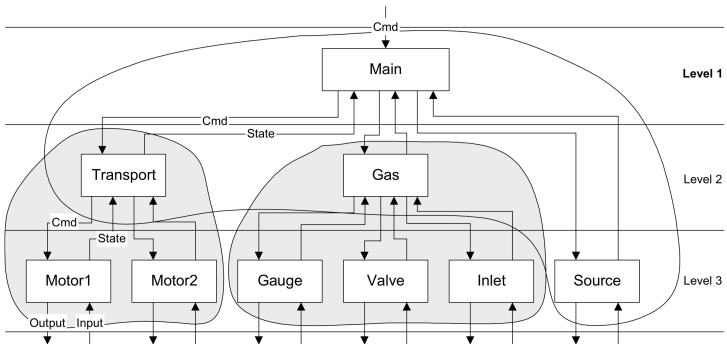
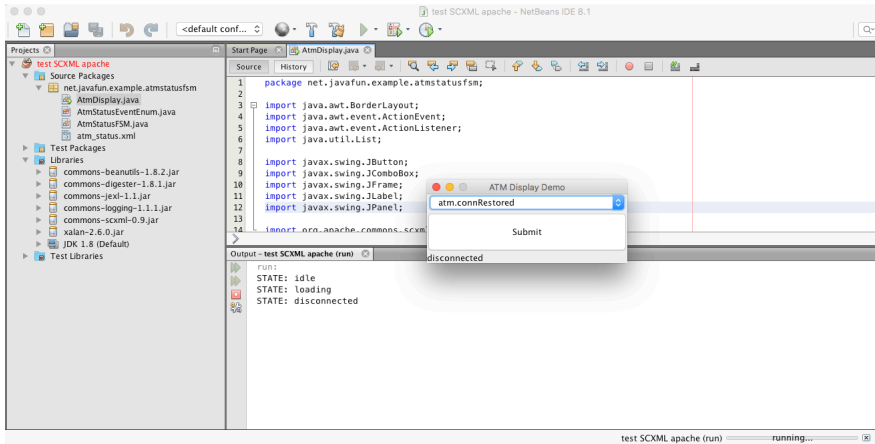


Figure 9.4 A hierarchical system of state machines.

Comparison of SCXML implementations.

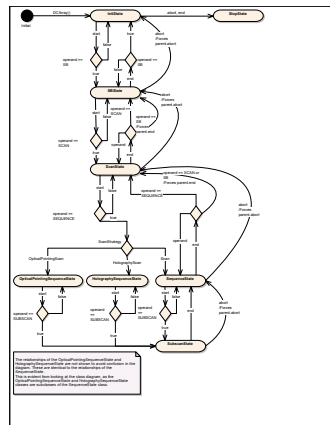
Implementation	Language	Standard	Documentation	License	Active
scxmlcc	C++	SCXML	Poor	GPL v3	✓
Apache Commons SCXML	Java	SCXML	Good	Apache License	✓
PySCXML	Python	SCXML	Poor	GPL v3	■
SCION	JavaScript	SCXML	Sufficient	Apache License	✓
uSCXML	C++	SCXML	Sufficient	? - OpenSource	✓
QM	C++	UML	Good	Privative	✓
NSF	C++	UML	Sufficient	Comercial	✓
Meta State Machine (MSM)	C++	UML	Good	Boost Software License	✓



Actual documentation understanding.

Event	Publisher	
DataCapturerStartedEvent	DataCapturer	Published after DataCaptur
ExecBlockStartedEvent	Array	Signals the start of the SB.
ScanStartedEvent	ObservingMode	Signals the start of the Sca
SubscanStartedEvent	ObservingMode	Signals the start of the Sub
SubScanProcessedEvent	DataCapturer	Published at the end of eac
SubScanSequenceEndedEvent	DataCapturer	Published at the end of eac to the Scan, not the sequer
ScanProcessedEvent	DataCapturer	Published at the end of eac
ScanEndedEvent	ObservingMode	Signals the end of the Scan
ScanReducedEvent	DataCapturer	Published after each TELC results (e.g. focus, bandpa
ExecBlockEndedEvent	Array	Signals the end of the SB.
ExecBlockProcessedEvent	DataCapturer	Published after all the resu
ASDMArchivedEvent	DataCapturer	Published after the proces

Events



State Machine

On schedule

Defining objectives and planning	DONE
Studying kibana, logstash and elasticsearch	DONE
Definition of the problematic	DONE
Analyze the observation process, and design a UML diagram.	DOING
Search, compare(Comparative table) and define a Implementation of state machine model and the standard to use.	DOING
Develop a State Machine Model for the observation process of high level operations including hardware configurations	TO DO
Design a architecture (Class Diagram and Activity Diagram) of the implementation	TO DO
...	TO DO

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