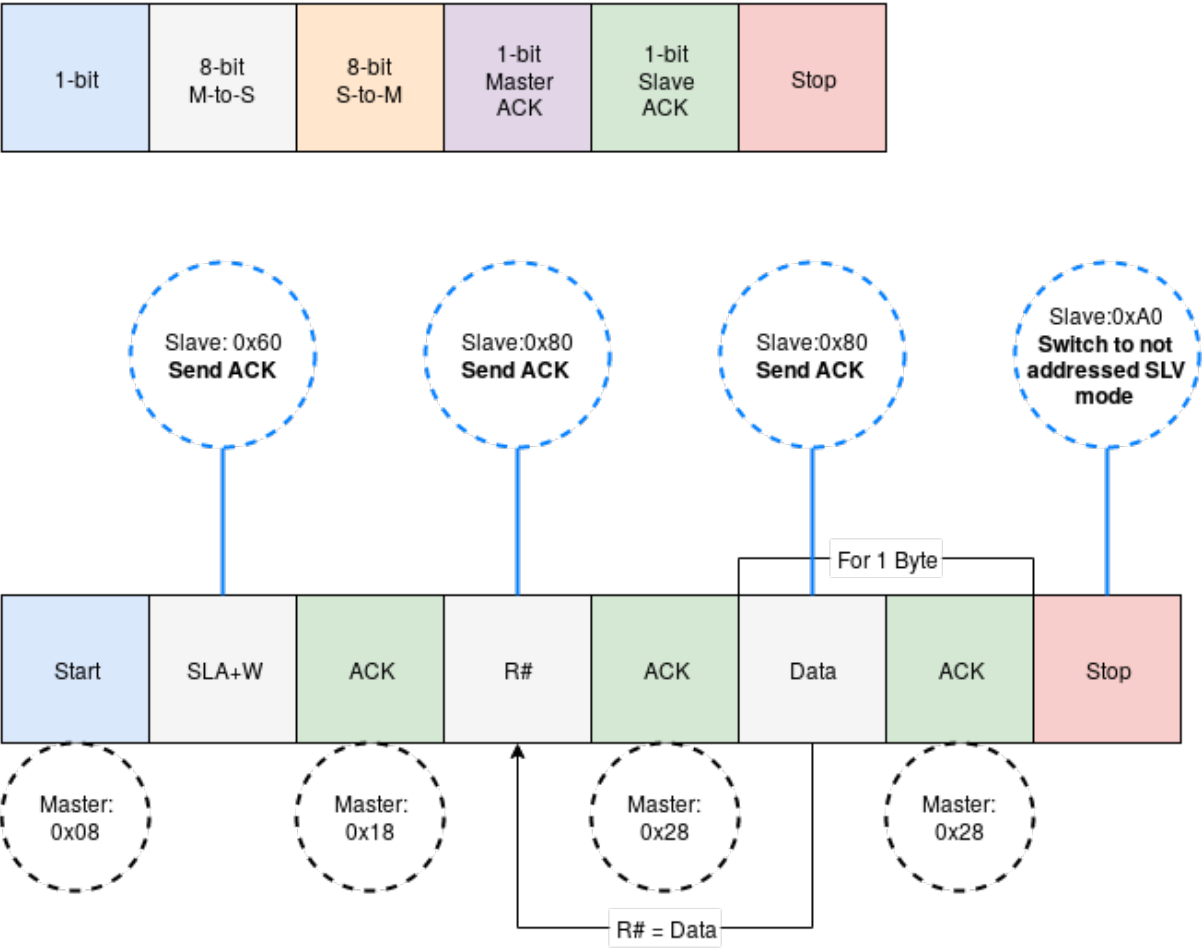
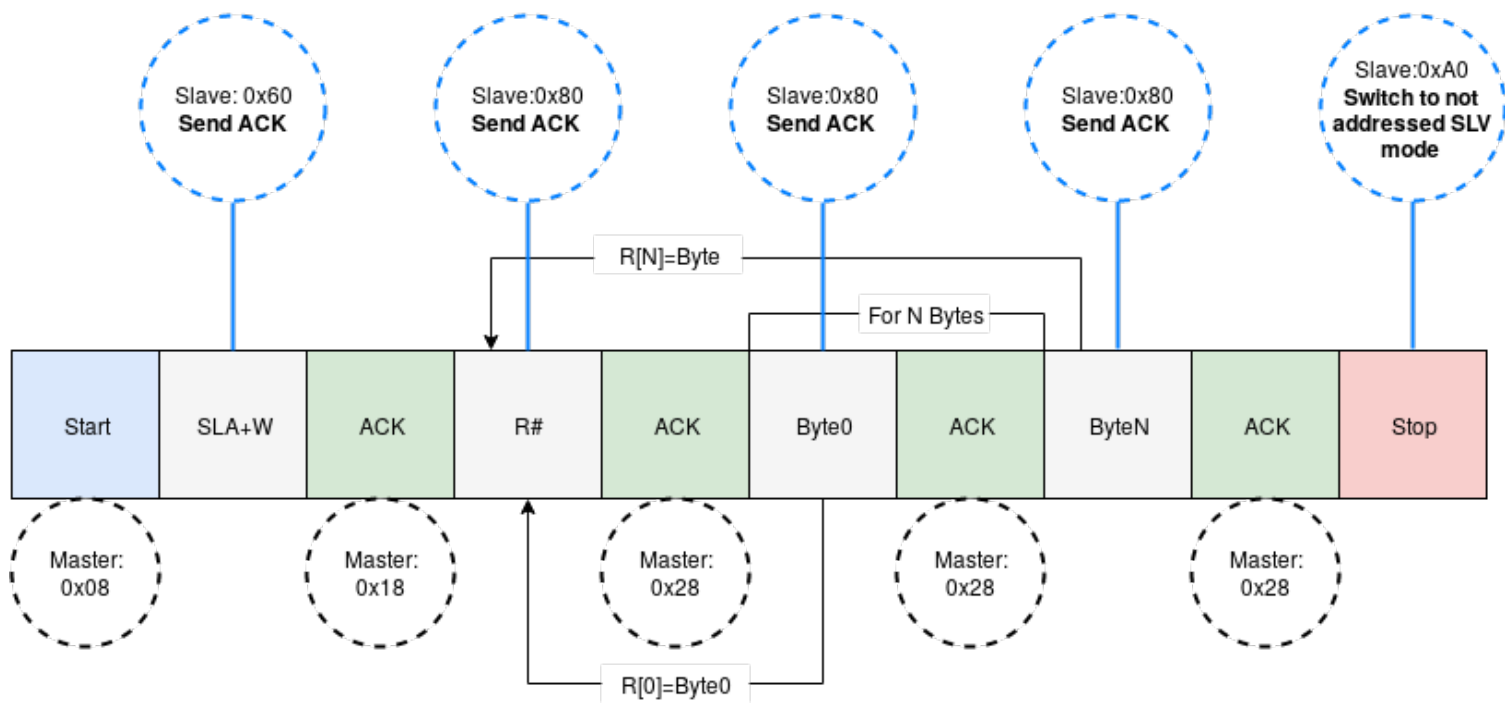


I2C State Machine

Master Write/Slave Receive





**Master Read/Slave Transmit:**

