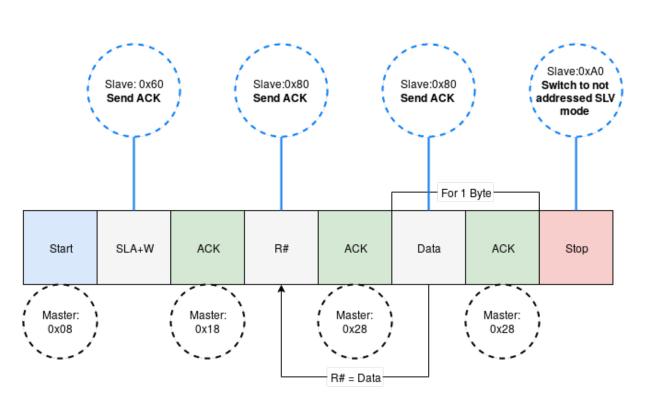
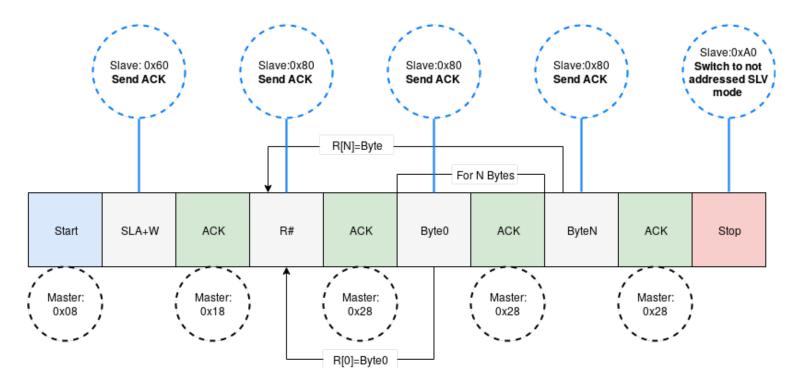
## **I2C State Machine**

## Master Write/Slave Receive

1-bit 8-bi		1-bit Master ACK	1-bit Slave ACK	Stop
------------	--	------------------------	-----------------------	------





**Master Read/Slave Transmit:** 

