```
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 *
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 */
/**
 * Ofile
 * @brief I2C2 Interrupt driven IO driver
 * 20131010 : Removed read vs. write mode variable
 */
#ifndef i2c2hpp_
#define i2c2hpp
#include "singleton_template.hpp"
#include "i2c_base.hpp"
/**
 * I2C2 Singleton Driver
 st This is a thin wrapper around I2C_Base class and merely gives the base
 * address of the I2C2 memory map.
 * @ingroup Drivers
 */
class I2C2 : public I2C_Base, public SingletonTemplate<I2C2>
{
    public:
        /// Initializes I2C2 at the given <code>Oparam</code> speedInKhz
        bool init(unsigned int speedInKhz);
        /// Initializes I2C2 at the given <code>Oparam</code> speedInKhz but as a Slave
        bool initSlave(const uint8_t slaveAddr, volatile uint8_t * buffer,
         uint32_t buffer_size);
```