Lecture 2: Data representation, addresses

September 4, 2019

601.229 Computer System Fundamentals



Welcome!

- ► Today:
 - ▶ Data representation
 - Addresses
 - Bitwise operations

Data representation

There are only kinds of people.

Those who understand binary and those who don't.

► Basic units

► Basic units

► Additive combination of units

II III VI XVI XXXIII MDCLXVI MMXVI

Basic units

Additive combination of units

► Basic units

Additive combination of units

► Subtractive combination of units

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Subtractive combination of units



► Basic units

Additive combination of units

Subtractive combination of units



Arabic Numerals

- Developed in India and Arabic world during the European Dark Age
- ▶ Decisive step: invention of zero by Brahmagupta in AD 628
- ► Basic units

0 1 2 3 4 5 6 7 8 9

► Positional system

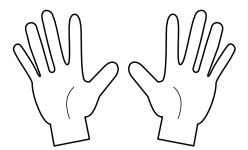
1 10 100 1000 10000 100000 1000000

Why Base 10?

dig∙it /ˈdijit/ •

noun

- any of the numerals from 0 to 9, especially when forming part of a number. synonyms: numeral, number, figure, integer "the door code has ten digits"
- a finger (including the thumb) or toe. synonyms: finger, thumb, toe; extremity "we wanted to warm our frozen digits"





► Decoding binary numbers

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Binary number	1	1	0	1	0	1	0	1
Position	7	6	5	4	3	2	1	0
Value	2 ⁷	2^{6}	0	2^4	0	2^2	0	2 ⁰

► Decoding binary numbers

Binary number	1	1	0	1	0	1	0	1	
Position	7	6	5	4	3	2	1	0	
Value	2^{7}	2^6	0	2^4	0	2^2	0	20	
	128	64	0	16	0	4	0	1	= 213

Clicker quiz 1

Clicker quiz omitted from public slides

- ▶ Numbers like 11010101 are very hard to read
- ⇒ Octal numbers

Binary number	1	1	0	1	0	1	0	1
Octal number	_		_		_	-		_
Octal Hulliber	•	J		_			5	

- ▶ Numbers like 11010101 are very hard to read
- \Rightarrow Octal numbers

Binary number	1	1	0	1	0	1	0	1
	_		_		_	-		_
Octal number	;	3		2			5	
Position	:	2		1			0	

- ▶ Numbers like 11010101 are very hard to read
- \Rightarrow Octal numbers

Binary number	1	1	0	1	0	1	0	1
	_		_		_	_		_
Octal number	;	3		2			5	
Position	:	2		1			0	
Value	3 >	< 8 ²	2	× 8	31	5	× 8	0

- ▶ Numbers like 11010101 are very hard to read
- \Rightarrow Octal numbers

Binary number	1 1	1 0	1	0	1	0	1		
		-		-	_		_		
Octal number	3		2			5			
Position	2		1			0			
Value	3×8	3^2 2	2 × 8	1	5	× 8	0		
	192		16			5		= 2	213

... but grouping three binary digits is a bit odd

- ▶ Grouping 4 binary digits \rightarrow base $2^4 = 16$
- "Hexadecimal" (hex = Greek for six, decimus = Latin for tenth)

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Hexadecimal number			 d				 5	

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Binary number	1	1	0	1	0	1	0	1	
Hexadecimal number Position		(d 1						
Value		13 ×	16	1		5 ×	16 ⁰		
		20	90			ĺ	5		= 213

Clicker quiz 2

Clicker quiz omitted from public slides

Examples

Decimal	Binary	Octal	Hexademical
0			
1			
2			
3			
8			
15			
16			
20			
23			
24			
30			
50			
100			
255			
256			

Examples

Decimal	Binary	Octal	Hexademical
0	0	0	0
1	1	1	1
2	10	2	2
3	11	3	3
8	1000	10	8
15	1111	17	f
16	10000	20	10
20	10100	24	14
23	10111	27	17
24	11000	30	18
30	11110	36	1e
50	110010	62	32
100	1100100	144	64
255	11111111	377	ff
256	100000000	400	100

Bytes and Words

- ➤ On all modern computers data is accessed in chunks of 8 bits: 1 byte
- ► Larger chunks of data ("words") are formed from multiple bytes:
 - ▶ 2 bytes = 16 bits
 - ▶ 4 bytes = 32 bits
 - ▶ 8 bytes = 64 bits
- Modern CPUs have instructions for doing operations on word-sized data values

C data types

- ► The "primitive" C data types typically map onto machine word sizes
 - ▶ ... but unfortunately, not in a way that's completely consistent across different machines and compilers
- "Typical" representations of C data types:

	Bytes used on						
Data type	32-bit systems	64-bit systems					
char	1	1					
short	2	2					
int	4	4					
long	4	8					

Portable integer types

- Note that constant values are still a problem!

Addresses

Memory and addresses

- Conceptually, memory (RAM) is a sequence of byte-sized storage locations
- ► Each byte storage location has an integer address
 - 0 is the lowest address
 - ► Highest address determined by number of *address bits* processor uses:
 - ▶ 32-bit processors ⇒ addresses have 32 bits
 - ▶ 64-bit processors ⇒ addresses have 64 bits

32 bit vs. 64 bit addresses

- ► 1 GB = 2^{30} , 1 TB = 2^{40}
- ► A 32-bit system can directly address 2³² bytes (4 GB)
 - ▶ Not that much memory by today's standards!
- ► A 64-bit system can directly access $2^{64} = 17,179,869,184$ GB = 16,777,216 TB
 - ► This is a *huge* address space
 - Note that actual systems don't support that much physical memory
 - ► However, tens or hundreds of GB of physical memory is not uncommon

Alignment

- ► To store the value of an *n*-bit word in memory, *n* contiguous bytes are used
- ▶ The address of the first byte is the address of the overall word
- ► Typically, an *n*-byte word must have an address that is an exact multiple of *n* ("natural" alignment)
 - ► For example, the first byte allocated for an 8-byte word must have an address that is an exact multiple of 8
- ► Attempt to load or store an *n*-byte word at an address that is not a multiple of *n* is an *unaligned access*
 - ▶ Best case: access works, reduced performance
 - ▶ Worst case: runtime exception that kills the program

Addresses in C

- ▶ Pointers in C are just memory addresses!
- ► The address-of operator (&), when applied to a variable, yields a pointer to the variable (i.e., the address of the first memory byte that is part of the variable's storage)
- ► The dereference operator (*), when applied to a pointer value (address), refers to the variable whose storage location is indicated by the address

Example C program

TODO

Bitwise operations

Bitwise operations

- ► *Bitwise* operations operate on the binary (bit-level) representation of an integer data value
- Logical operations: and, or, exclusive or, complement
- ► Shifts: left shift, right shift

Operations on boolean values

We can think of bit values (1 or 0) as being *Boolean* values (true or false)

Logical operations on bits ${\boldsymbol a}$ and ${\boldsymbol b}$:

		and	or	xor
а	b	a & b	a b	a ^ b
0	0	0	0	0
0	1	0	1	1
1	0	0	1	1
1	1	1	1	0

Logical negation ("complement") on a single bit **a**:

а	~a
0	1
1	0

Bitwise operations in C

- ► The C bitwise operators perform logical operations (and, or, xor, negation) on the bits of the binary representation(s) of integer values
 - ► For example, x | y computes a result whose bits are formed by applying the bitwise or operator (|) to each pair of bits in x and y
- Example code (bitwise or):

```
int x = 11;
int y = 40;
int z = x | y;
printf("%d\n", z);
```

▶ What does this code do?

```
int x = 11;
int y = 40;
int z = x | y;
printf("%d\n", z);

decimal binary
```

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int x = 11;
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```

	decimal	binary
х	11 = 8 + 2 + 1	00001011

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	decimal	binary
Х	11 = 8 + 2 + 1	00001011
У	40 = 32 + 8	00101000

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int x = 11;
int y = 40;
int z = x | y;
printf("%d\n", z);
```

	decimal	binary
х	11 = 8 + 2 + 1	00001011
У	40 = 32 + 8	00101000
$x \mid y$	43 = 32 + 8 + 2 + 1	00101011

```
int x = 11;
int y = 40;
int z = x | y;
printf("%d\n", z);
```

	decimal	binary
Х	11 = 8 + 2 + 1	00001011
У	40 = 32 + 8	00101000
хІу	43 = 32 + 8 + 2 + 1	00101011

Bit is 1 in result if corresponding bit is 1 in either operand value

Shifts

- ► Shifts move bits to the left or right in the binary representation of a data value
- ► Example code (left shift):

```
int x = 21;
int y = x << 3;
printf("%d\n", y);</pre>
```

► What does this code do?

```
int x = 21;
int y = x << 3;
printf("%d\n", y);

decimal binary</pre>
```

```
int x = 21;

int y = x << 3;

printf("%d\n", y);

\frac{\text{decimal}}{\text{x}} \quad \text{binary}
```

```
int x = 21;
int y = x << 3;
printf("%d\n", y);</pre>
```

	decimal	binary
х	21 = 16 + 4 + 1	00010101
x << 3	168 = 128 + 32 + 8	10101000

	decimal	binary
X	21 = 16 + 4 + 1	00010101
x << 3	168 = 128 + 32 + 8	10101000

Each bit in original value is shifted 3 places to the left; the lowest 3 bits of result become 0

Why bitwise operations are useful

- ▶ Bitwise operations (logical operations and shifts) are useful because they allow precise manipulations of data values at the level of individual bits:
 - ► Selecting arbitrary bits
 - ► Clearing or setting arbitrary bits