

```
1  // Jonathan Monreal
2
3  module partD(a, b, y1, y2, y3);
4
5      input a, b;
6      output y1, y2, y3;
7
8      // This is the body of the module in design flow style
9
10     assign y1 = a & b;
11     assign y2 = a | b;
12     assign y3 = a ^ b;
13
14 endmodule
```