

# Methods and Systems for Quantized Pitch-Family Selection and Invariant Control of Golden-Ratio Logarithmic-Spiral Geometries

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## Abstract

Disclosed are methods, systems, and computer-readable media for enforcing *quantized pitch families* and invariant geometric relationships in logarithmic-spiral scaffolds defined using the golden ratio  $\varphi$ . In various embodiments, a spiral scaffold is defined by

$$r(\theta) = r_0 \cdot \varphi^{\kappa\theta/(2\pi)},$$

where  $r_0 > 0$  is a base radius,  $\theta$  is an angular coordinate, and  $\kappa \in \mathbb{Z}$  is an integer pitch-family parameter. By restricting  $\kappa$  to integers (or otherwise quantized values), a design space is partitioned into discrete pitch families characterized by a per-turn multiplier  $M(\kappa) = \varphi^\kappa$ . The disclosure provides closed-form invariants including (i) a step ratio  $\varphi^{\kappa\Delta\theta/(2\pi)}$  for any angular increment  $\Delta\theta$ , (ii) the per-turn multiplier  $M(\kappa)$ , and (iii) pitch-family shift relationships  $M(\kappa + d) = M(\kappa)\varphi^d$  for integers  $d$ . These invariants enable scale-invariant design rules, validation of manufactured geometry, database indexing of geometries by family, and constrained optimization that selects  $\kappa$  from discrete families subject to manufacturing, electrical, and geometric constraints. The disclosure further provides methods for selecting, searching, and validating pitch-family parameters, and for compiling and distributing fabrication artifacts and configuration packages that encode pitch-family invariants.

## Technical Field

The present disclosure relates to geometric parameterization and selection of logarithmic-spiral geometries, and more particularly to apparatus and methods for enforcing discrete pitch-family constraints and invariant relationships in golden-ratio logarithmic spirals used in rotors, traces, coil arrays, and associated manufacturing and control workflows.

## Background

Logarithmic spirals appear in multiple engineering contexts (e.g., antennas, inductors, and mechanical spiral structures). Conventional approaches typically treat the spiral “growth rate” as a continuous variable that is curve-fit or adjusted during prototyping. This practice makes it difficult to:

- reproduce a geometry across scales and manufacturing processes,
- build discrete-element arrays that preserve intended multiplicative spacing,

- index and search design libraries in a compact parameter space,
- enforce cross-run comparability when geometries drift through continuous tuning,
- validate manufactured fidelity beyond basic dimensional checks.

Accordingly, there is a need for a spiral geometry specification that (i) partitions the design space into discrete pitch families and (ii) provides closed-form invariants that are preserved under scaling and discretization.

## Summary

This disclosure provides systems and methods that treat the pitch parameter  $\kappa$  of a golden-ratio logarithmic spiral as a *quantized design variable* (e.g., an integer). This yields discrete pitch families with predictable invariants. In one aspect, a computing system selects  $\kappa$  from a discrete set based on constraints. In another aspect, the system validates manufactured geometry by comparing measured step ratios or per-turn multipliers against closed-form invariants.

The disclosure further provides:

- a pitch-family index  $F = \kappa$  stored and transmitted as an integer;
- an invariant per-turn multiplier  $M(\kappa) = \varphi^\kappa$ ;
- an invariant step ratio  $S(\kappa, \Delta\theta) = \varphi^{\kappa\Delta\theta/(2\pi)}$ ;
- a pitch-family shift identity  $M(\kappa + d) = M(\kappa)\varphi^d$ ;
- methods to search over discrete pitch families and select a family meeting constraints;
- methods to compile, label, and distribute fabrication files and configuration packages that encode the pitch family.

## Brief Description of the Drawings

Drawings may be provided in a later filing or as attachments to this disclosure. For purposes of the present specification, the following figures are described:

- **FIG. 1** shows discrete pitch families indexed by  $\kappa$  and the associated per-turn multiplier  $M(\kappa) = \varphi^\kappa$ .
- **FIG. 2** shows step ratio invariance for multiple base radii  $r_0$ .
- **FIG. 3** shows a constrained search over integer  $\kappa$  subject to geometry bounds and manufacturing constraints.
- **FIG. 4** shows an example database schema and a family-indexed design library.
- **FIG. 5** shows manufactured-geometry validation by comparing measured step ratios to invariant predictions.
- **FIG. 6** shows optional packaging of pitch-family parameters and invariants into signed configuration artifacts.

## Definitions and Notation

Unless otherwise indicated:

- $\varphi$  (golden ratio) is defined as  $\varphi = (1 + \sqrt{5})/2$ .
- $r_0 \in \mathbb{R}_{>0}$  is a base radius (scale parameter).
- $\theta \in \mathbb{R}$  is an angular coordinate (radians).
- $\kappa \in \mathbb{Z}$  is an integer pitch-family parameter.
- A *pitch family* refers to the set of geometries sharing the same integer  $\kappa$  (and thus the same per-turn multiplier).
- A *per-turn multiplier* refers to  $M(\kappa) = \varphi^\kappa$ .
- A *step ratio* refers to  $S(\kappa, \Delta\theta) = \varphi^{\kappa\Delta\theta/(2\pi)}$ .
- A *configuration artifact* refers to a file or record encoding  $(r_0, \kappa)$  and optional derived invariants.

## Detailed Description

### 1. Spiral Scaffold Definition (Context)

In many embodiments, a spiral scaffold is defined by:

$$r(\theta; r_0, \kappa) = r_0 \cdot \varphi^{\kappa\theta/(2\pi)}. \quad (1)$$

This disclosure focuses on *quantized selection and invariant relationships* of the pitch-family parameter  $\kappa$ , including design rules and validation workflows. The scaffold itself may be used for rotors, traces, coil arrays, and other embodiments.

### 2. Closed-Form Invariants

**2.1 Step ratio (invariant under base-radius scaling).** For any  $\Delta\theta \in \mathbb{R}$ ,

$$\begin{aligned} \frac{r(\theta + \Delta\theta; r_0, \kappa)}{r(\theta; r_0, \kappa)} &= \frac{r_0 \varphi^{\kappa(\theta + \Delta\theta)/(2\pi)}}{r_0 \varphi^{\kappa\theta/(2\pi)}} \\ &= \varphi^{\kappa\Delta\theta/(2\pi)}. \end{aligned} \quad (2)$$

Define  $S(\kappa, \Delta\theta) := \varphi^{\kappa\Delta\theta/(2\pi)}$ . The step ratio does not depend on  $r_0$  or  $\theta$ .

**2.2 Per-turn multiplier.** For  $\Delta\theta = 2\pi$ ,

$$\frac{r(\theta + 2\pi; r_0, \kappa)}{r(\theta; r_0, \kappa)} = \varphi^\kappa. \quad (3)$$

Define  $M(\kappa) := \varphi^\kappa$ . The multiplier describes the radial scaling after one full revolution.

**2.3 Pitch-family shift identity.** For any integer  $d$ ,

$$M(\kappa + d) = \varphi^{\kappa+d} = \varphi^\kappa \cdot \varphi^d = M(\kappa) \varphi^d. \quad (4)$$

Thus pitch families form a multiplicative lattice.

**2.4 Equivalent exponential form.** Eq. (1) may be written:

$$r(\theta) = r_0 \cdot e^{(\kappa \ln \varphi) \theta / (2\pi)}. \quad (5)$$

This form is useful for numerical stability and for implementing design-rule solvers.

### 3. Quantized Pitch-Family Constraint

The key design constraint is that  $\kappa$  is not treated as an arbitrary real number. Instead,  $\kappa$  is restricted to a discrete set, e.g. integers:

$$\kappa \in \mathbb{Z} \quad (\text{or a discrete subset of integers}).$$

This has multiple technical advantages:

- **Compact design-space index.** A single integer indexes a family.
- **Reproducibility across iterations.** Iteration does not drift continuously.
- **Scale invariance.** Designs can scale via  $r_0$  without changing invariants.
- **Constraint-friendly search.** Discrete search over  $\kappa$  supports robust optimization under manufacturing constraints.

### 4. Family Selection under Constraints

**4.1 Constraint examples.** Typical constraints include:

- an outer radius limit  $r(\theta_{\max}) \leq r_{\max}$ ,
- an inner radius limit  $r(\theta_{\min}) \geq r_{\min}$ ,
- minimum feature size, minimum curvature radius, and spacing constraints for traces,
- target per-turn multiplier bounds  $M_{\min} \leq M(\kappa) \leq M_{\max}$ ,
- discretization constraints (e.g., chord length or radius increments per sample).

**4.2 Example: bounding radius after  $T$  turns.** Let  $\theta_{\max} = 2\pi T$  for  $T \in \mathbb{N}$ . Then:

$$r(2\pi T) = r_0 \cdot \varphi^{\kappa T}.$$

Given  $r(2\pi T) \leq r_{\max}$ , one obtains:

$$\kappa \leq \frac{\ln(r_{\max}/r_0)}{T \ln \varphi}.$$

When  $\kappa$  is restricted to integers, the feasible set is  $\kappa \in \{\dots, \lfloor \ln(r_{\max}/r_0)/(T \ln \varphi) \rfloor\}$ , enabling discrete search and stable selection.

**4.3 Example: selecting sampling density from step ratio.** Given a desired multiplicative radial ratio  $q > 0$  between successive samples separated by  $\Delta\theta$ , solve:

$$q = S(\kappa, \Delta\theta) = \varphi^{\kappa \Delta\theta / (2\pi)}.$$

For  $\kappa \neq 0$ :

$$\Delta\theta = \frac{2\pi \ln q}{\kappa \ln \varphi}.$$

This yields a direct design rule linking pitch-family selection to discretization density.

**4.4 Discrete search (non-limiting).** In one embodiment, a system searches over a discrete candidate set  $\kappa \in \mathcal{K} \subset \mathbb{Z}$ , computes derived invariants  $M(\kappa)$  and  $S(\kappa, \Delta\theta)$ , checks constraints, and selects a  $\kappa$  that optimizes an objective (e.g., maximize feasible radius range, minimize curvature error, minimize manufacturing risk).

## 5. Manufacturing and Metrology Validation Using Invariants

**5.1 Measuring step ratios.** Given a manufactured part, measure radii at angles  $\theta$  and  $\theta + \Delta\theta$  (by optical metrology, CMM, or image processing) to obtain an empirical step ratio:

$$\hat{S}(\Delta\theta) = \frac{\hat{r}(\theta + \Delta\theta)}{\hat{r}(\theta)}.$$

Compare against predicted  $S(\kappa, \Delta\theta)$  from Eq. (2).

**5.2 Estimating  $\kappa$  from measurements.** Taking logs:

$$\ln \hat{S}(\Delta\theta) \approx \frac{\kappa \Delta\theta}{2\pi} \ln \varphi.$$

Thus:

$$\hat{\kappa} \approx \frac{2\pi}{\Delta\theta} \cdot \frac{\ln \hat{S}(\Delta\theta)}{\ln \varphi}.$$

In one embodiment,  $\hat{\kappa}$  is rounded to the nearest integer and compared to the stored design  $\kappa$ . Deviations beyond tolerance indicate manufacturing error, mis-scaling, or measurement error.

**5.3 Family-indexed quality control.** In one embodiment, a quality-control system stores  $\kappa$  as a lot attribute and enforces that measured  $\hat{\kappa}$  matches the intended family within tolerance. This yields a family-level control chart and lot acceptance criteria.

## 6. Data Structures, Libraries, and Configuration Artifacts

**6.1 Database indexing.** In one embodiment, a design library indexes spiral geometries by:

$$(\kappa, r_0, T, \text{layer id, manufacturing profile}).$$

The integer  $\kappa$  enables efficient lookup, de-duplication, and search.

**6.2 Configuration artifacts.** In one embodiment, a configuration artifact encodes:

$$\mathcal{C} = (r_0, \kappa, n, \theta_{\text{start}}, M(\kappa), \text{constraints}),$$

optionally including cryptographic hashes and signatures to prevent unintended drift or tampering in iterative workflows.

## 7. Example Embodiments (Non-Limiting)

**Embodiment A: family-labeled CAD generation.** A CAD tool receives  $(r_0, \kappa)$ , generates the curve, embeds a label “FAMILY= $\kappa$ ” in the drawing metadata, and exports the CAD file. Downstream tooling verifies the family label and derived  $M(\kappa)$ .

**Embodiment B: constrained selection for PCB spiral.** Given trace width and spacing constraints, the system enumerates  $\kappa \in \{-3, -2, -1, 0, 1, 2, 3\}$ , computes curvature bounds and expected radius ranges, and selects the smallest  $|\kappa|$  meeting spacing constraints while achieving a required outer radius within a board outline.

**Embodiment C: manufacturing validation via optical scan.** An optical scan estimates  $\hat{\kappa}$  from measured step ratios and compares it to the stored  $\kappa$ . If mismatch is detected, the part is rejected or reworked.

**Embodiment D: discrete family sweep.** For exploratory work, the system generates a set of coupons with  $\kappa$  in a discrete range and identical  $r_0$ , enabling controlled comparison between pitch families.

## Claims (Draft)

**Note:** The following claims are an initial, non-limiting claim set intended to preserve multiple fallback positions. Final claim strategy should be reviewed by counsel.

### Independent Claims

1. **(Method)** A method of selecting a pitch family for a golden-ratio logarithmic spiral geometry, the method comprising: receiving a constraint set for a spiral geometry; enumerating candidate pitch-family parameters  $\kappa$  from a discrete set; for each candidate  $\kappa$ , computing a per-turn multiplier  $M(\kappa) = \varphi^\kappa$  and determining whether the candidate satisfies the constraint set; selecting a pitch-family parameter  $\kappa$  that satisfies the constraint set; and generating a fabrication-ready representation of the spiral geometry using the selected  $\kappa$ .
2. **(System)** A system comprising one or more processors and memory storing instructions that, when executed by the one or more processors, cause the system to: store a pitch-family parameter  $\kappa$  as a quantized value; compute one or more invariants of a golden-ratio logarithmic spiral geometry including at least one of (i) a step ratio  $S(\kappa, \Delta\theta) = \varphi^{\kappa\Delta\theta/(2\pi)}$  or (ii) a per-turn multiplier  $M(\kappa) = \varphi^\kappa$ ; and output at least one of (a) a fabrication artifact representing the geometry or (b) a configuration artifact encoding the quantized pitch family.
3. **(Non-transitory medium)** A non-transitory computer-readable medium storing instructions that, when executed by one or more processors, cause the one or more processors to: receive measurement data of a manufactured spiral geometry; compute an estimated pitch-family parameter  $\hat{\kappa}$  from the measurement data using a closed-form relationship between measured step ratios and  $\kappa$ ; compare the estimated pitch-family parameter to a stored intended pitch-family parameter  $\kappa$ ; and output a validation result indicating whether the manufactured spiral geometry conforms to the stored pitch family.

### Dependent Claims (Examples; Non-Limiting)

4. The method of claim 1, wherein enumerating candidate pitch-family parameters comprises enumerating integer values  $\kappa \in \mathbb{Z}$ .
5. The method of claim 1, wherein the constraint set comprises an outer radius bound after a specified number of turns.

6. The method of claim 1, further comprising selecting a sampling density based on a desired multiplicative radial ratio  $q$  using  $\Delta\theta = 2\pi \ln q / (\kappa \ln \varphi)$ .
7. The system of claim 2, wherein the configuration artifact further includes a cryptographic hash of the pitch-family parameter and derived invariants.
8. The system of claim 2, wherein the discrete set of candidate pitch-family parameters comprises a subset of integers authorized by a design policy.
9. The non-transitory medium of claim 3, wherein the measurement data is obtained from an optical scan of a manufactured part.
10. The non-transitory medium of claim 3, wherein computing the estimated pitch-family parameter comprises computing  $\hat{\kappa} = (2\pi/\Delta\theta) \cdot (\ln \hat{S}(\Delta\theta) / \ln \varphi)$  and rounding to a nearest integer.
11. The non-transitory medium of claim 3, further comprising generating a quality-control report indexed by the stored pitch-family parameter  $\kappa$ .
12. The method of claim 1, wherein generating the fabrication-ready representation comprises exporting at least one of DXF, SVG, STL, or GERBER files.

## Additional Embodiments and Fallback Positions (Non-Limiting)

The following are included to maximize optionality for later claim drafting:

- Quantization of  $\kappa$  may be implemented as integer quantization, half-integer quantization, or membership in a finite approved set (e.g.,  $\kappa \in \{-3, -2, -1, 0, 1, 2, 3\}$ ).
- Pitch-family selection may be performed by brute-force enumeration, constraint programming, integer programming, or mixed-integer optimization.
- Validation may use one or more measured step ratios across multiple  $\Delta\theta$  values and may average or regress to reduce noise.
- Pitch-family parameters and invariants may be used to label and version designs, test coupons, and manufacturing lots, enabling traceability.
- A pitch-family-aware toolchain may reject fabrication jobs where the requested  $\kappa$  deviates from an approved family set or where derived invariants violate constraints.

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**End of Specification (Draft)**