



- Ch1 Overview of System Design Using SystemC
- Ch2 Overview of SystemC



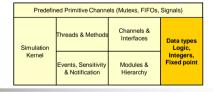
- Ch3 Data Types
- Ch4 Modules
- Ch5 Notion of Time
- Ch6 Concurrency
- Ch7 Predefined Channels
- Ch8 Structure
- Ch9 Communication
- Ch10 Custom Channels and Data
- Ch11 Transaction Level Modeling



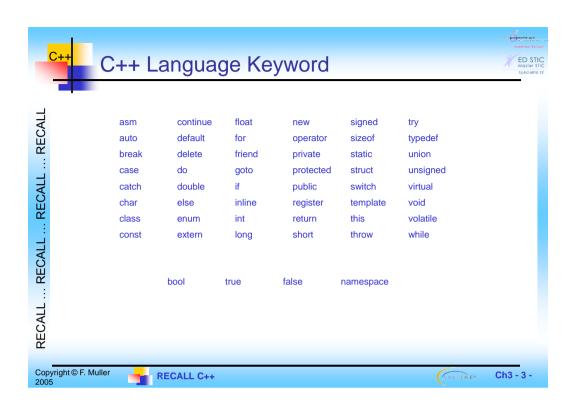


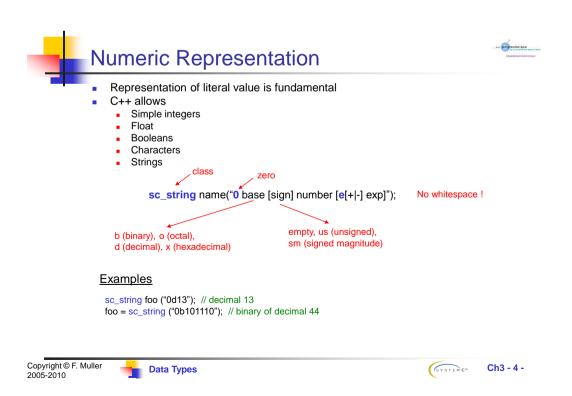


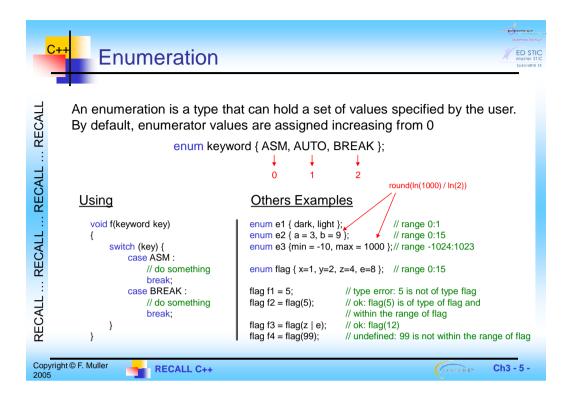




- Numeric Representation
- Native & Arithmetic Data Types
- Bit Types
- Fixed-Point Data Types
- User Defined Data Types
- Higher level of abstraction with STL
- Conclusion









Numeric Representation



	sc_numrep	Prefix	ivieaning
enum sc_numrep	SC_DEC	0d	Decimal
{ SC_NOBASE = 0,	SC_BIN	0b	Binary
SC_BIN = 2, SC_OCT = 8,	SC_BIN_US	0bus	Binary unsigned
SC_DEC = 10, SC_HEX = 16,	SC_BIN_SM	0bsm	Binary signed magnitude
SC_BIN_US, SC_BIN_SM,	SC_OCT	00	Octal
SC_OCT_US, SC_OCT_SM,	SC_OCT_US	0ous	Octal unsigned
SC_HEX_US, SC_HEX_SM,	SC_OCT_SM	0osm	Octal signed magnitude
SC_CSD };	SC_HEX	0x	Hex
	SC_HEX_US	0xus	Hex unsigned

SC_HEX_SM

0xsm

D...("...

Hex signed magnitude

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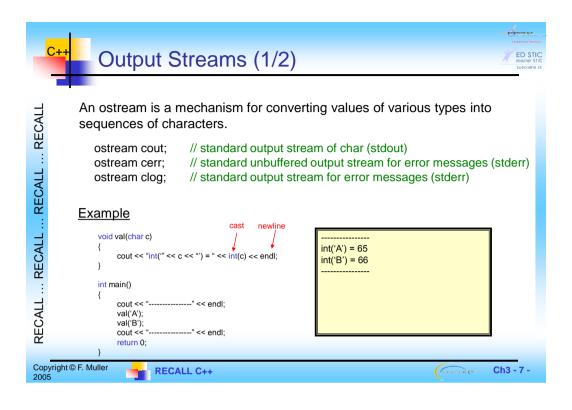
sc_int<5>(-13)*

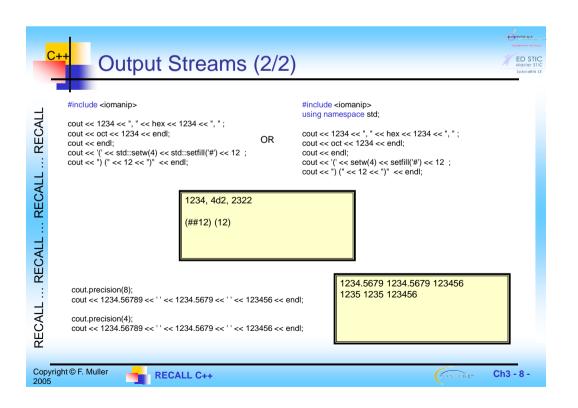
"-0d13" "0b10011" "0bus01101" "-0bsm01101"

"0063" "0ous15"

"-0osm03" "0xf3" "0xus0d"

"-0xsm0d"







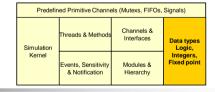
Numeric Representation Example



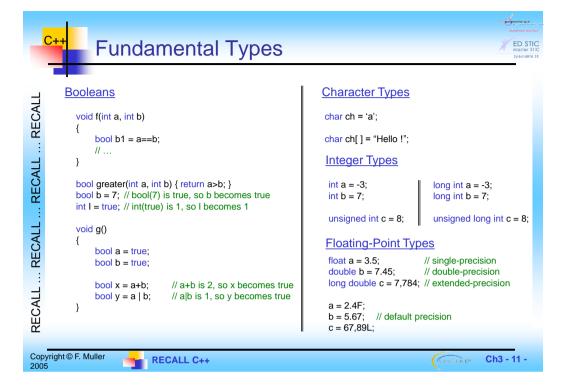
```
sc int<8> rx data = 106:
                                                               numeric_representation
                            sc int<4> tx buf = -5:
                             \begin{array}{lll} \text{cout} << \text{"Default: } rx\_data=" << rx\_data.to\_string() << \text{endl}; \\ \text{cout} << \text{"Binary: } rx\_data=" << rx\_data.to\_string(SC\_BIN) << \text{endl}; \\ \text{cout} << \text{"Binary unsigned: } rx\_data=" << rx\_data.to\_string (SC\_BIN\_US) << \text{endl}; \\ \text{cout} << \text{"Binary sign magnitude: } rx\_data=" << rx\_data.to\_string(SC\_BIN\_SM) << \text{endl}; \\ \end{array} 
                            cout << "Octal: tx_buf=" << tx_buf.to_string (SC_OCT) << endl;
                            cout << "Binary without base: rx_data=" << rx_data.to_string(SC_BIN, false) << endl;
                            Default: rx_data=106
                                                    Binary: rx_data=0b01101010
                                                    Binary unsigned: rx_data=0bus1101010
                                                    Binary sign magnitude: rx_data=0bsm01101010
                                                    Octal: tx_buf=0o73
                    Output produced
                                                    Hexadecimal: tx_buf=0xb
                                                    Decimal: tx_buf=-5
                                                    Binary without base: rx_data=01101010
                                                    Hexadecimal without base: tx_buf=b
                                                    Decimal without base: tx_buf=-5
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                                                                                                                                              Ch3 - 9 -
                                      Data Types
```







- Numeric Representation
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Native Data Types

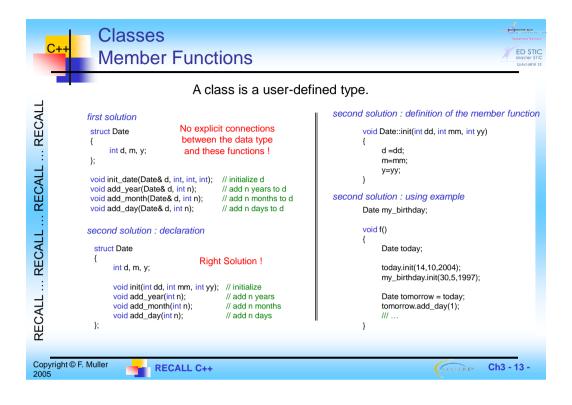
Data Types

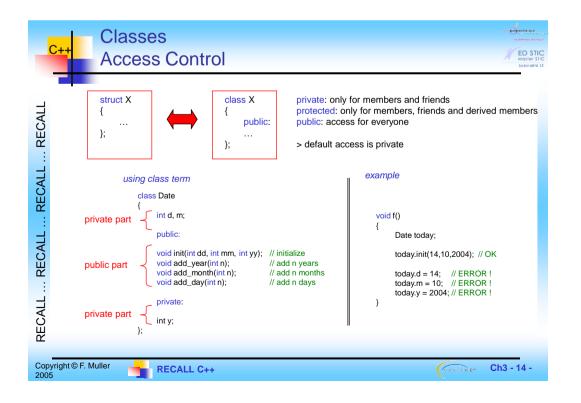


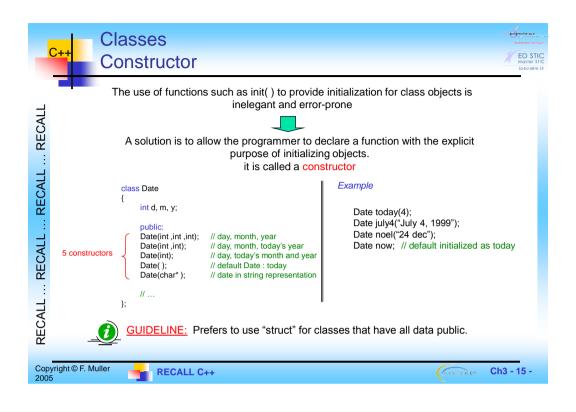
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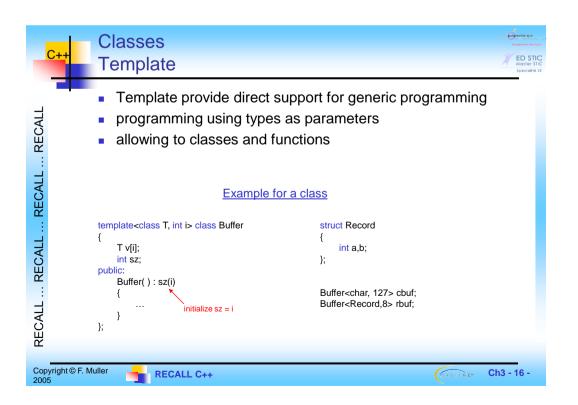
- SystemC supports all the native C++ data types
- Most efficient in terms of memory usage
- Most efficient execution speed of the simulator

```
// Example
                                                            spark_offset;
                                            int
                                            unsigned
                                                            repairs = 0;
                                                            mileage;
                                            unsigned long
                                            short int
                                                            speedometer;
                                            float
                                                            temperature;
                                            double
                                                            time_of_last_request;
Not equal to sc_string ! (SystemC v2.01)
                                            std::string
                                                            license_plate;
    equal to string! (SystemC 2.1)
                                                            WARNING_LIGHT = true;
                                            const bool
                                                            compass { SW, W, NW, N, NE, E, SE, S };
                                            enum
```











Arithmetic Data Types sc_int and sc_uint



- By default : 64 bits
- Slower than the native types (int)

```
1 to 64 bits wide
sc int<length>
                  name ...;
sc_uint<length> name ...;
```

Example

sc int<4> a; // Represents variable "a" of 4 bits width





GUIDELINE: One necessary condition for using sc_int is when using synthesis tools that require hardware representation.

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Data Types





Arithmetic Data Types sc_bigint and sc_biguint

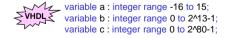


- More than 64 bits!
- Slower than sc_int

```
sc_bigint</length>
                  name ...;
sc_biguint// name ...;
```

Example

```
sc int<5>
                  a; // 5 bits : 4 plus sign
sc_uint<13>
                  b; // 13 bits : no sign
sc_biguint<80> c; // 80 bits : no sign
```





GUIDELINE: Do not use sc_bigint for 64 or fewer bits. Doing so cause performance to suffer compared to using sc_int.









	Predefined Primitive Channels (Mutexs, FIFOs, Signals)					
Sim	Simulation Kernel	Threads & Methods	Channels & Interfaces	Data types Logic,		
Ke		Events, Sensitivity & Notification	Modules & Hierarchy	Integers, Fixed point		

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sc_bit and sc_bv (1/3) Introduction



- sc_bit (bit)
 - '0' or '1' value / SC_LOGIC_0 or SC_LOGIC_1
 - VHDL : bit
- sc bv
 - vector of bit
 - VHDL : bit_vector

sc_bit name ...; sc_bv<bitwidth> name ...;

Example

sc_bit flag(SC_LOGIC_1); sc_bv<5> positions = "01101"; sc_bv<6> mask = "100111"; positions.range(3,2) = "00"; positions[2] = mask[0] ^ flag;

variable name : bit;

variable name : bit_vector(0 to bitwidth-1); variable name : bit_vector(bitwidth-1 downto 0);

₹VHDL ₹ variable flag : bit := '1'; variable mag : bit := 1, variable positions : bit_vector(0 to 4) := "01101"; variable mask : bit_vector(0 to 5) := "100111";

position(2 to 3) := "00"; positions(2) := mask(0) xor flag;









operator	function	usage	bit	bit_vector
&	bitwise AND	expr1 & expr2	√	√
1	bitwise OR	expr1 expr2	√	√
^	bitwise XOR	expr1 ^ expr2	√	√
~	bitwise NOT	~expr	√	√
<<	bitwise shift left	expr << constant		√
>>	bitwise shift right	expr >> constant		√
=	assignment	value_holder = expr	√	√
&=	compound AND assignment	value_holder &= expr	√	√
=	compound OR assignment	value_holder = expr	√	√
^=	compound XOR assignment	value_holder^= expr	√	√
==	equality	expr1 == expr2	√	√
!=	inequality	expr1 != expr2	√	√
[]	bit selection	variable[index]		√
(,)	concatenation	(expr1, expr2, expr3)		√

// Bit example bool ready; sc_bit flag = sc_bit('0'); ready = ready & flag; if (ready == flag) // Bit vector example sc_bv<8> ctrl_bus; ctrl_bus[5] = '0' & ctrl_bus[6];

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Data Types



ctrl_bus << 2; // multiply by 4



sc_bit and sc_bv (3/3) Methods for bit vectors



Methods for bit vectors

method	function	usage
range()	range selection	var.range(index1,index2)
and_reduce()	reduction AND	var.and_reduce()
nand_reduce()	reduction NAND	var.nand_reduce()
or_reduce()	reduction OR	var.or_reduce()
nor reduce()	reduction NOR	var.nor_reduce()
xor_reduce()	reduction XOR	var.xor_reduce()
xnor_reduce()	reduction XNOR	var.xnor_reduce()

sc_bv<8> ctrl_bus;
$$\begin{split} & \mathsf{ctrl_bus.range}(0,\!3) = \mathsf{ctrl_bus.range}(7,\!4); \\ & \mathsf{mult} = (\mathsf{ctrl_bus}[0], \mathsf{ctrl_bus}[0], \mathsf{ctrl_bus}[0], \mathsf{ctrl_bus}[1]); \end{split}$$
ctrl_bus[0] = ctrl_bus.and_reduce(); ctrl_bus[1] = mult.or_reduce();

// Bit vector example

```
variable active : bit_vector(4 downto 0); VHDL active := positions and mask;
sc_bv<5> active = position & mask;
```

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sc logic and sc lv (1/2) Introduction



- sc_logic
 - '0', '1', 'Z', 'X' value / SC_LOGIC_0, SC_LOGIC_1, SC_LOGIC_Z, SC_LOGIC_X
 - sc_dt: Log_1, Log_0, Log_Z, Log_X
 - VHDL: std_logic
- sc_lv
 - range(), and_reduce(), or_reduce(), nand_reduce(), nor_reduce(), xor_reduce()
 - VHDL: std_logic_vector

```
variable name : std_logic;
sc logic
                       name ...;
sc lv<br/>bitwidth>
                       name ...;
                                                   variable name : std_logic_vector(0 to bitwidth-1);
                                                   variable name : std_logic_vector(bitwidth-1 downto 0);
Example
                                                variable buf : std_logic := 'Z';
                                                variable data_drive : std_logic_vector(7 downto 0) := "ZZ01XZ1Z";
using namespace sc_dt;
                                                data_drive(5 downto 4) := "ZZ";
                                                                                 2 AHDI'S
sc_logic buf(sc_dt::Log_Z);
                                               buf := '1'; -- ZZZZXZ1Z
sc_lv<8> data_drive("ZZ01XZ1Z");
data_drive.range (5,4) = "ZZ"; // ZZZZXZ1Z
buf = '1';
```

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Data Types



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sc_logic and sc_lv (2/2) **Resolution Table**



	AND						
1	&	'0'	'1'	'X'	ʻZ'		
1	'0'	'0'	'0'	'0'	'0'		
	'1'	'0'	'1'	'X'	'X'		
	'X'	'0'	'X'	'X'	'X'		
	'Z'	'0'	'X'	'X'	'X'		

		NOI		
~	ʻ0'	'1'	'X'	ʻZ'
	'1'	'0'	'X'	'X'

OR

_	'0'	'1'	'X'	ʻZ'
'0'	'0'	'1'	'X'	'X'
'1'	'1'	'1'	'1'	'1'
'X'	'X'	'1'	'X'	'X'
ʻZ'	'0'	'1'	'X'	'X'

	XOR						
1	٨	'0'	'1'	'X'	ʻZ'		
1	'0'	'0'	'1'	'X'	'X'		
	'1'	'1'	'0'	'X'	'X'		
	'X'	'X'	'X'	'X'	'X'		
	'Z'	'X'	'X'	'X'	'X'		

'0'	'1'	'X'	ʻZ'
'1'	'0'	'X'	'X'

using sc_dt;

sc_logic pulse, trig; sc_bit select = LOG_1;

pulse != select; select = trig;

trig = SC_LOGIC_Z; // This is identical to : trig = sc_logic('Z');

bool wrn;

sc_logic pena (SC_LOGIC_1); // Initialize to '1'

wrn = pena.to_bool();

if (pena.to_bool()) cout << "pena is " << pena << endl;

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Data Types







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Introduction (1/2)



- Fixed Point Type
 - wl (word length)
 - which shall be the total number of bits in the number representation.
 - iwl (integer word length)
 - which shall be the number of bits in the integer part (the position of the binary point relative to the left-most bit).
- Fixed-point representation, 3 cases
 - wl < iwl
 - There are (iwl-wl) zeros between the LSB and the binary point.
 - 0 <= iwl <= wl</p>
 - The binary point is contained within the bit representation.
 - iwl < 0</p>
 - There are (-iwl) sign extended bits between the binary point and the MSB.
 - For an unsigned type, the sign extended bits are zero.
 - For a signed type, the extended bits repeat the MSB.









 $[-2^{(iwl-1)}, 2^{(iwl-1)} - 2^{-(wl-iwl)}]$ Range of value for signed fixed point format Range of value for <u>unsigned</u> fixed point format $[0, 2^{(iwl)} - 2^{-(wl-iwl)}]$

Index	wl	iwl	Fixed-point repre- sentation*	Range signed	Ranged unsigned	
1	5	7	xxxxx00.	[-64,60]	[0,124]	wl < iwl
2	5	5	xxxxx.	[-16,15]	[0,31]	
3	5	3	xxx.xx	[-4,3.75]	[0,7.75]	0 <= iwl <= v
4	5	1	x.xxxx	[-1,0.9375]	[0,1.9375]	
5	5	0	.xxxxx	[-0.5,0.46875]	[0,0.96875]	
6	5	-2	.ssxxxxx	[0.125,0.1171875]	[0,0.2421875]	iwl < 0
7	1	-1	.sx	[-0.25,0]	[0,0.25]	

^{*}x is an arbitrary binary digit, 0, or 1. s is a sign extended digit, 0, or 1,

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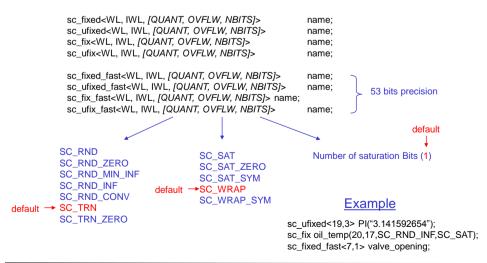






SystemC Fixed-Point Data Types



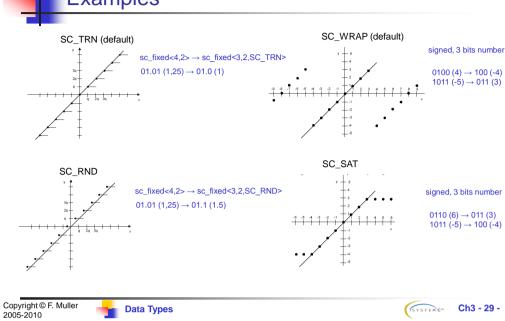


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	Predefined Primitive Channels (Mutexs, FIFOs, Signals)					
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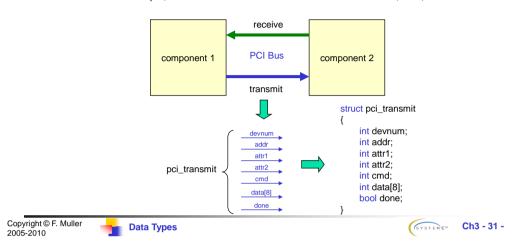
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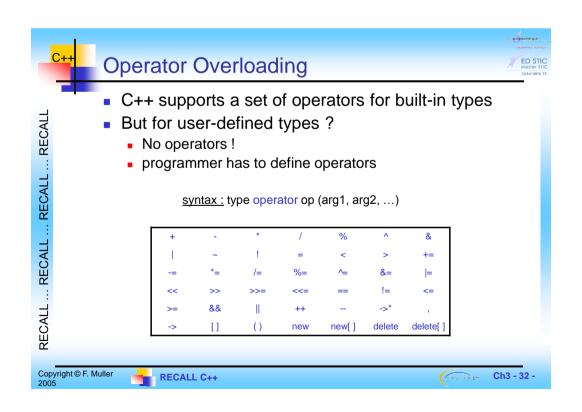


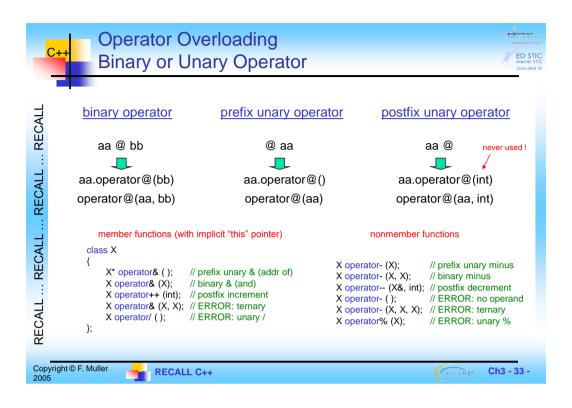


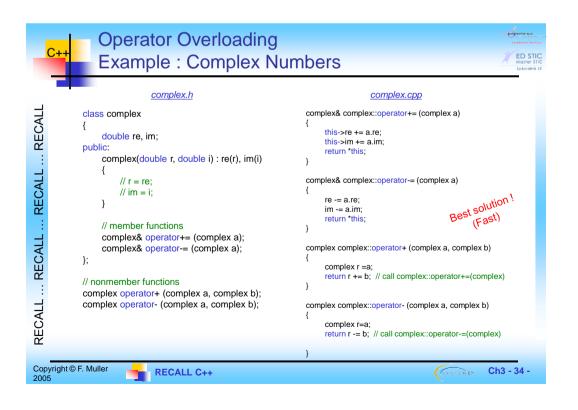


- New Data Types
 - enumeration types
 - record types
- Used by High Level abstraction
 - for example, a bus is considered like a structure included control, data, address

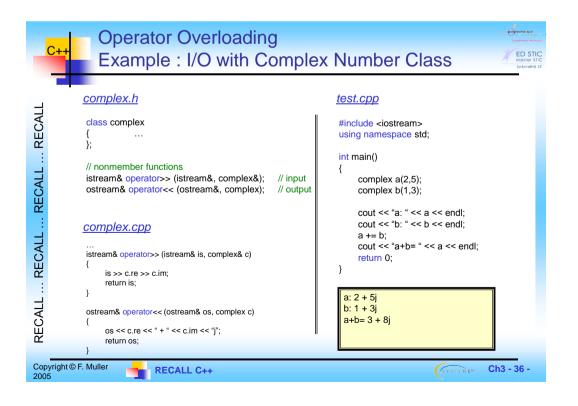




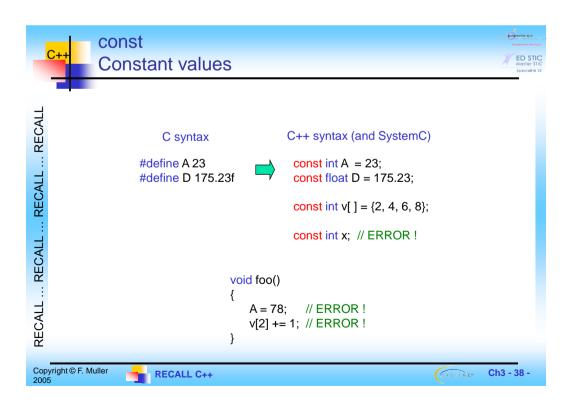




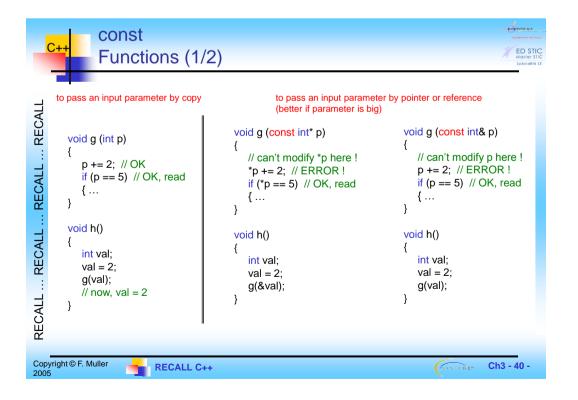
```
Operator Overloading
             Example: Enumerations
                          Declaration
... RECALL
                          enum Day {sun, mon, tue, wed, thu, fri, sat };
                           Day& operator++ (Day& d)
... RECALL
                               return d = (sat == d)? sun : Day(d+1);
                           }
... RECALL
                         Using
                           Day day = mon;
                          // day = monday
RECALL
                           day++;
                          // day = tuesday
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                       RECALL C++
                                                                                         Ch3 - 35 -
```

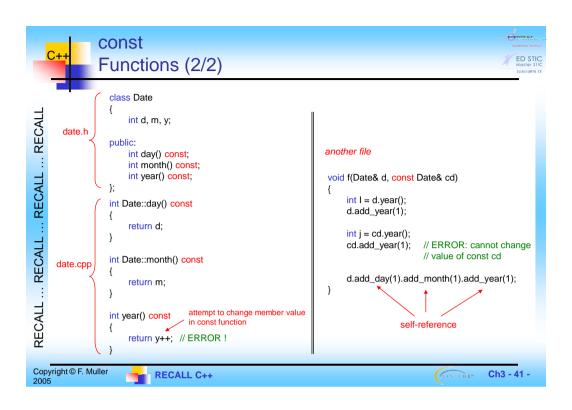


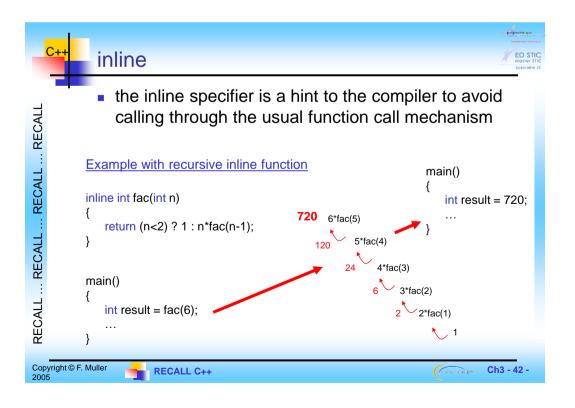
```
Self-Reference
                     Each member function knowns what object it was invoked for and can explicitly
                     refer to it
... RECALL
                     called "this" pointer
                     "this" points to the current class
                                                                     Date& Date::add_year(int n)
                     "this" cannot be changed
                                                                          if (d == 29 && m == 2 && !leapyear(y+n))
         class Date
RECALL
                                                                              d = 1;
                                                                              m = 3;
               int d, m, y;
                                                                          y += n;
return *this:
         public:
               Date& add_year(int n);
                                         // add n years
... RECALL
               Date& add_month(int n); // add n months
               Date& add_day(int n);
                                         // add n days
                                                                    Date& Date::add_year(int n)
                                                                         if (this->d == 29 && this->m == 2 && !leapyear(this->y+n))
         Example
          void f(Date& d, const Date& cd)
                                                                             this->m = 3;
                                                                         this->y += n;
                                                                         return *this;
              d.add_day(1).add_month(1).add_year(1);
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                               RECALL C++
                                                                                                                    Ch3 - 37 -
```



```
const
             Constant pointers & values
RECALL
     int a = 1:
                                                      int a = 1:
     const int c = 2;
                                                                         constant values
                                                      int b = 3:
                                      constant values
                                                                              constant values and pointer
                                                      const int c = 2;
     const int* p1 = &c; // OK
RECALL
                                                      const int d = 8;
     const int* p2 = &a; // OK
                                                      const int* const p1 = &d;
                                                      int* const p2 = &a;
     int* p3 = &c; // ERROR: init of int*
                                                                               constant pointer
                   // with const int *
RECALL
                                                      *p1 = 3; // ERROR
                                                      *p2 = 7; // OK
     *p3 = 7; // try to change the value of c!
                                                      p1 = &c; // ERROR: const pointer!
                                                      p2 = &b; // ERROR: const pointer!
     *p2 = 5; // ERROR: p2 is const int *
     p2 = &c; // OK
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                        RECALL C++
                                                                                            Ch3 - 39 -
```









User Data Type Compulsory Operators



- In SystemC, you must define 3 operators for a new data type
 - assignment, operator =
 - equality, operator ==
 - stream output, operator <<
- one methods to trace waves
 - sc trace()

```
struct X // or class X
   X& operator= (const X&);
   bool operator== (const X&) const;
ostream& operator<< (ostream&, X);
void sc_trace (sc_trace_file *tf, const X& arg, const sc_string& name);
```

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Data Types

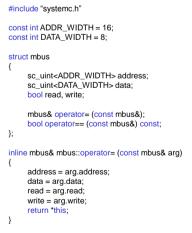


waveform



Example: Micro bus





mbus.h

```
inline bool mbus::operator== (const mbus& arg) const
     return (
           (address == arg.address) &&
           (data == arg.data) &&
           (read == arg.read) &&
           (write == arg.write));
inline ostream& operator<< (ostream& os, const mbus& arg)
      os << "address=" << arg.address <<
          " data=" << arg.data << " read=" << arg.read <<
" write=" << arg.write << endl;
     return os;
inline void sc_trace (sc_trace_file *tf, const mbus& arg, const sc_string& name)
     sc_trace (tf, arg.address, name+".address");
     sc trace (tf, arg.data, name+".data");
     sc_trace (tf, arg.read, name+".read");
      sc_trace (tf, arg.write, name+".write");
```

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	Predefined Primitive Channels (Mutexs, FIFOs, Signals)				
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Standard Template Library (STL)



- generic containers
 - vector<T> (a variable-sized vector)
 - map<key,val> (an associated array)
 - list<T> (a doubly-linked list)
 - deque<T> (a double-ended queue)
- manipulation methods
 - for_each()
 - count()
 - min_element()
 - max_element()
 - search()
 - transform()
 - reverse()
 - sort()







Standard Template Library (STL) Example of vector class



```
#include <vector>
int main(int argc, char* argv[])
   std::vector<int> mem(1024);
   for (unsigned i=0; i != 1024; i++)
       mem.at(i) = -1; // initialize memory to known values
    mem.resize(2048); // increase size of memory
}
```

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Operators for SystemC Data Types



 SystemC data types support all the common operations with operator overloading

	<u>Operator</u>	Special Methods	
Comparison	== != > >= < <=	Bit Selection	bit(idx), [idx]
Arithmetic	++ * / % + -	Range Selection	range(high,low), (high, low)
Bitwise	~ & ^	Conversion	to_double(), to_int(), to_int64(), to_long(), to_uint(), to_uint64(), to_ulong(), to_string(type)
Assignment	= &= = ^= *= /= %= += -= <<=>>=		
		Testing	is_zero(), is_neg(), length()
		Bit Reduction	and_reduce(), nand_reduce(), or_reduce(), nor_reduce(), xor_reduce(), xnor_reduce()

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Operators for SystemC Data Types Mixed Types



- For compiling mixed types, you must
 - have one of the arguments be same type as result
 - perform an explicit conversion of one of at least one of the operand arguments

result = arg1 op arg2 op arg3 ...;

Example

```
sc_int<3> d(3);
sc_int<5> e(15);
sc_int<7> f(14);
sc_int<7> sum = d + e + f; // Works
sc_int<64> g("0x700000000000000");
sc_int<64> h("0x700000000000000");
sc_int<64> i("0x7000000000000000");
sc_bigint<70> bigsum = g + h + i; // Doesn't works !
bigsum = sc\_bigint < 70 > (g) + h + i; // Works
```









- For one bit
 - bool var
- For vectors and unsigned arithmetic
 - sc_uint<n> var
- For signed arithmetic
 - sc_int<n> var
- If vector size is more than 64 bits
 - sc_bigint var
 - sc_biguint var
- For loop indices, etc.
 - int var
 - other C++ integer type
- Use sc_logic and sc_lv<n> types for only those signals that are carry the four logic values

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